

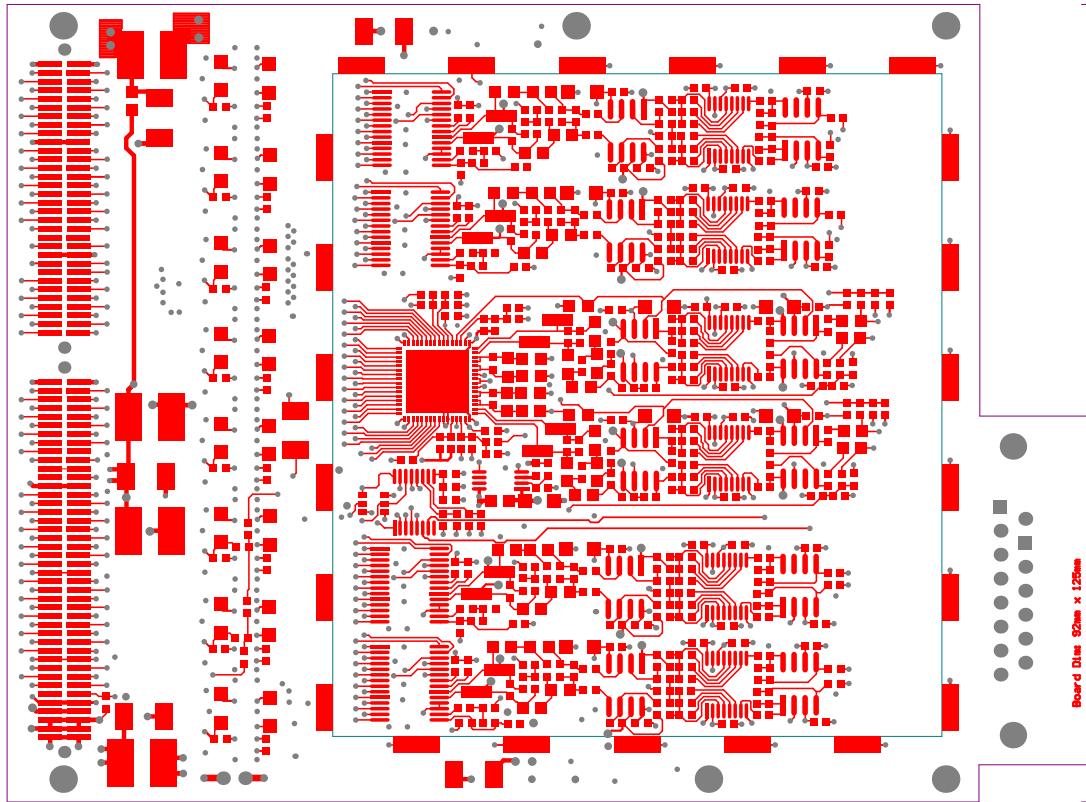
McGill: DfMux Mezzanine Board Rev4

Nominal Size: 4.920 x 3.625 inches

FINISHED BOARD: 0.062 (Nom) 370HR

ARRANGE LAYERS SEQUENTIALLY: L1-L6

Thieving Copper Dots CAN be added to INNER layers if needed



L1 (TOP) .GTL

BOARD OUTLINE

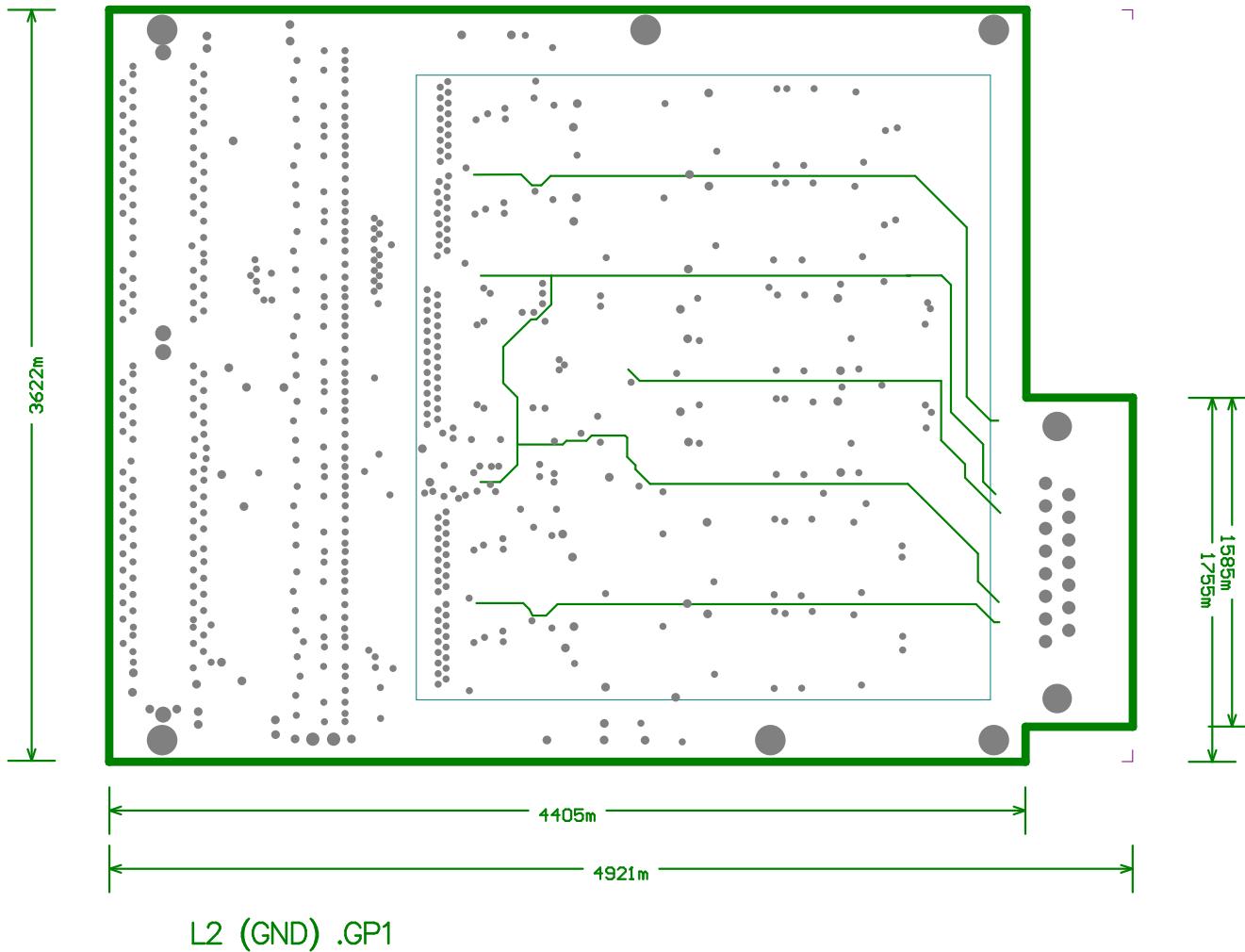
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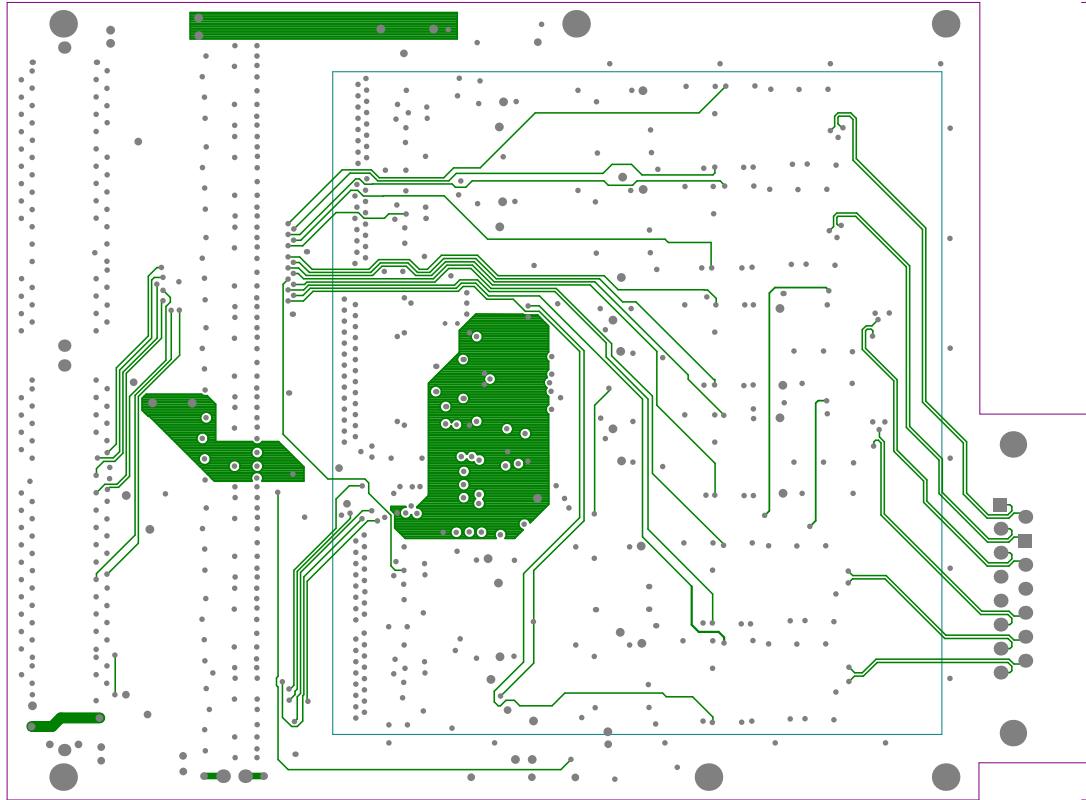
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uCCW
Analog



L3 (ML1).G1

BOARD OUTLINE

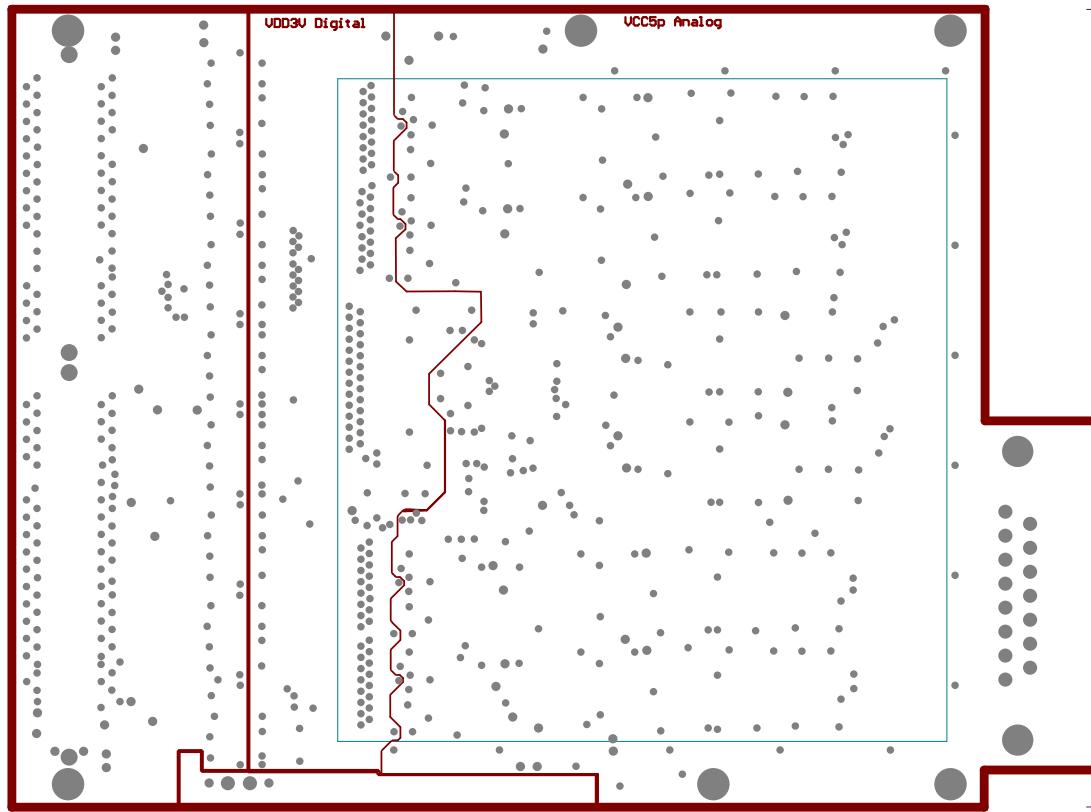
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L4 (PWR) .GP2

BOARD OUTLINE

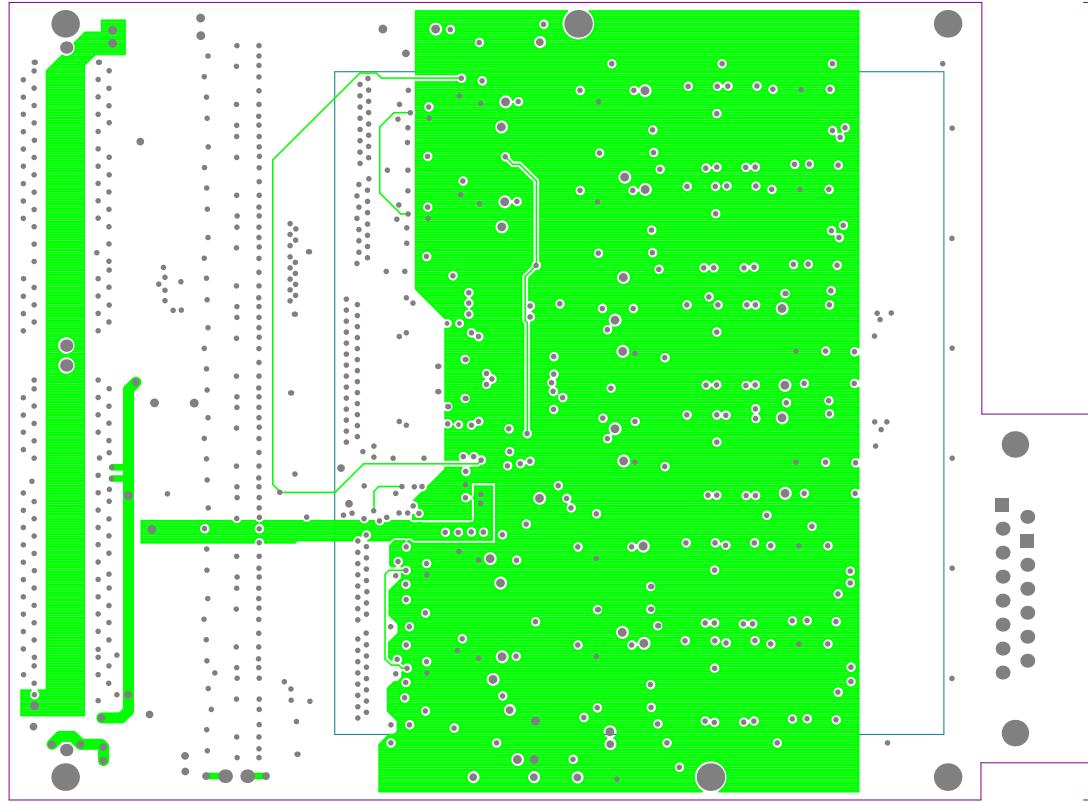
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L5 (ML2) .G2

BOARD OUTLINE

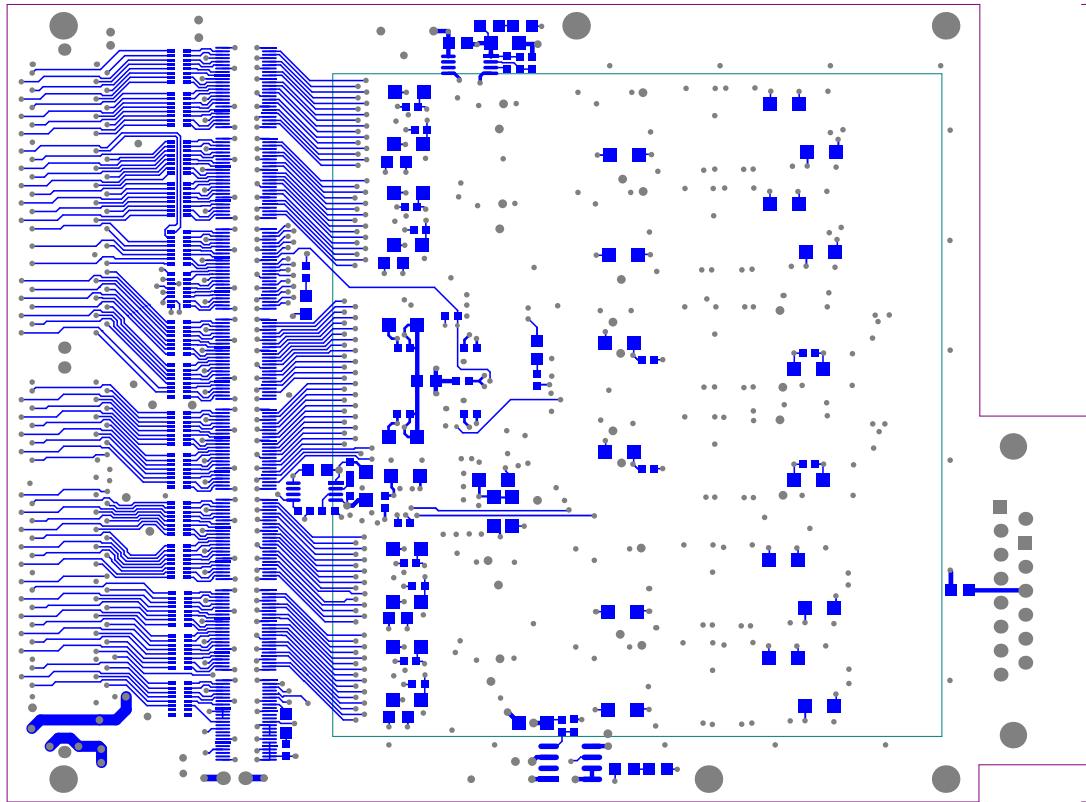
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Le (BOT).GBR
BOARD OUTLINE

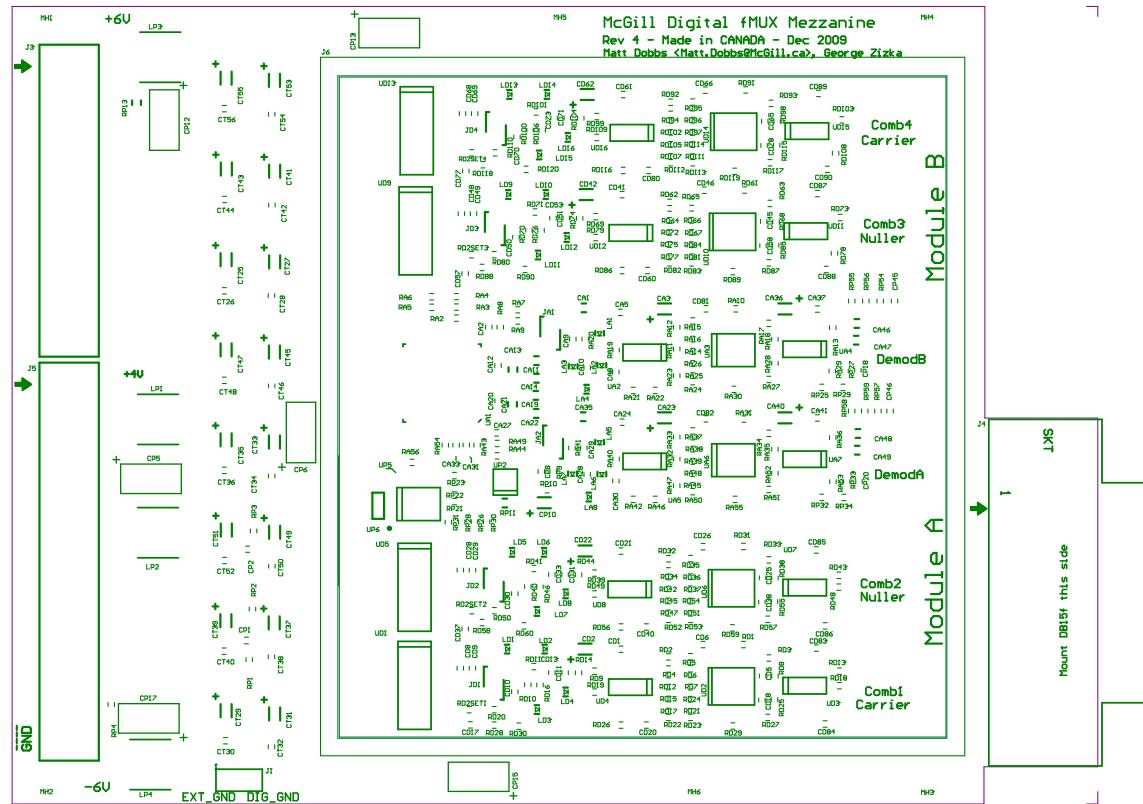
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TOP IDENT (.GTO)

BOARD OUTLINE

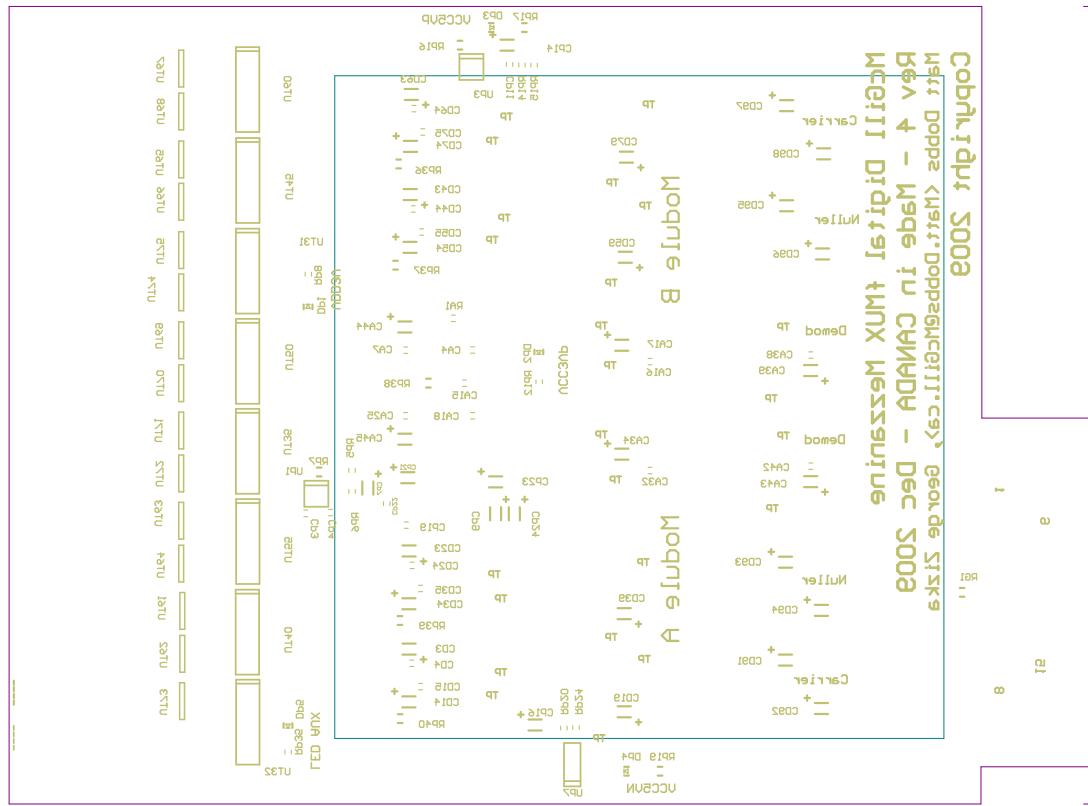
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BOTTOM IDENT (.CBO)

BOARD OUTLINE

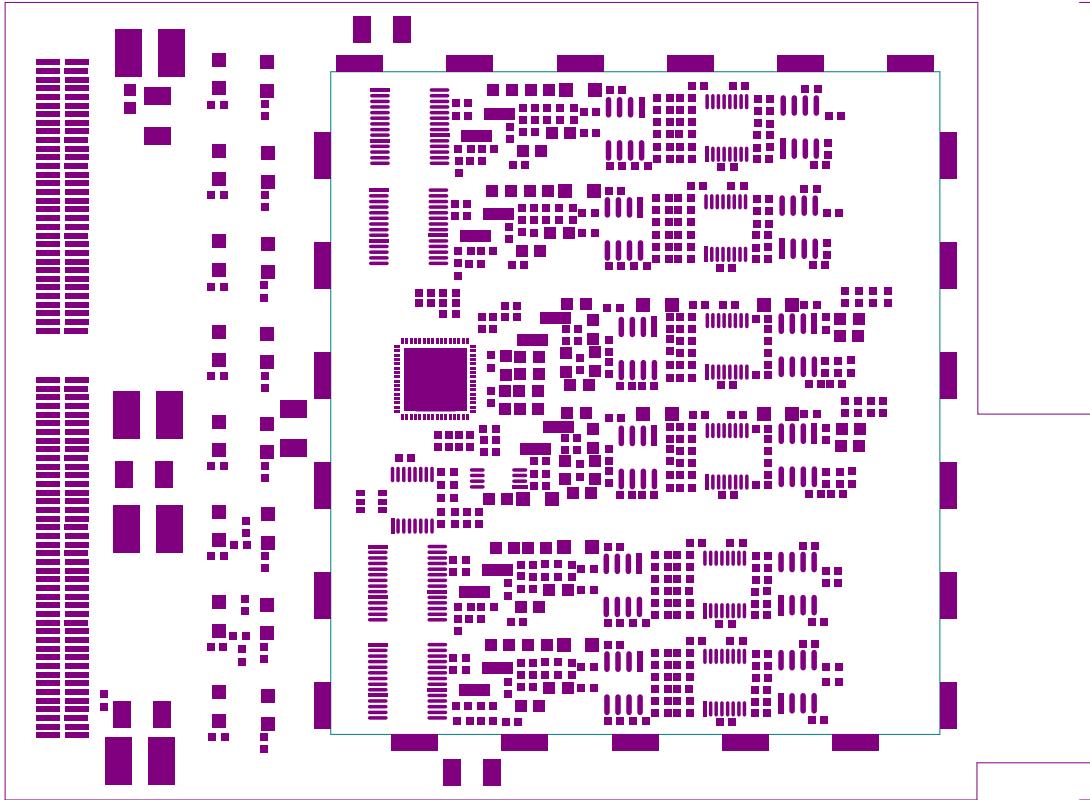
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TOP SOLDER PASTE (.GTP)

BOARD OUTLINE

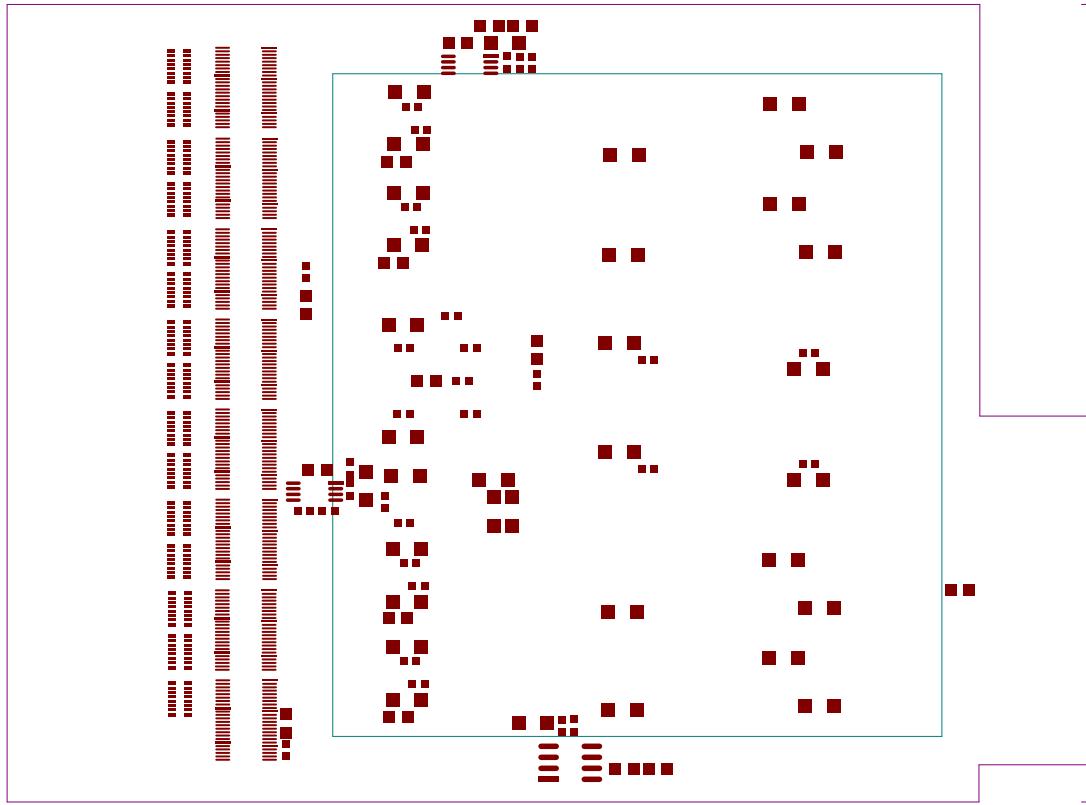
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BOARD OUTLINE

BOTTOM SOLDER PASTE (GBP)

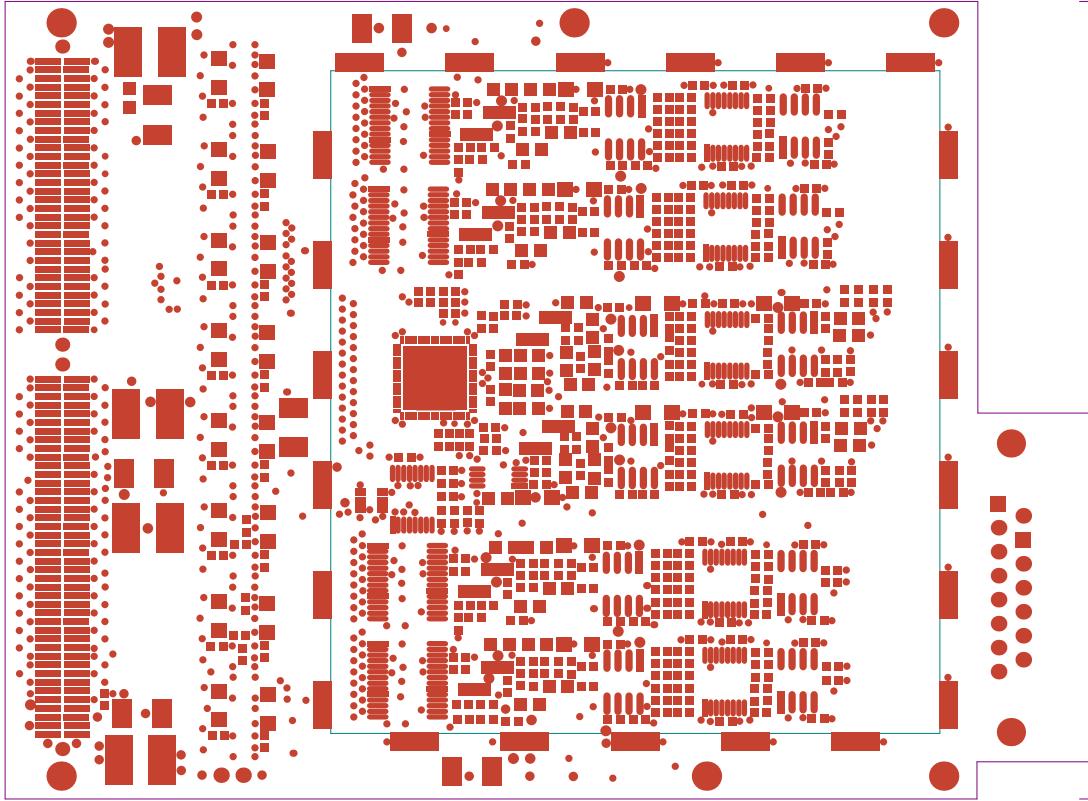
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TOP SOLDER MASK (.GTS)

BOARD OUTLINE

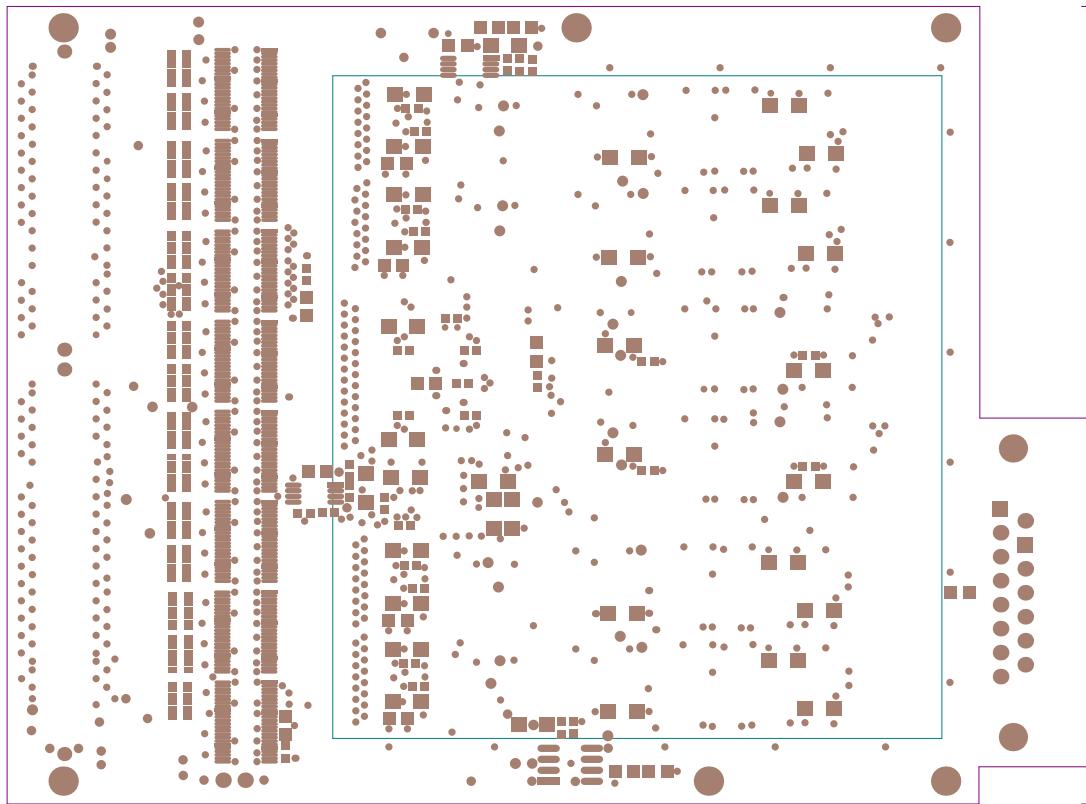
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BOTTOM SOLDER MASK (GDS)

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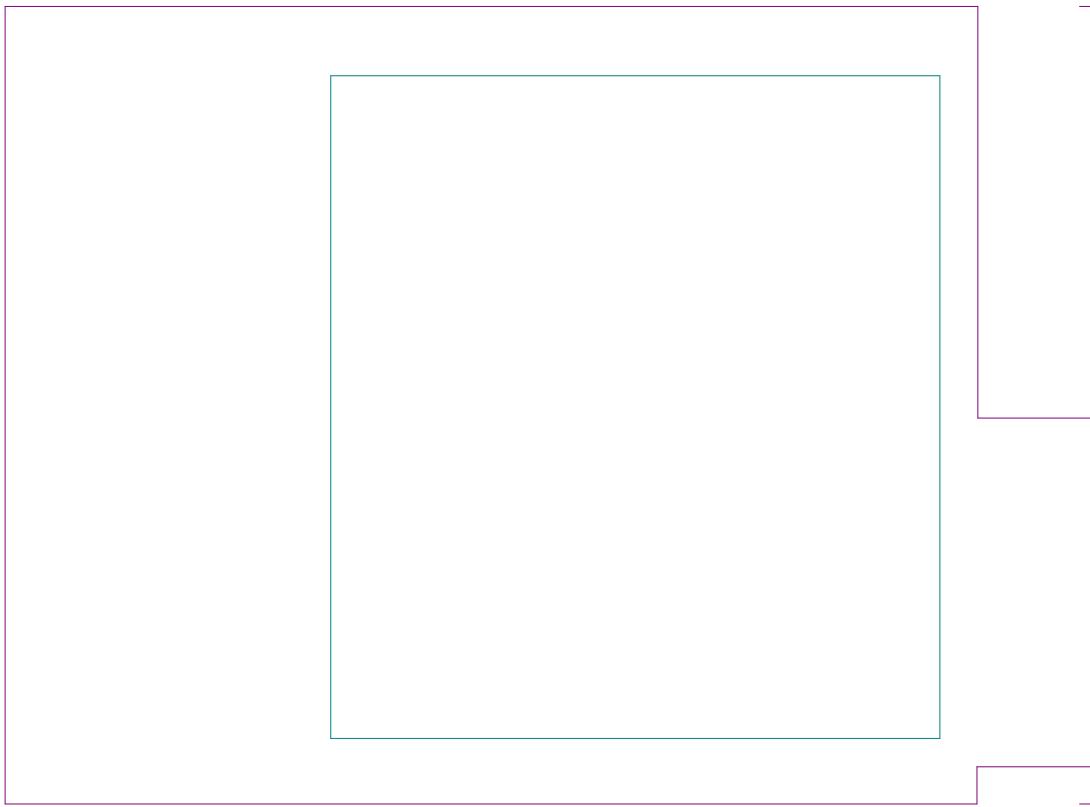
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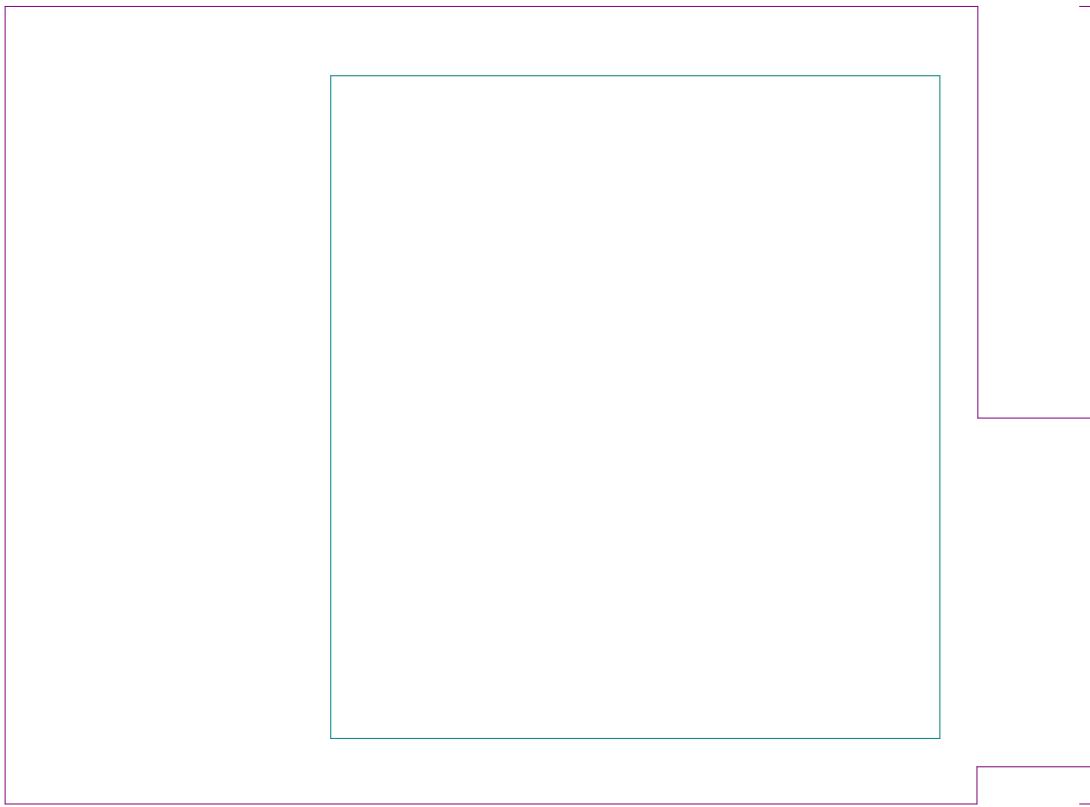
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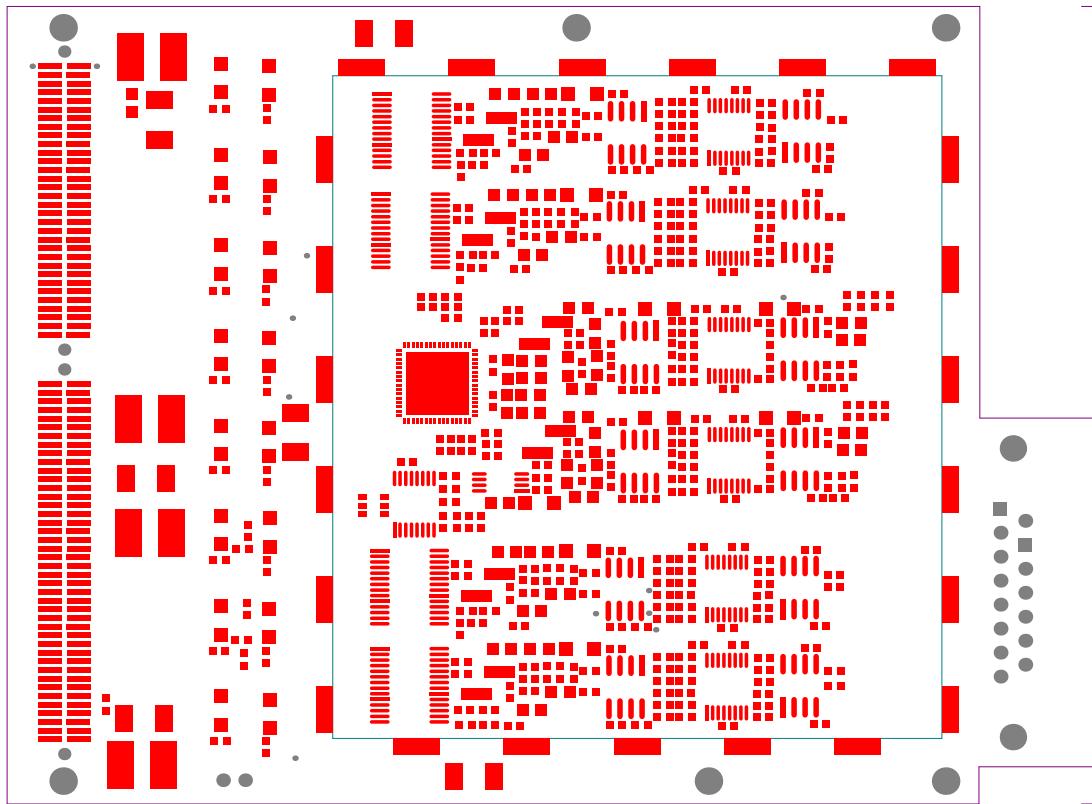
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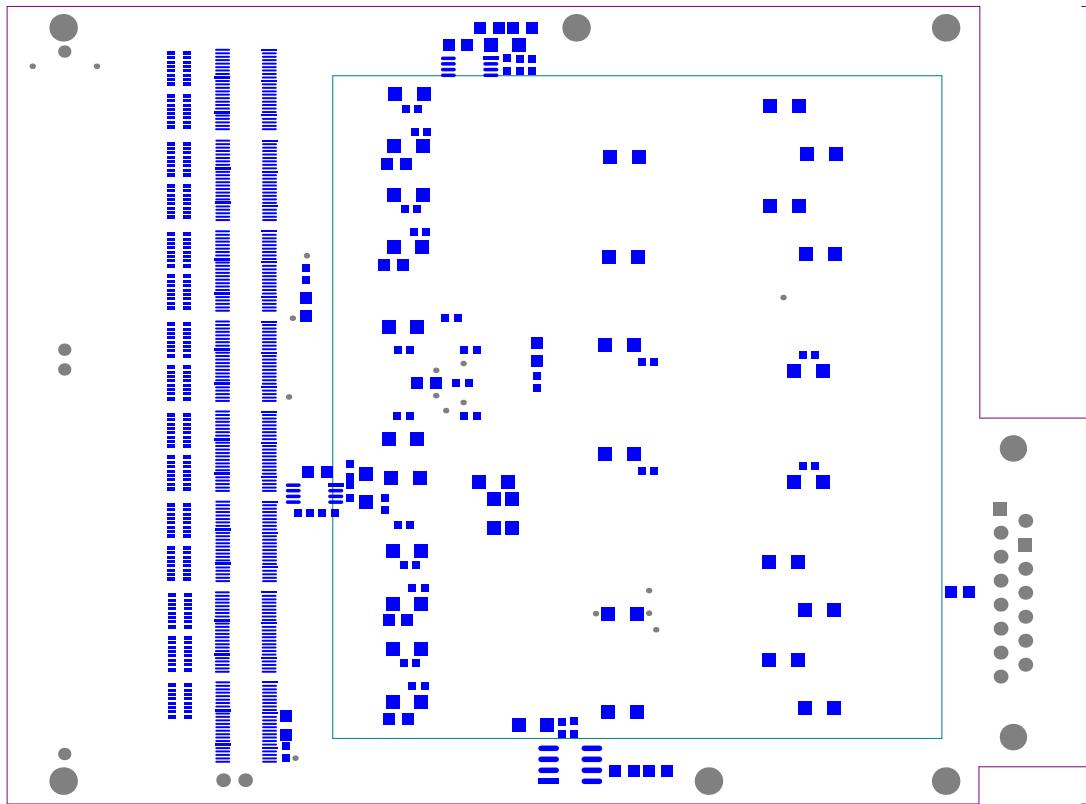
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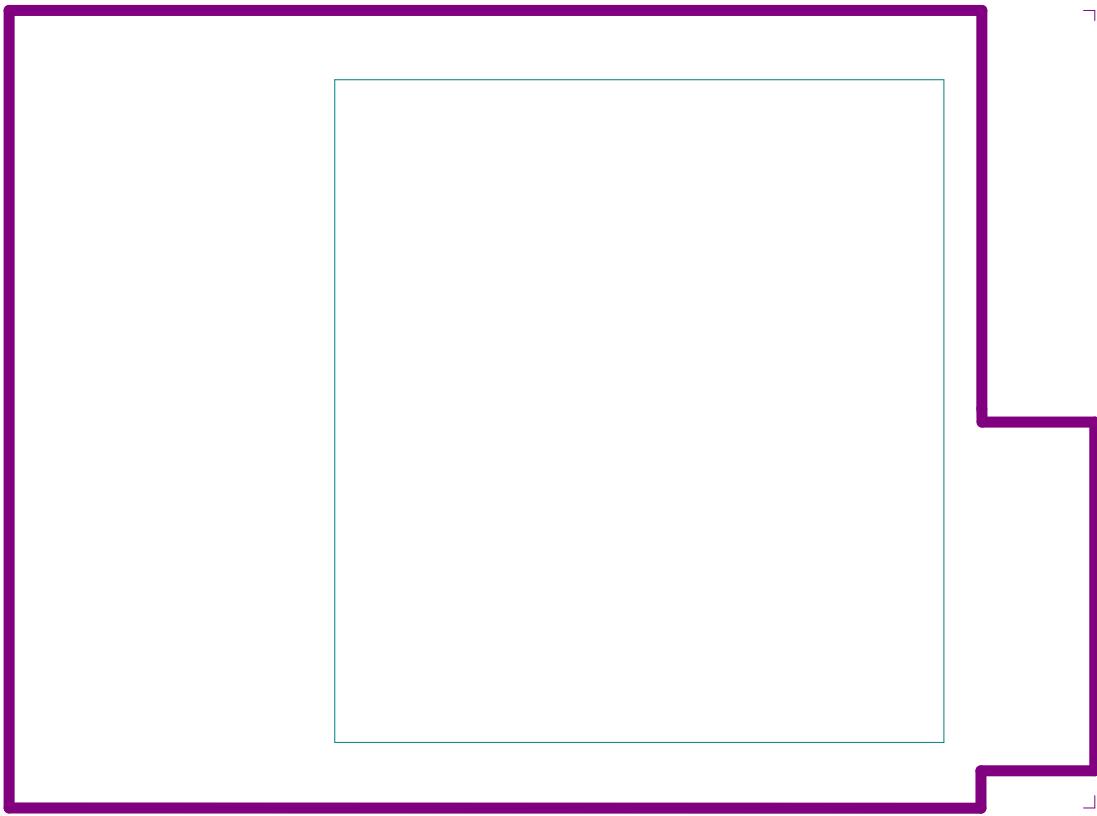
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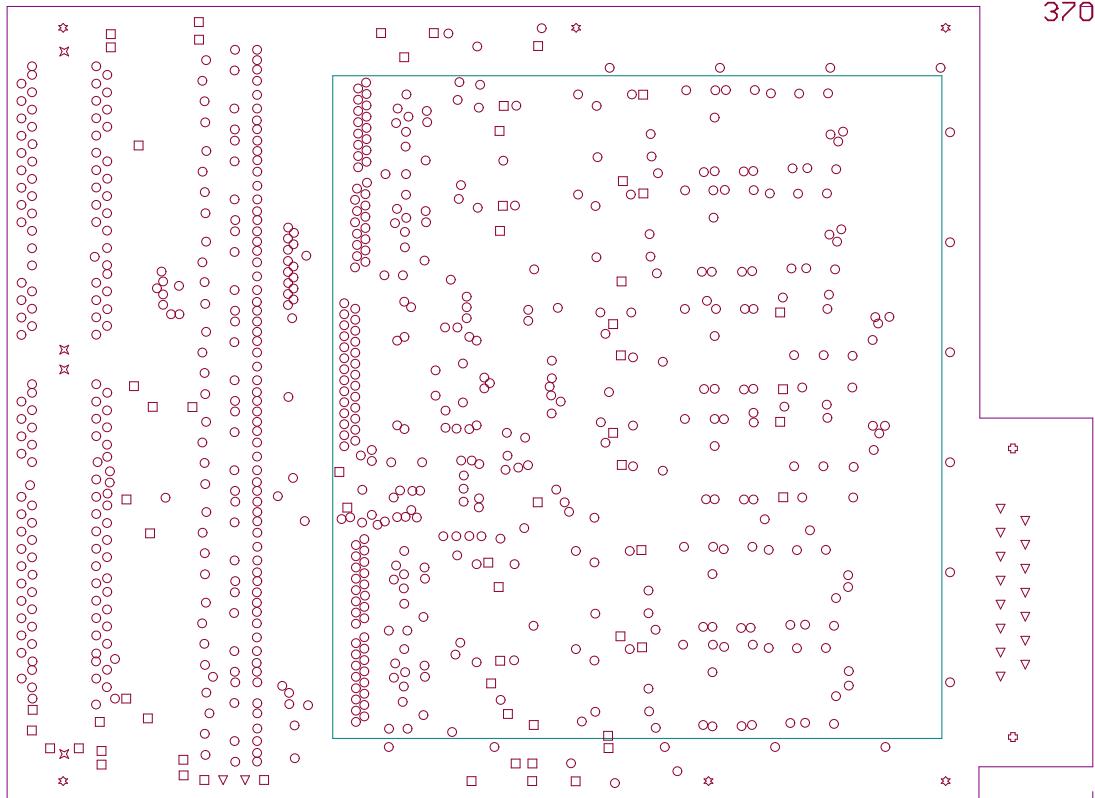
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370HR : 6 Layer PCB, Approximate Build:

- L1 ————— TopLayer : 0.5oz Plus Plating
4.0 mils spacing
- L2 ————— Plane 1: 1oz
5.0 mils spacing
- L3 ————— MidLayer1: 1/2oz
Excess Material
- L4 ————— MidLayer4: 1/2oz
5.0 mils spacing
- L5 ————— Plane 2 : 1oz
4.0 mils spacing
- L6 ————— Bottom Layer : 0.5oz Plus Plating
Total thickness = .093

ALL ROUTED LAYERS:
7 mils = 50 OHMS

FINISHED HOLE SIZES			
Symbol	Hit Count	Tool Size	Plating
○	711	12mil (0.305mm)	PT
□	62	20mil (0.508mm)	PT
▽	17	42mil (1.067mm)	PT
✖	4	55mil (1.397mm)	NF
✚	2	120mil (3.048mm)	PT
❖	6	125mil (3.175mm)	NF
802 Total			

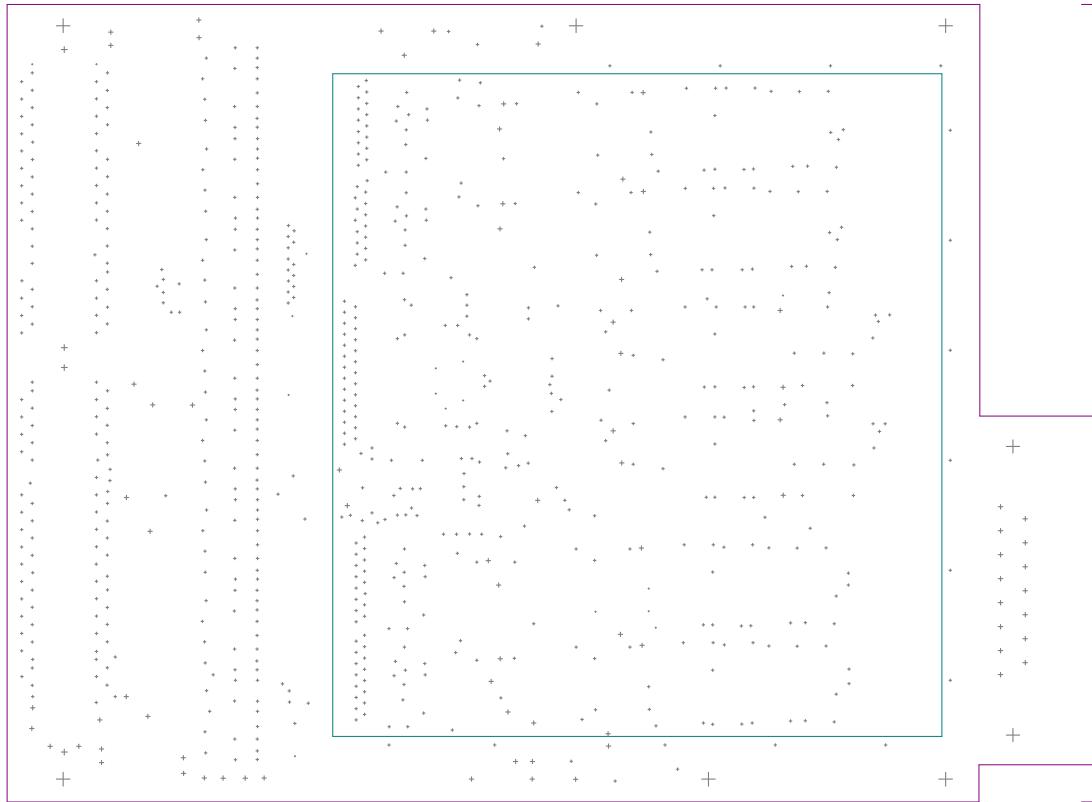
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