

Mezzanine Module I Mezzanine Module II LEDs: LEDs: Power Good (G) ETHERNET RX (G) ETHERNET TX (G) FPGA INIT (R) LIT TO SLAVE (Y) PROG DONE (G) SQUID CTRL ETHERNET SERIAL COMM **(** McGill Digital fMUX FPGA Motherboard Rev. 3 - Made in CANADA - September 2007, Montreal M.Dobbs, McGill University < matt.dobbs@mcgill.ca > N.Starinski, University of Montreal < startnsky@lps.umontreal.ca > C48 **(**