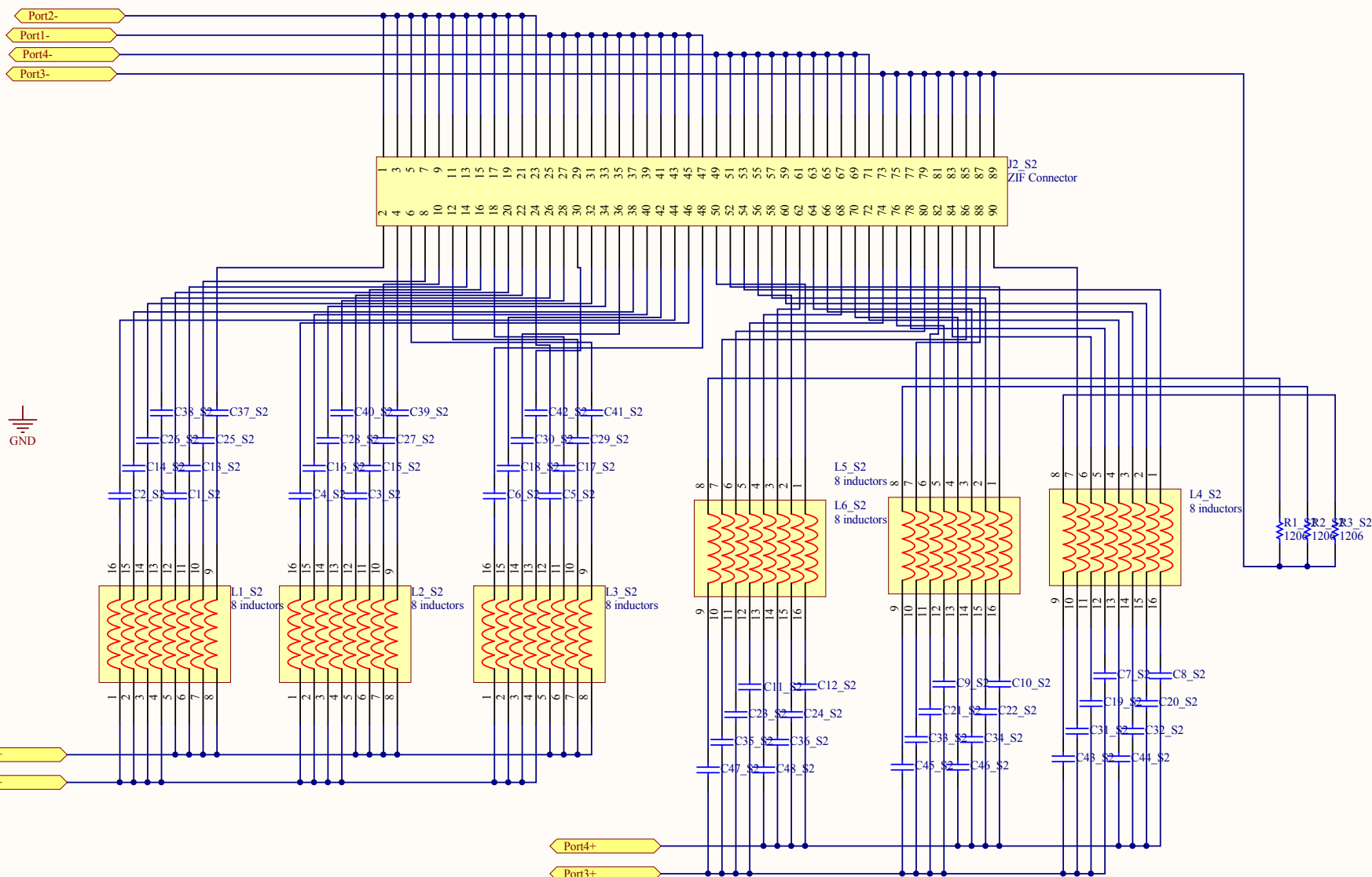


This will be the schematic for the majority of either side of the board. We will need to add ports to this page so signals can go in and out of this schematic page to the top level. On the top level we will have two copies of this page and the one MicroD connector. Given that the MicroD has 21 pins, at most 10 single signal ports are needed on this page

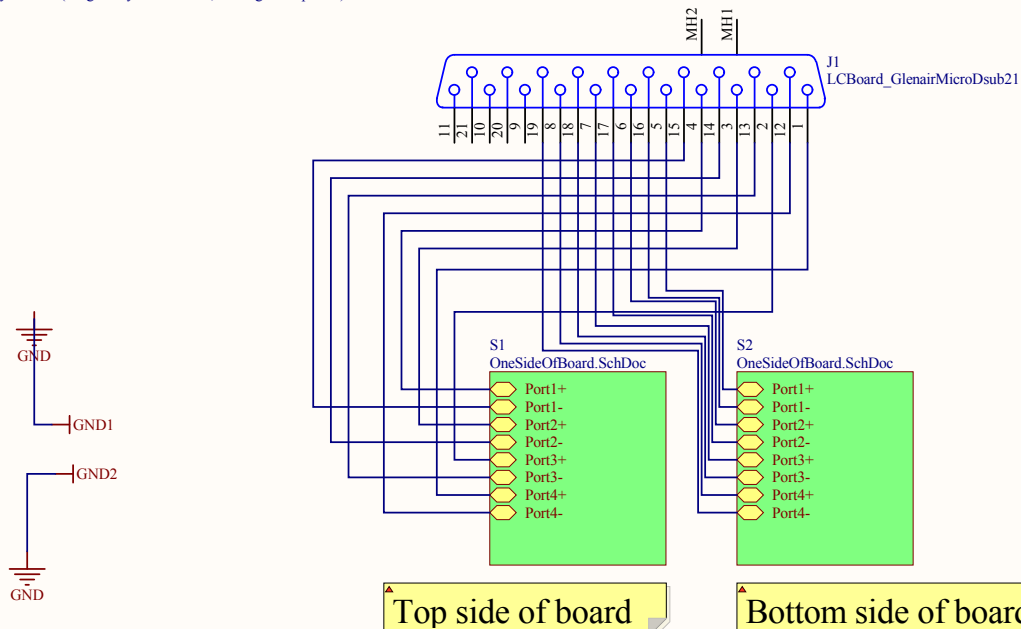
Title		
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Date:	27/06/2011	Sheet of
File:	C:\Users\...\OneSideOfBoard.SchDoc	Drawn By:



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File:	C:\Users\...\OneSideOfBoard.SchDoc	Drawn By:

6 layer board (2 signal layers each side, and 2 ground planes)



Partitioning the design like this into two sides, allows us to use Altiums rooms to save us some time. Only do the PCB layout for one of rooms, we can then copy the room formating and apply it to the other room. You then have two completely identical rooms. Its possible to then flip the other room onto the opposite side of the board.

Title		
Size Letter	Number	Revision
Date:	27/06/2011	Sheet of
File:	C:\Users\...LCboardRev2.SchDoc	Drawn By:

