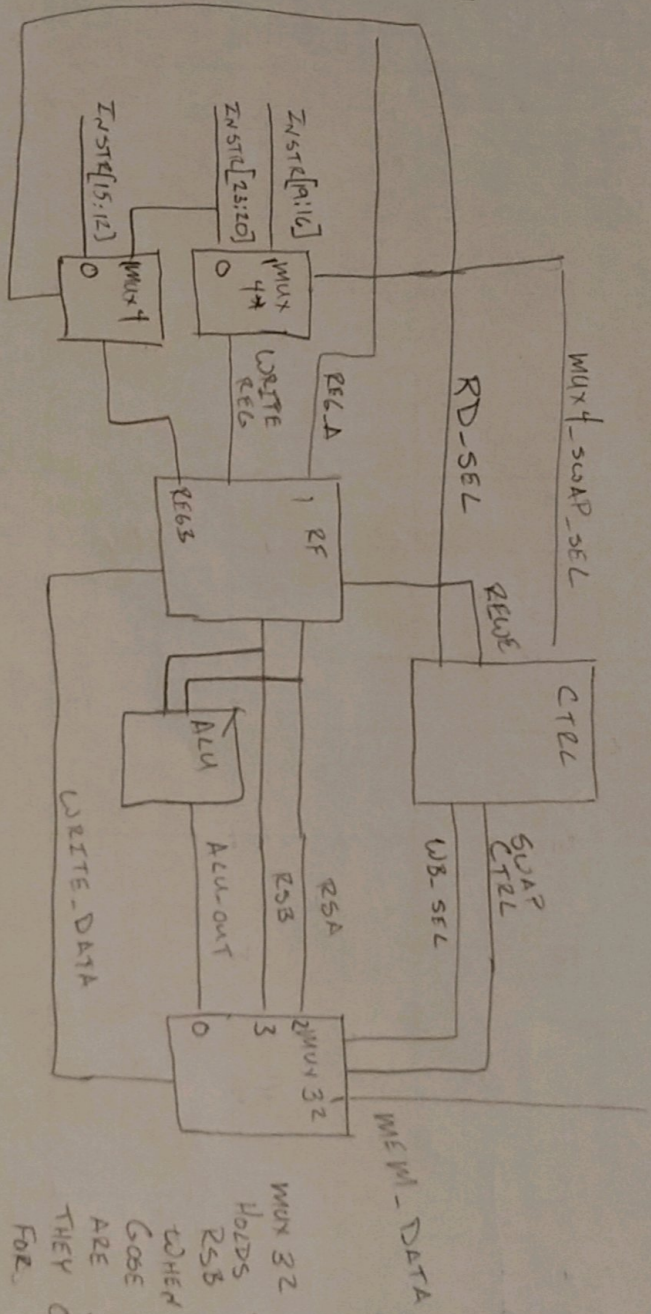


DA MUX 4*
 CONTROLS WHICH
 REGISTER
 IS WRITTEN
 TO AFTER
 MEM IT IS
 CHARGED TO
 1 TO WRITE
 TO THE OTHER
 REG



MEM 32
 HOLDS RSA &
 RSB AS VALUES
 WHEN SWAP-CTEL
 GOES TO 1 THEY
 ARE STORED THEN
 THEY CAN BE KEPT
 FOR OUT PUT LATER