AVR32 Power Manager

1. Run 12MHz, with OSC0, standard settings

```c
#include <avr32/io.h>
#include "board.h"
#include "pm.h"

pm_switch_to_osc0(&AVR32_PM, FOSC0, OSC0_STARTUP);
```

2. Run 33MHz, expanded from above

```c
pm_switch_to_osc0(&AVR32_PM, FOSC0, OSC0_STARTUP);

pm_pll_setup(&AVR32_PM, 0,  // pll.
                       10,  // mul.
                       1,   // div.
                       0,   // osc.
                       16); // lockcount.
pm_pll_set_option(&AVR32_PM, 0, // pll.
                       1,   // pll_freq.
                       1,   // pll_div2.
                       0); // pll_wbwdisable.
pm_pll_enable(&AVR32_PM, 0);
pm_wait_for_pll0_locked(&AVR32_PM);
pm_cksel(&AVR32_PM,
                       1,   // pbadiv.
                       0,   // pbasel.
                       1,   // pbbdiv.
                       0,   // pbbsel.
                       1,   // hsbdiv.
                       0);  // hsbsel.
pm_switch_to_clock(&AVR32_PM, AVR32_PM_MCCTRL_MCSEL_PLL0);
```