

Consider ways to modify your input such that
each iteration performs all the operations possible

	M	A	Q	β	Count	Math/ALU
Initialization	0 1 1 0 0 1 0 0	0 0 0 0 0 0 0 0	1 0 1 1 0 0 1 1	0	8	
Subtraction	0 1 1 0 0 1 0 0	1 0 0 1 1 1 0 0	1 0 1 1 0 0 1 1	0	8	
Shift	0 1 1 0 0 1 0 0	1 1 0 0 1 1 1 0	0 1 0 1 1 0 0 1	1	7	
Shift	0 1 1 0 0 1 0 0	1 1 1 0				
Addition	0 1 1 0 0 1 0 0	0 1 0 0				
Shift	0 1 1 0 0 1 0 0	0 0 1 0				
Shift	0 1 1 0 0 1 0 0	0 0 0 1 0 0 1 0	1 1 0 0 1 0 1 1	0	4	
Subtraction	0 1 1 0 0 1 0 0	1 0 1 0 1 1 1 0	1 1 0 0 1 0 1 1	0	4	
Shift	0 1 1 0 0 1 0 0	1 1 0 1 0 1 1 1	0 1 1 0 0 1 0 1	1	3	
Shift	0 1 1 0 0 1 0 0	1 1 1 0 1 0 1 1	1 0 1 1 0 0 1 0	1	2	
Addition	0 1 1 0 0 1 0 0	0 1 0 0 1 1 1 1	1 0 1 1 0 0 1 0	1	2	
Shift	0 1 1 0 0 1 0 0	0 0 1 0 0 1 1 1	1 1 0 1 1 0 0 1	0	1	
Subtraction	0 1 1 0 0 1 0 0	1 1 0 0 0 0 1 1	1 1 0 1 1 0 0 1	0	1	
Shift	0 1 1 0 0 1 0 0	1 1 1 0 0 0 0 1	1 1 1 0 1 1 0 0	1	0	

Pseudo Code Info

Booth's Multiplication Algorithm

```

01 boothMultiply(multiplicand,
multiplier){
02   Register M=multiplicand
03   Register A=0
04   Register Q=multiplier
05   Bit  $\beta$ =0
06   Integer count=REGISTER_SIZE
07
08   while (count > 0) {
09
10     switch ([leastSignificantBit
(Q), $\beta$ ]) {
11       case [1,0]: A=A-M
12         break
13
14       case[0,1]: A=A+M
15         break
16     }
17
18     //Shift A, Q, and  $\beta$  1 bit
19     signPreservingRightShift(1,
A, Q,  $\beta$ )
20
21     count--
22   }
23 }

```