

Crossbar Switch

Directory Controller_0

Memory
Port

...

dmaReq

Req To
Dir

Resp
From Dir

DRAMCache_0: HMC

Slave Port

Master Port

Memory_0: DDR3

Slave Port

Directory Controller_1

Memory
Port

...

dmaReq

Req To
Dir

Resp
From Dir

DRAMCache_1: HMC

Slave Port

Master Port

Memory_1: DDR3

Slave Port