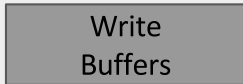
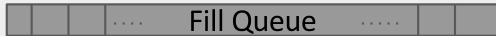
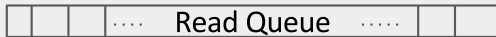


# DRAMCache Controller



Control Logic  
& Scheduler

## BUSES

Addr & cmd

Data

Stacked DRAM  
device