

# Crossbar Switch

## Directory Controller\_0

Memory Port

...

dmaReq

Req To Dir

Resp From Dir

### DRAMCache\_Ctrl:0

Slave Port

Bypass

Master Port

Bus

HMC Device

### Memory\_Ctrl:0

Slave Port

Bus

DDR3 Device

## Directory Controller\_1

Memory Port

...

dmaReq

Req To Dir

Resp From Dir

### DRAMCache\_Ctrl:0

Slave Port

Bypass

Master Port

Bus

HMC Device

### Memory\_Ctrl:0

Slave Port

Bus

DDR3 Device