Memory

Address Instruction %eip 0x4026cc ret 0x133700 0x565877 0x4028cc mul %rsp 0x4028cc

Before executing ret

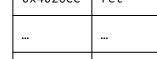
Execute ret After executing ret

Memory		
Address	Instruction	
0x4026cc	ret	

•		
Address	Instruction	
0x4026cc	ret	

Address	Inst
0x4026cc	ret

0x4026cc	ret



0x4028cc

%eip

mul



%rsp

0x133700



0x565877

Stack

