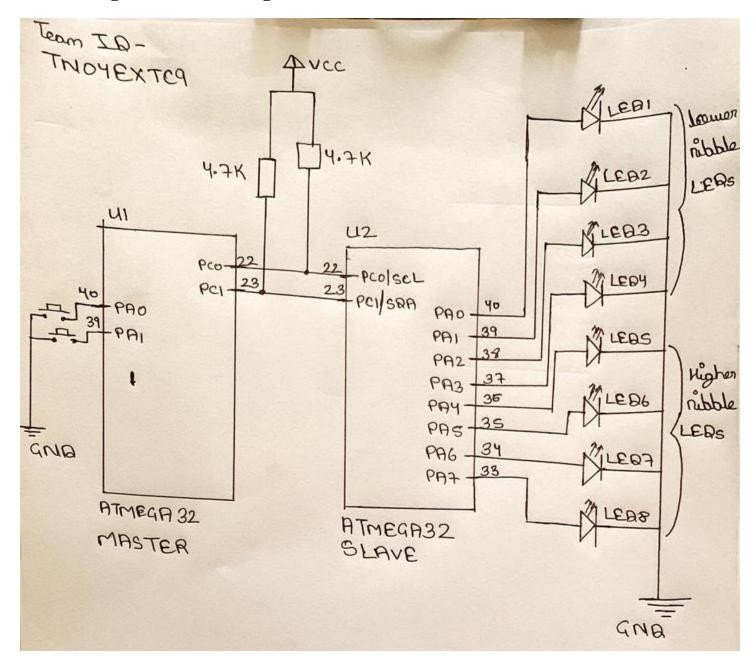
AVR I2C

Team Id: TN04ExTC9

Circuit Digarm for for Program:



- ➤ In Master Atmega32, there are Two Switchs are connected at PA0 and PA1. And In Slave Atmega32, there are 8 LED's are connected at PA0 to PA7.
- ➤ The lower nibble LEDs are connected from PA0 to PA3, and The Higher nibble LEDs are connected from PA4 to PA7.
- ➤ When switch PA0 is pressed then Data (0x0F) will be written in TWDR according to The Master Atmega32 program given below. The Lower nibble LEDs ON for 5 second at recevier side or Slvae Atmega32.
- ➤ When switch PA0 is pressed then Data (0xF0) will be written in TWDR according to The Slave Atmega32 program given below. The Higher nibble LEDs ON for 5 second at recevier side or Slvae Atmega32.
- TWI only uses 2 bidirectional wires to establish communication among multiple devices. It can also adapt to the needs of various slave devices.

Program for Master Atmega32:

```
Advanced Mode  Quick Launch (Ctrl+Q)
AVR I2C BUS TX - Microchip Studio
File Edit View VAssistX ASF Project Build Debug Tools Window Help
                                                                                                                                                     O → O | 👸 → 🕮 🖆 → 當 ≌ 🛂 | 🛣 🗇 🗇 | 🤊 → C → | 🔠 🔍 | ▶ 🗦 Debug
                                                                                                         ▼ Debug Browser ▼
 → C:\Users\heman\Documents\Atmel Studio\7.0\AVR I2C BUS TX\AVR I2C BUS TX\main.cster ATmega32
                                                                                                                                                                                       - ₹Go
                                                                                                                                                                                            ‡
                                                                                                                                                                                                Solution Explore
      #define F_CPU 20000000
#include <avr/io.h>
                                                                         // 20 MHz Clock Speed
      #include <util/delay.h>
      #define Input_Switch DDRA
#define hight_Input PORTA
    □void TWI_Init()
            TWSR=(0<<TWPS1)|(0<<TWPS0);
                                                                         // Setting prescalar bits
// SCL freq= F_CPU/(16+2(TWBR)4^TWPS)
    pvoid TWI_Start()
           TWCR = (1<<TWINT)|(1<<TWEN)|(1<<TWSTA);
while((TWCR & (1<<TWINT)) == 0);</pre>
                                                                        // Clear TWI interrupt flag, Put start condition on SDA, Enable TWI // Wait till start condition is transmitted
            while((TWSR & (0xF8)) != 0x08);
                                                                         // Check for the acknowledgement
    □void TWI_Write_Addr(unsigned char Addr)
                                                                         // Address and write instruction
// Clear TWI interrupt flag,Enable TWI
// Wait till complete TWDR byte transmitted
            TWCR = (1<<TWINT)|(1<<TWEN);
           while((TWSR & (1<<TWINT)) == 0);
while((TWSR & (0xF8)) != 0x18);
                                                                         // Check for the acknoledgement
    pvoid TWI_Write_Data(unsigned char Data)
                                                                         // put data in TWDR
// Clear TWI interrupt flag,Enable TWI
            TWCR = (1 << TWINT) | (1 << TWEN);
           while((TWCR & (1<<TWINT)) == 0);
while((TWSR & (0xF8)) != 0x28);
                                                                         // Wait till complete TWDR byte transmitted
                                                                         // Check for the acknoledgement
    pvoid TWI_Stop()
            TWCR = (1<<TWINT)|(1<<TWEN)|(1<<TWSTO);
while((TWCR & (1<<TWSTO)) == 0);
                                                                        // Clear TWI interrupt flag, Put stop condition on SDA, Enable TWI
// Wait till stop condition is transmitted
            TWI_Init();
             hile(1)
                 TWI_Start();
                 TWI Write Addr(0x20):
                 if((PINA & (1<<PA0)) == 0)
                                                                         // for a 1st switch pressed
                      TWI_Write_Data(0x0F);
                                                                        // Lower nibble LED's on at receiver side for 5 Second
                        delav ms(5000):
                       TWI_Write_Data(0x00);
                                                                         // Lower nibble LED's off at receiver side after 5 Second
                 if((PINA & (1<<PA1)) == 0)
                                                                        // for a 2nd switch pressed
                       TWI_Write_Data(0xF0);
                                                                         // Higher nibble LED's on at receiver side for 5 Second
                       _delay_ms(5000);
TWI_Write_Data(0x00);
                                                                        // Higher nibble LED's off at receiver side after 5 Second
                 TWI_Stop();
                                                                          - | ≗ | ≦ | ≝ | ॐ
           "C:\Program Files\Microchip\xc8\v2.31\bin\avr-objcopy.exe" -j .eeprom --set-section-flags=.eeprom=alloc,load --change-section-lma .eeprom
"C:\Program Files\Microchip\xc8\v2.31\bin\avr-objdump.exe" -h -5 "AVR I2C BUS TX.elf" > "AVR I2C BUS TX.lss"
executing task "RunCompilerTask".
      Done executing task "RunCompil
Task "RunOutputFileVerifyTask"
Task "RunOutputFileVerifyTask"

Program Memory Usage : 300 bytes 0.9 % Full

Data Memory Usage : 0 bytes 0.0 % Full

Data Memory Usage : 0 bytes 0.0 % Full

Done executing task "RunOutputFileVerifyTask".

Done building target "CoreBuild" in project "AVR IZC BUS TX.cproj".

Target "PostBuildVerent" skipped, due to false condition; ('$(PostBuildEvent)' != '') was evaluated as ('' != '').

Target "Build" in file "C:\Program Files (x86)\Atmel\Studio\7.0\Vs\Avr.common.targets" from project "C:\Users\heman\Documents\Atmel Studio\7.0\A\
Done building target "Build" in project "AVR IZC BUS TX.cproj".

Done building target "Build" in project "AVR IZC BUS TX.cproj".
 Build succeeded. ====== Build: 1 succeeded or up-to-date, 0 failed, 0 skipped ====
```

Program for Slave Atmega32:

```
Advanced Mode  Quick Launch (Ctrl+Q)
AVR I2C BUS RX - Microchip Studio
File Edit View VAssistX ASF Project Build Debug Tools Window Help
 G → O | 👸 → 🕮 🖆 → 當 ≌ 👺 | 🐰 🗇 🗇 | 🤊 → 🦿 → | 🔠 🔍 | ▶ 🗦 Debug
                                                                                                           ▼ Debug Browser ▼
💹 👸 ■ 📗 🕨 🕨 🖟 🚼 🤼 🛣 Hex. 🤏 🛜 🕶 🛒 🖫 🖽 🖼 🕮 🛗 🛗 🕍 🕍 🛒 🗯 ATmega32 🥻 None on 💂
                      ₹ DDRA

    Co
    // Program for Slave ATmega32
    #define F_CPU 20000000
                                                    // 20 MHz Clock Speed
    #include <avr/io.h>
    #include <util/delay.h>
    #define LED_DDR DDRA
    #define LED_PORT PORTA
   □void TWI Init(unsigned char Slave Addr)
                                                        // Fill slave address to TWAR
        TWAR = Slave_Addr;
    }
   □unsigned char TWI_Read_Data()
    {
        unsigned char x;
        TWCR = (1 << TWINT) | (1 << TWEA) | (1 << TWEN);
                                                        // Clear TWI interrupt flag, Get acknowlegement, Enable TWI
                                                        // Wait for TWINT flag
        while((TWCR & (1<<TWINT)) == 0);</pre>
        while((TWSR & (0xF8)) != 0x80);
                                                        // Wait for acknowledgement
        x = TWDR:
                                                        // Get value from TWDR
        return x;
   pvoid TWI_Match_ACK()
    {
        while((TWSR & (0xF8)) != 0x60)
            TWCR = (1<<TWINT)|(1<<TWEA)|(1<<TWEN); // Clear TWI interrupt flag, Get acknowlegement, Enable TWI,
            while((TWCR & (1<<TWINT)) == 0);</pre>
                                                      // Wait for TWINT flag

int main(void)

        LED_DDR = 0xFF;
                                                        // Making PORTA As Output port
        TWI_Init(0x20);
        while(1)
             TWI_Match_ACK();
            LED_PORT = TWI_Read_Data();
                                                       // Send the receive value on PORTB
            _delay_ms(5000);
                                                        // Lower or Higher nibble LED's on/off for 5 second
   }
106 % ▼ 4
Output
                                                    Show output from: Build
        C:\Program Files (x86)\Atmel\Studio\7.0\shellUtils\make.exe all --jobs 8 --output-sync
        make: Nothing to be done for 'all'.
    Done executing task "RunCompilerTask".
    Task "RunOutputFileVerifyTask"
                                     : 202 bytes 0.6 % Full
: 0 bytes 0.0 % Full
               Program Memory Usage
               Data Memory Usage
    Warning: Memory Usage estimation may not be accurate if there are sections other than .text sections in ELF file Done executing task "RunOutputFileVerifyTask".
 Done building target "CoreBuild" in project "AVR I2C BUS RX.cproj".
 Target "PostBuildEvent" skipped, due to false condition; ('$(PostBuildEvent)' != '') was evaluated as ('' != '').
 Target "Build" in file "C:\Program Files (x86)\Atmel\Studio\7.0\Vs\Avr.common.targets" from project "C:\Users\heman\Documents\Atmel Studio\7.0\A\
 Done building target "Build" in project "AVR I2C BUS RX.cproj".
 Done building project "AVR I2C BUS RX.cproj".
 Build succeeded.
  ====== Build: 1 succeeded or up-to-date, 0 failed, 0 skipped ========
```