

B.TECH. IV Semester-7	L	T	P	C
CS 701: Artificial Intelligence	3	0	2	4

Unit - 1	6 Hours
<i>Introduction to AI:</i> Basics of problem- solving: problem representation paradigms, state space, satisfiability vs optimality, pattern classification problems, example domains.	
Unit - 2	14 Hours
<i>Problems, State Space Search & Heuristic Search Techniques:</i> Search Techniques : Problem size, complexity, approximation and search; depth, breadth and best search; Hill Climbing, Best-First Search, Problem Reduction, Constraint Satisfaction, Means-Ends Analysis, knowledge based problem solving, artificial neural networks: Hopfield Network, Learning In Neural Network, Application Of Neural Networks, Recurrent Networks, Distributed Representations.	
Unit - 3	14 Hours
<i>Knowledge Representation & Acquisition:</i> Knowledge representation: First order and non-monotonic logic; rule based, frame and semantic network approaches, Knowledge Acquisition: Learn ability theory, approaches to learning.	
<i>Uncertainty:</i> Uncertainty Treatment: formal and empirical approaches including Bayesian theory, belief functions, certainty factors, and fuzzy sets. Detailed Discussion from Example Domains: Industry, Language, Medicine, Verification, Vision, Knowledge Based Systems.	
Unit - 4	8 Hours
<i>Game Playing & Expert Systems:</i> Overview, Minimax, Alpha-Beta Cut-off, Refinements, Iterative deepening, The Blocks World, Components Of A Planning System, Goal Stack Planning, Nonlinear Planning Using Constraint Posting, Hierarchical Planning, Reactive Systems, Languages and Machines : AI languages and systems, special purpose architectures, expert systems, Case studies	
Total Contact Time: 42 Hours	

Recommended Books
1. Stuart Russell and Peter Norvig, Artificial Intelligence: A Modern Approach, Prentice-Hall, Pearson Education 2. Nils J. Nilsson, Artificial Intelligence: A New Sythesis, Morgan-Kaufmann. 3. Artificial Intelligence: Elaine Rich And Kevin Knight, Tata Mcgraw-Hill 4. E. Charniack and D. McDermott, Artificial Intelligence, Addison Wesley 5. Winston P.H., Artificial Intelligence, 3rd edition, Addison Wesley <u>Useful Links</u> 1. AI on the Web 2. http://www.aaai.org

B.TECH. IV Semester-7	L	T	P	C
EC 702: Internet of Things	3	0	2	4

Unit - 1	10 Hours
<p><u>Introduction to IoT</u>: Defining IoT, Characteristics of IoT, Physical design of IoT, Logical design of IoT, Functional blocks of IoT, Communication models & APIs</p> <p><u>IoT & M2M</u>: Machine to Machine, Difference between IoT and M2M, Software define Network</p> <p><u>Sensors and Actuator Modules</u>: Concept, layout, working and applications of different sensors and actuators (temperature sensor, motion sensor LDR sensors, IR sensor, Ultrasonic sensor, Relay etc.)</p>	
Unit - 2	10 Hours
<p><u>Embedded Systems for IoT</u>: Introduction to Arduino Programming, Arduino interfacing with sensors and actuators, IoT based actuator operation using Arduino, Introduction to R-pi and its Programming, R-pi hardware interfacing and applications, Smartphone interfacing with R-pi, Wi-Fi module interfacing with R-pi and associated applications.</p>	
Unit - 3	8 Hours
<p><u>Network & Communication Protocol</u>: Wireless medium access issues, MAC protocol survey, Constrained Application Protocol (CoAP), Message Queue Telemetry Transport Protocol (MQTT), Sensor deployment & Node discovery, Data handling and analytics, Cloud platform for IoTs.</p>	
Unit - 4	14 Hours
<p><u>Developing IoT based Systems</u>: Experiments with Arduino Hardware and sensor interfacing procedures. Automatic lighting control using IoT, home automation, connected health, smart farming, industry applications, connected vehicles, smart city, developing sensor based application through embedded system platform. Introduction to Python, Implementing IoT concepts with python</p> <p><u>Challenges in IoT</u>: Design challenges, Development challenges, Security challenges, other challenges</p>	
Total Contact Time: 42 Hours	

Recommended Books
<ol style="list-style-type: none"> 1. Vijay Madisetti, Arshdeep Bahga, "Internet of Things: A Hands-On Approach", Universities Press. 2. Adrian McEwen, Hakim Cassimally, "Designing the Internet of Things" John Wiley (2014) 1st ed. 3. Hanes David, Salgueiro Gonzalo, Grossetete Patrick, Barton Rob, Henry Jerome, "IoT Fundamentals: Networking Technologies, Protocols and Use Cases for the Internet of Things", Pearson (2016). 4. Waltenegus Dargie, Christian Poellabauer, "Fundamentals of Wireless Sensor Networks: Theory and Practice", John Wiley & Sons, Ltd (2011).

B.TECH. IV Semester-7	L	T	P	C
EC 703: Image Processing and Computer Vision	3	0	2	4

Unit - 1	10 Hours
<p><u>Introduction</u>: Digital Image, Image Processing Origins; Imaging In X-Rays, Ultraviolet, Visible Infrared, Visible, Microwave And Radio Bands; Fundamentals Of Image Processing; Components Of Image Processing Systems.</p> <p><u>Digital Image Fundamentals</u>: Visual Perception — Human Eye, Brightness Adaptation And Discrimination, Electromagnetic Spectrum; Image Sensing And Acquisition — Single, Strip And Array Sensors, Image Formation Models; Image Sampling And Quantization — Basic Concepts, Representation Of Image, Spatial And Gray Level Resolution, Aliasing, Zooming And Shrinking; Relationships Between Pixels — Nearest Neighbor, Adjacency, Connectivity, Regions, And Boundaries; Distance Measures; Image Operations On A Pixel Basis; Linear And Nonlinear Operations.</p>	
Unit - 2	10 Hours
<p><u>Image Enhancement in Spatial Domain and Frequency Domain</u>: Gray Level Transformations - Image Negatives, Log, Power-Law And Piecewise Linear Transformation Functions; Histogram Processing — Equalization, Matching; Enhancement Operations - Arithmetic, Logic, Subtraction And Averaging; Spatial Filtering — Linear And Order-Statistics For Smoothing, First And Second Derivatives/Gradients For Sharpening, 2-D Fourier Transform, Its Inverse And Properties; Discrete And Fast Fourier Transform; Convolution And Correlation Theorems; Filtering In Frequency Domain - Low Pass Smoothing, High Pass Sharpening, Homomorphic Filtering.</p>	
Unit - 3	12 Hours
<p><u>Image Restoration</u>: Image Degradation And Restoration Processes; Noise 'Models - Spatial Properties, Noise Probability Density Functions, Periodic Noise, Estimation Of Noise Parameters; Restoration In The Presence Of Noise and Mean Filters, Order-Statistics Filters, Adaptive Filters; Linear Position-Invariant Degradations And Estimation; Geometric Transformations - Spatial Transformation, Gray-Level Interpolation.</p> <p><u>Morphological Image Processing</u>: Preliminaries-Set Theory And Logic Operations In Binary Images; Basic Morphological Operations - Opening, Closing Operators, Dilation And Erosion; Morphological Algorithms - Boundary Extraction, Region Filling, Extraction Of Connected Components, Convex Hull, Thinning, Thickening, Skeletons; Extension Of Morphological Operations To Gray-Scale Images.</p>	
Unit - 4	10 Hours
<p><u>Image Segmentation</u>: Detection Of Discontinuities — Point, Line And Edges; Edge Linking And Boundary Detection - Local Processing, Global Processing Using Hough Transform; Thresholding - Local, Global And Adaptive; Region-Based Segmentation - Region Growing, Region Splitting And Merging; Motion Detection.</p> <p><u>Image Representation and Description</u>: Representations - Chain Codes, Polygonal Approximations, Signatures, Boundary Segments, Skeletons; Boundary Descriptors - Shape Numbers, Statistical Moments; Regional Descriptors - Topological, Texture And Moments Of 2-D Functions.</p>	
Total Contact Time: 42 Hours	

Recommended Books
<ol style="list-style-type: none"> 1. Gonzalez R. C. and Woods R. E, "Digital Image Processing", Pearson Prentice Hall, 3rd Ed., 2008. 2. Linda Shapiro and Stockman George, "Computer Vision", Prentice Hall, 1st Ed., 2001. 3. Forsyth D. and Ponce J., "Computer Vision - A Modern Approach", Prentice-Hall, 1st Ed., 2003. 4. Sonka M. Hlavac V., Boyle R., "Image Processing, Analysis and Machine Vision", Cengage Learning,

2r4 Indian Reprint, 2009.

5. Jain R., Kasturi R. and Schunk B., "Machine Vision", McGraw - Hill, 1st Ed., 1995.

6. Jain A. K., "Fundamentals Of Digital Image Processing", PHI, 1st Ed., 1989.

7. Ballard D. H. and Brown C. N., "Computer Vision", Prentice Hall, 1st Ed., 1982.

B.TECH. IV Semester-7	L	T	P	C
AE 704: Innovation and Entrepreneurship	3	0	1	4

Unit - 1	10 Hours
<u>Concepts of Entrepreneurship:</u> Scope of Entrepreneurship, Definitions of Entrepreneurship and Entrepreneur, Characteristics of an Entrepreneur, Entrepreneurial Development models and Theories, Entrepreneurs Vs Managers Classification of Entrepreneurs; Major types of Entrepreneurship – Techno Entrepreneurship, Women Entrepreneurship, Social Entrepreneurship, Intrapreneurship (Corporate entrepreneurship), Rural Entrepreneurship, Family Business etc.; Problems for Small Scale Enterprises and Industrial Sickness; Entrepreneurial Trait Tests; Entrepreneurial Environment – Political, Legal, Technological, Natural, Economic, Socio – Cultural etc. ; Motivation; Business Opportunity Identification	
Unit - 2	12 Hours
<u>Functional Management area in Entrepreneurship:</u> <u>Marketing Management:</u> Basic concepts of Marketing, Development of Marketing Strategy and Marketing plan, Online Marketing, New Product Development Strategy <u>Operations Management:</u> Basic concepts of Operations management, Location problem, Development of Operations strategy and plan <u>Personnel Management:</u> Main operative functions of a Personnel Manager, Development of HR strategy and plan <u>Financial Management:</u> Basics of Financial Management, Ratio Analysis, Capital Budgeting, Working Capital Management, Cash Flow Statement, Break Even Analysis	
Unit - 3	10 Hours
<u>Project Planning:</u> Product Development – Stages in Product Development; Feasibility analysis – Technical, Market, Economic, Financial etc.; Project report; Project appraisal; Setting up an Industrial unit – procedure and formalities in setting up an Industrial unit; Business Plan Development <u>Protection of Innovation Through IPR:</u> Introduction to Intellectual Property Rights – IPR, Patents, Trademarks, Copy Rights	
Unit - 4	10 Hours
<u>Innovation and Incubation:</u> Innovation and Entrepreneurship, Creativity, Green Technology Innovations, Grassroots Innovations, Issues and Challenges in Commercialization of Technology Innovations, Introduction to Technology Business Incubations, Process of Technology Business Incubation <u>Sources of Information and Support for Entrepreneurship:</u> State level Institutions, Central Level institutions and other agencies	
Total Contact Time: (42 + 14) Hours	

Recommended Books
1. Desai Vasant, Dynamics of Entrepreneurial Development and Management, Himalaya Publishing House, India, 6 th Revised Edition, 2011 2. Charantimath P. M., Entrepreneurial Development and Small Business Enterprises, Pearson Education, 3 rd Edition, 2018 3. Holt David H., Entrepreneurship: New Venture Creation, Pearson Education, 2016 4. Chandra P., Projects: Planning, Analysis, Selection, Financing, Implementation and Review, Tata McGraw Hill, 9 th Edition, 2019 5. Banga T. R. & Shrama S.C., Industrial Organisation & Engineering Economics, Khanna Publishers, 25 th Edition, 2015 6. Prasad L.M., Principles & Practice Of Management, Sultan Chand & Sons, 8th Edition, 2015

B.TECH. IV Semester-7	L	T	P	C
CS 751: Block-chain Technology	3	0	0	3

Unit - 1	8 Hours
<u>Introduction</u> : Introduction, Background and History, Purpose and Scope, Money, Currency, Ledgers, Bitcoin Core, Distributed Database, Two General Problem, Byzantine General problem and Fault Tolerance, Hadoop Distributed File System, Distributed Hash Table, ASIC resistance, Turing Complete. Cryptography: Hash function, Digital Signature - ECDSA, Memory Hard Algorithm, Zero Knowledge Proof	
Unit - 2	12 Hours
<u>Block Chain</u> : Introduction, Advantage over conventional distributed database, Block chain Network, Mining Mechanism, Distributed Consensus, Merkle Patricia Tree, Gas Limit, Transactions and Fee, Anonymity, Reward, Chain Policy, Life of Block chain application, Soft & Hard Fork, Private and Public block chain.	
Unit - 3	14 Hours
<u>Distributed Consensus</u> : Nakamoto consensus, Proof of Work, Proof of Stake, Proof of Burn, Difficulty Level, Sybil Attack, Energy utilization and alternate.	
<u>Crypto Currency</u> : History, Distributed Ledger, Bitcoin protocols - Mining strategy and rewards, Ethereum -Construction, DAO, Smart Contract, GHOST, Vulnerability, Attacks, Side chain, Name coin	
Unit - 4	8 Hours
<u>Crypto Currency Regulation</u> : Stakeholders, Roots of Bit coin, Legal Aspects-Crypto currency Exchange, Black Market and Global Economy. Applications: Internet of Things, Medical Record Management System, Domain Name Service and future of Block chain, Block chain Use Cases – Finance, Industry, Blockchain in Government and Blockchain Security	
Total Contact Time: 42 Hours	

Recommended Books
<ol style="list-style-type: none"> 1. Arvind Narayanan, Joseph Bonneau, Edward Felten, Andrew Miller and Steven Goldfeder, Bitcoin and Cryptocurrency Technologies: A Comprehensive Introduction, Princeton University Press 2. Antonopoulos, Mastering Bitcoin: Unlocking Digital Cryptocurrencies 3. Satoshi Nakamoto, Bitcoin: A Peer-to-Peer Electronic Cash System 4. DR. Gavin Wood, "ETHEREUM: A Secure Decentralized Transaction Ledger,"Yellow paper.2014 5. Nicola Atzei, Massimo Bartoletti, and Tiziana Cimoli, A survey of attacks on Ethereum smart contracts

B.TECH. IV Semester-7	L	T	P	C
EC 751: Wireless and Mobile Communication	3	0	0	3

The Cellular Concept - System Design Fundamentals	12 Hours
Introduction to Wireless Communication System: Overview of wireless Communication: History, Technical issues, brief of current wireless systems. Cellular system, Hexagonal geometry cell and concept of frequency reuse, Channel Assignment Strategies Distance to frequency reuse ratio, Channel & co-channel interference reduction factor, S/I ratio consideration and calculation for Minimum Co-channel and adjacent interference, Handoff Strategies, Umbrella Cell Concept, Trunking and Grade of Service, Improving Coverage & Capacity in Cellular System-cell splitting, Cell sectorization, Repeaters, Micro cell zone concept, Channel antenna system design considerations.	
Mobile Radio Propagation Model, Small Scale Fading	10 Hours
Large scale path loss:-Free Space Propagation loss equation, Path-loss of NLOS and LOS systems, Reflection, Ray ground reflection model, Diffraction, Scattering, Link budget design, Max. Distance Coverage formula, Empirical formula for path loss, Indoor and outdoor propagation models, Small scale multipath propagation, Impulse model for multipath channel, Delay spread, upper bound Small scale, Multipath Measurement parameters of multipath channels, Types of small scale Fading, Rayleigh and Rician distribution, Statistical for models multipath fading channels.	
Multiple Access Techniques for Wireless Communications	8 Hours
Introduction, Comparisons of multiple Access Strategies Time Division Multiple Access, Code Division Multiple Access, Frequency Division Multiple Access, Orthogonal Frequency Division Multiplexing.	
Wireless Systems and Recent trends in Wireless Communication	12 Hours
GSM system architecture, Radio interface, Protocols, Localization and calling, Handover, Authentication and security in GSM, GSM speech coding, Concept of spread spectrum, Architecture of IS-95 CDMA system, Air interface, CDMA channels, Soft handoff, Power control in CDMA, RAKE Receiver, GPRS system architecture. Introduction to Wi-Fi, WiMAX, ZigBee Networks, Wireless Adhoc Network and Mobile Portability, Security issues and challenges in a Wireless network. Recent developments from Third Generation (3G) to 5G wireless networks.	
Total Contact Time: 42 Hours	

Recommended Books
1. Theodore S. Rappaport, Wireless Communication, Prentice hall.
2. Upena D. Dalal , Wireless Communication, Oxford Higher Education.
3. Rajpandya, Mobile and personal Communication system and services, IEEE press (PHI).
4. Vijay Garg, Wireless Communications and Networking, Elsevier
5. T.G.Palanivelu, R. Nakkeeran , Wireless Mobile Communication, PHI.

B.TECH. IV Semester-7	L	T	P	C
EC 752: Analog VLSI Design	3	0	0	3

Prerequisite
Basic MOS Device Physics: General Considerations, MOS I/V Characteristics, Second-Order Effects, MOS Device Effects; Feedback: General Considerations, Feedback Topologies, Effect of Loading Electronic Circuits (EC 304)

Single - Stage & Differential Amplifiers	12 Hours
The Cascode Amplifier, Current - Mirror Circuit with Improved Performance, Some Useful Transistor Pairings Differential Amplifier with a Current - Mirror Load, Multistage Amplifiers	
Frequency Response of Amplifiers, Feedback & Stability	16 Hours
High-Frequency Response of the Common-Gate and Cascode Amplifiers, High-Frequency Response of the Source and Emitter Followers, High-Frequency Response of Differential Amplifiers, Other Wideband Amplifier Configurations Effect of Feedback on the Amplifier Poles, Stability Study Using Bode Plots, Frequency Compensation	
Operational Amplifiers Circuits	14 Hours
The Two - Stage CMOS OP-AMP The Circuit, Input Common-Mode Range and Output Swing, DC Voltage Gain, Common-Mode Rejection Ratio (CMRR), Frequency Response, Slew Rate, Power-Supply Rejection Ratio (PSRR), Design Trade-Offs, A Bias Circuit for the Two-Stage CMOS Op Amp	
Total Contact Time: 42 Hours	

Recommended Books
<u>Text-Book</u> 1. A. S. Sedra and K. C. Smith, "Microelectronic Circuits", Oxford University Press, 7th Edition.
<u>Reference Books</u> 2. Razavi Behzad, "Design of Analog CMOS Integrated Circuit", Tata McGraw-Hill, 2002 3. John D. A. and Martin K., "Analog Integrated Circuit Design", Wiley, 1997 4. Allen Philip and Holberg Douglas, "CMOS Analog Circuit Design", Oxford University Press, 2002 5. Gregorian R. and Temes G.C., "Analog MOS ICs for Signal Processing", Wiley 1986 6. Baker Jacob R., Harry W. Li and Boyce David E., "CMOS: Circuit Design, Layout and Simulation", Wiley Interscience, 2003

B.TECH. IV Semester-7	L	T	P	C
CS 761: Computer Ethics and Public Policy	3	0	0	3

Unit - 1	14 Hours
<p><u>Introduction: Why Computer Ethics?:</u> A Brief History of Computers and the Internet, Meaning of Ethics, Distinction between Law and Ethics, Computer Ethics as a Unique Kind of Ethics</p> <p><u>Philosophical & Professional Ethics:</u> Descriptive and normative claims, Ethical Relativism, Utilitarianism, Deontological Theories, Virtue Ethics, Individual and Social Policy ethics, Characteristics of Profession, codes of ethics and professional conduct, The Ethical Decision-Making Process, Steps in the Ethical Decision-Making Process, Culture Clash on the Net Case and its solution</p>	
Unit - 2	14 Hours
<p><u>Privacy Concerns and Case Studies:</u> Understanding computer privacy issues , Reframing the issues, Legislative background, Global Perspective, Proposal for better privacy protection, cybersquatting, Fake IDs, Identity Theft, Intellectual Property, Open-Source Software, Phishing, Pharming, Software Bombs, Sale of Term Papers Online, Sale of Academic Degrees Online, Web Spoofing, Internet Fraud Case, Free Software Case, Finals Week Case, Software Licensing: Stuck in the Middle Case, Borrowed Hardware Case, Risks of academic Cheating by Computer Case, Cookies, Employee Monitoring, Government Surveillance, Hackers, Fingering Case, E-mail Addresses Case, Deceased Student Case, Chain Letter Case, Accountability of Bloggers, Online Voting, Google and Chinese Censorship Case, File Transfer Case, Ethical Issues in Parasitic Computing</p>	
Unit - 3	14 Hours
<p><u>Ethics and the Internet: Ethics Online & Social Implications and Social Values.:</u> Significant characteristics, Hacking and Hackers ethics, New spice of old crime, Netiquette, Policy approaches, ACM Code of Ethics and Professional Conduct, Software Engineering Code of Ethics and Professional Practice, The Ten Commandments of Computer Ethics, The World Summit on the Information Society, Geneva Plan of Action, Tunis Commitment, Technology and Social Change, Embedded values and Impeded values, Democratic values in Internet, Access and Digital divide, Overarching and Future Issues</p>	
Total Contact Time: 42 Hours	

Recommended Books
<ol style="list-style-type: none"> 1. Johnson, Deborah G. (2001). Computer ethics, 3rd edition. Upper Saddle River, NJ: Prentice Hall. A philosophical survey of the ethical issues arising around computer technology. 2. Moor, James H. (1985). "What Is Computer Ethics?" Metaphilosophy 16(4): 266–275. Classic piece on why the study of computer ethics is needed. 3. Advisory Committee on Automated Personal Data Systems. Records, Computers, and the Rights of Citizens. U. S. Department of Health, Education, and Welfare Publication No. (OS) 73–94, July 1973.

B.TECH. IV Semester-7	L	T	P	C
CS 762: Web 2.0	3	0	0	3

Client	6 Hours
Languages - JavaScript, Angular and Node JS, Python, HTML5 – Review JSON - JSON Grammar, JSON in JavaScript, JSON in Java/Python - Create, Parse JSON data, XML - JSON comparison AJAX, Tools, Mashup	
Protocols & Frameworks	12 Hours
HTTP & HTTPS - Protocol & Messages - Version, Headers, Body, Methods, Status codes, Meaning of Error Codes (200, 400, 401, 402, 404, etc) Resources - URIs, URL, URN - Syntax and Schemes, Relative URLs WebSocket - Events, Methods and Attributes Architectural components of the web - Proxies, Cache, Gateways, Tunnels, Agents REST API - Fundamentals, Resource Modeling, URI, Representations, Usage of HTTP SOAP - Protocol Introduction, Comparison with REST API Syndication Protocols (Atom, RSS)	
LAMP (Linux, Apache, MySQL, PHP/Perl/Python) Stack	12 Hours
Tomcat/Apache Web Server - Overview, Configuration, Working Model & Web Site Creation, Usage of TCP/IP, Log Files, Apache Modules & Directives, Security - Anonymous Access, Authentication, Certificate based interaction, Allow & Deny of Hosts, Session Management Client-Server Relationship; Web Server Serving Static, Embedded and Dynamic contents from DB and using Python, HTML; Getting data from Client & Link; Multi-tasking	
MEAN Stack, Patterns, Dockerization/Containers	12 Hours
Introduction to Node.js, MongoDB, AngularJS and ExpressJS Node.js - Writing a web-server, Event Loop, Concurrency, Asynchronous coding, Callback Functions, Exception Handling, Event Emitters, Event Listeners, Promises ExpressJs - Router, Middleware, Routes, Generating HTML AngularJS - Data binding - OneWay, TwoWay Bindings, Digest Loop, Controllers, Directives Tasks, Debugging, Testing Introduction, Singleton, MVC, Proxy. Architectural Patterns – MVC. Design Patterns - Singleton, Proxy Deploying the Services on Kubernetes/Dockers. Building and running a Docker.	
Total Contact Time: 42 Hours	

Recommended Books
<ol style="list-style-type: none"> 1. "JSON at Work: Practical Data Integration for the Web", 1st Edition, Tom Marrs, O'REILLY 2. "JavaScript: The Definitive Guide", 5th Edition, David Flanagan, O'REILLY 3. "HTTP: The Definitive Guide", by David Gourley, Brian Totty, Marjorie Sayer, Anshu Aggarwal, Sailu Reddy, O'REILLY 4. "REST API Design Rulebook" by Mark Masse, O'REILLY 5. "Node.js, MongoDB and AngularJS Web Development: The Definitive Guide to Building JavaScript-Based Web Applications from Server to Frontend (Developer's Library)", 1st Edition, Brad Dayley 6. "Apache: The Definitive Guide", 3rd Edition by Ben Laurie, Peter Laurie, O'REILLY 7. Tomcat: The Definitive Guide, 2nd Edition, Jason Brittain, Ian F Darwin, O'REILLY 8. "Getting MEAN with Mongo, Express, Angular, and Node", by Simon Holmes

B.TECH. IV Semester-7	L	T	P	C
EC 761: VLSI Testing & Verification	3	0	0	3

Prerequisite
VLSI Design

Unit - 1	8 Hours
<u>Introduction to VLSI testing</u> : Importance of testing, Challenges in VLSI testing, Levels of abstractions in VLSI testing, Functional vs. Structural approach to testing, Complexity of the testing problem, Controllability and Observability, Generating test for a single stuck at fault in combinational logic, D-algorithm, FAN and PODEM algorithms, Test optimization and fault coverage.	
Unit - 2	12 Hours
<u>Design for testability (DFT)</u> : Testability analysis, Scan cell design, Scan architectures, Scan design rules, Scan design flow, Special purpose scan designs Logic and fault simulation, Fault detection, Adhoc and structured approaches to DFT, Various kinds of scan design, Fault models for PLAs, Bridging and delay faults and their tests.	
<u>Test generation</u> : Random test generation, Boolean difference, ATPG algorithms for combinational circuits, Sequential ATPG, Untestable faults, IDDQ testing The LFSRs and their use in random test generation and response compression (including MISRs).	
Unit - 3	10 Hours
<u>Built-in self-test (BIST)</u> : Design rules, Exhaustive testing, Pseudo-random testing, Pseudo-exhaustive testing, Output response analysis, Logic BIST architectures Test compression: Test stimulus compression, Test response compaction, Architectures for test compression.	
<u>Boundary scan and core based testing</u> : IEEE standards for digital boundary scan, Embedded core test standards Analog and mixed signal testing, Delay testing, Physical failures, Soft errors Reliability, FPGA testing, MEMS testing, RF testing, High speed I/O testing.	
Unit - 4	12 Hours
<u>Introduction to verification</u> : Importance of verification, Verification plan, Verification flow, Levels of verification, Verification methods and languages.	
<u>Verification techniques</u> : Introduction to Hardware Verification methodologies, Verifications based on simulation, analytical and formal approaches. Functional verification, Timing verification, Formal verification. Basics of equivalence checking and model checking.	
Total Contact Time: 42 Hours	

Recommended Books
1. Parag K. Lala, An Introduction to Logic Circuit Testing, Morgan & Claypool Publishers. 2. Thomas Kropf, Introduction to Formal Hardware Verification, Springer. 3. Michael L. Bushnell and Vishwani D. Agrawal, Essentials of Electronic Testing, Springer India. 4. M. Abramovici, M. Breuer, and A. Friedman, Digital System Testing and Testable Design, Jaico Publishing House 5. Baker Jacob R., Harry W. Li and Boyce David E., "CMOS: Circuit Design, Layout and Simulation", Wiley Interscience, 2003

B.TECH. IV Semester-7	L	T	P	C
EC 762: Electronic System Design	3	0	0	3

Unit - 1	10 Hours
<u>DC-to-DC Converter</u> Introduction, Simple DC to DC Converter: Series Controlled Regulator, Shunt Controlled Converter, Practical Regulators Switched Mode Power Converters: Primitive DC-to-DC converter, A simplified analysis of the primitive converter, Nonidealities in the primitive converters More Versatile Power Converters: Buck Converter, Boost Converter, Buck-Boost Converter Discontinuous Mode of Operation in DC to DC Converters: Buck converter in DCM operation Isolated DC to DC converters: Forward Converter, Push-Pull converter, Half and Full Bridge converter, Fly-back converter	
Unit - 2	10 Hours
<u>The 741 BJT OP-AMP</u> : The 741 Circuit, DC Analysis, Small-Signal Analysis, Frequency Response, Slew Rate <u>Non-Linear OP-AMP Circuits</u> : The Basic Logarithmic Amplifier (Log Amplifier with a diode, Log Amplifier with a BJT), The Basic Antilog Amplifier, Signal Compression with Logarithmic Amplifiers <u>Power Amplifiers</u> : IC power amplifiers: A Fixed-Gain IC Power Amplifier, The Bridge Amplifier; Class D Power Amplifiers; Power Transistors: Package and Heat Sinks, Power BJTs, Power MOSFETs, Thermal Considerations	
Unit - 3	10 Hours
<u>Analog Multiplier</u> : Simple Multiplier Using An Emitter Coupled Transistor Pair, Gilbert Multiplier Set, Complete Four Quadrants Analog Multiplier, IC Multiplier, Application Of Analog Multiplier <u>Transconductance-C Filters</u> : Methods for IC Filter Implementation, Transconductors, Second-order G_m -C filter <u>Switched Capacitor Filter</u> : The Basic Principle, Practical Circuits, Final Remarks. <u>Process Control Systems</u> : Proportional Control, Proportional Plus Integral (PI) Control, Proportional Plus Derivative (PD) Control, Proportional Plus Integral Plus Derivative (PID) Control	
Unit - 4	12 Hours
<u>Sample-and-Hold (S/H) Characteristics</u> : Sample Mode, Hold Mode, Aperture Error <u>DAC Specifications</u> : Differential Nonlinearity, Integral Nonlinearity, Offset, Gain Error, Latency, Signal-to-Noise Ratio (SNR), Dynamic Range <u>ADC Specifications</u> : Quantization Error, Differential Nonlinearity, Missing Codes, Integral Nonlinearity, Offset and Gain Error, Aliasing, Signal-to-Noise Ratio, Aperture Error <u>DAC Architectures</u> : Digital Input Code, Resistor String: Mismatch errors related to the resistor-string DAC, Integral Nonlinearity of the resistor-string DAC, Differential Nonlinearity of the Worst-Case Resistor-String DAC <u>ADC Architectures</u> : Flash ADC (Accuracy issues for the Flash ADC) <u>ADC0808/ADC0809</u> : Introduction, Functional Description, Analog Inputs: Ratiometric Inputs, Absolute Analog Inputs, Differential Inputs, Analog Input Considerations; Microprocessor Interfacing, Interfacing to the 8080, Conclusion <u>DAC0808</u> : General Description, Features, Block and Connection Diagrams, Ratings and Electrical Characteristics, Typical Applications	
Total Contact Time: 42 Hours	

Recommended Books
<ol style="list-style-type: none">1. V. Ramanarayanan, "Course Material on Switched Mode Power Conversion", IISC, 2007.2. A. S. Sedra and K. C. Smith, "Microelectronic Circuits", Oxford University Press, 7th Edition.3. Botkar K. R., "Integrated Electronics", Khanna Publishers, 10th Ed., 2006.4. Baker Jacob R., Harry W. Li and Boyce David E., "CMOS: Circuit Design, Layout and Simulation", Wiley Interscience, 20035. " ANB-247 Using the ADC0808/ADC0809 8-Bit μP Compatible A/D Converters with 8-Channel Analog Multiplexer", TI Application Note.6. " DAC0808 8-Bit D/A Converter", TI Application Note.