B.TECH. II Semester-2	L	Т	Р	С
CS 201: Data Structure & Algorithm	3	0	2	4

## **Introduction to Data Structure, Basic Data Types**

14 Hours

Basic Terminology, Internal representation of Primitive Data structure: Integers, Floating point numbers, Packed decimal, Characters, Structures, Unions, and Pointers.

Arrays: Definition, Memory organization, Operations on Arrays: Traversing, Insertion, Deletion, Updating, Resizing

Stacks: Basic operations, Stack, Dstack and applications

Queues: Operations of queues, Circular Queue, Priority Queue, Dequeue, Application of queues Linked list: Singly linked lists and memory representation, Operations of Link list (Traversing, Searching, Insertion, Deletion, inversion, concatenation, copying and comparison, allocation and deallocation), Doubly linked list and operations, Circular Link list, Multilevel link list

Trees 10 Hours

Introduction, Binary Trees and their representation, Operations on Binary trees: Creation, transformation of trees into binary trees, traversal, Searching, Insertion and Deletion.

Type of trees: Complete Binary trees, Extended binary trees, General trees, AVL trees, Threaded trees, B trees

Application: Arithmetic expression evaluation, infix-prefix-postfix notation conversion.

Graph 8 Hours

Graph Terminologies, Types of graph, Representation of graphs: Adjacency list, Adjacency matrix, Incidence matrix, Graph Traversal (BFS, DFS), Connected Component, minimum spanning trees, Shortest path algorithms, Topological Sort, Activity Networks and Critical Paths.

## **Illustrated Algorithms**

10 Hours

Sorting (Bubble, Selection, Insertion, Heap sort), Dictionaries, hashing table and functions, collision resolution methods and techniques, Searching(Linear Search, Binary Search), Character String and different string operations

**Total Contact Time: 42 Hours** 

- 1. "An Introduction to Data Structures with Applications", Trembley & Sorenson, 2/E, TMH, 1991.
- 2. "Data Structures using C and C++", Tanenbaum & Augenstein, 2/E, PHI, 2007.
- 3. "Fundamentals of Data Structures", Horowitz and Sahani, Galgotia Publications, reprint 2004.
- 4. "Introduction to Algorithms", T. H. Cormen, C. E. Leiserson, R. L. Rivest, 2/E, PHI, 2001.
- 5. "Data Structures and Program Design in C", Robert L.Kruse, C.L.Tondo and Brence Leung, Pearson Education, 2/E, 2001

B.TECH. I Semester-2	L	Т	Р	С
EC 202: Electronic Devices and Circuits	3	0	2	4

### **Prerequisite**

Signals, Frequency Spectrum of Signals, Circuit Models for Amplifiers Intrinsic Semiconductors, Doped Semiconductors, Current Flow in Semiconductors

Diodes 10 Hours

The pn Junction, The pn Junction with an Applied Voltage, Capacitive Effects in the pn Junction The Ideal Diode, Terminal Characteristics of Junction Diodes, Modeling the Diode Forward Characteristic, Operation in the Reverse Breakdown Region—Zener Diodes, Rectifier Circuits, Limiting and Clamping Circuits, Special Diode Types (Schottky-Barrier Diode, Varactors, Photodiodes, Light-Emitting Diodes (LEDs), etc.)

## **MOS Field-Effect Transistors & Bipolar Junction Transistors**

12 Hours

### **MOSFETs**

Device Structure and Physical Operation, Current—Voltage Characteristics, MOSFET Circuits at DC, The Body Effect and Other Topics

#### **BJTs**

Device Structure and Physical Operation, Current–Voltage Characteristics, BJT Circuits at DC, Transistor Breakdown and Temperature Effects

# Transistor Amplifiers 10 Hour

Basic Principles, Small - Signal Operation and Models (The MOSFET case, The BJT case, Summary Tables), Basic Configurations, Biasing (The MOSFET case, The BJT case), Discrete - Circuit Amplifiers

### **Building Blocks of Integrated Circuit Amplifiers, Differential Amplifiers**

10 Hours

IC Design Philosophy, IC Biasing—Current Sources, Current Mirrors, & Current-Steering Circuits; The Basic Gain Cell, The Common - Gate and Common - Base Amplifiers

The MOS Differential Pair, The BJT Differential Pair, Common - Mode Rejection, DC Offset

**Total Contact Time: 42 Hours** 

### **Recommended Books**

### Text-Book

- 1. A. S. Sedra and K. C. Smith, "Microelectronic Circuits", Oxford University Press, 7th Edition. Reference-Books
- 2. Boylestad Robert L. and Nashlesky Louis, "Electronics Device & Circuits and Theory", PHI, 10th Edition, 2009.
- 3. Millman Jacob, Halkias Christos C. and Parikh C., "Integrated Electronics", McGraw-Hill, 2nd Edition, 2009.
- 4. Schilling Donald L. and Belove E., "Electronics Circuits Discrete and Integrated", McGraw-Hill, 3rd Edition, 1989, Reprint 2008.
- 5. "Electronic Principles", Malvin Albert & David J. Bates, Tata McGraw Hill, 7th edition, 2007.
- 6. "Pulse, Digital and Switching Waveforms", Millman J., Taub H. and Mothiki Suryaprakash, McGraw-Hill, 2nd Ed., 2007.

B.TECH. II Semester-2	L	T	Р	С
AE 203: Engineering Mechanics	3	0	2	4

## Introduction Forces/Equilibrium of Rigid Body

6 Hours

Scalar and vectors, system of forces, resultant force, Statics of particles. Free-body diagrams. Equilibrium of particle in two dimensions, Resultants of three or more concurrent forces, Resolution of a force into components. Rectangular components of a force. Resultants by rectangular components, Concurrent force system in space: Resolution of a force into rectangular components in space, Coplanar Non- Concurrent Force Systems, Moments about Points and Axes. Equilibrium, Noncoplanar Non-concurrent Forces.

#### **Centroid Moment of Inertia**

6 Hours

Distributed forces: Centroid and centre of gravity. Determination of centroid of lines and areas using integral technique, Determination of centroid of composite wires and areas, Centroid of volumes. Theorems of Pappus-Guldinus and its applications, Second moment of areas. Definition of moment of inertia. Determination of moment of areas by integration, Parallel axis theorem for Moment of Inertia. MI of composite areas.. Concept of Mass moment of inertia of bodies.

TRUSS 6 Hours

Types of structures in Engineering. Trusses and beams: definition, stability and determinateness, Determination of reactions at supports for planar trusses. Basic assumption for analysis of trusses. Procedures for analysis of trusses, Analysis of plane trusses by method of joints. Concept of zero force members. Analysis of plane trusses by method of sections.

Beams and Cables 10 Hours

Beams - Definitions, types of beams, types of loading, types of supports. Determination of reactions for simply supported and overhanging beams. Relation between distributed load, Shear force and Bending moment Shear force and Bending moment in beams with diagrams

Cables - Cables with Concentrated Loads

Friction 4 Hours

The Laws of Dry Friction. Coefficients of Friction, Angles of Friction, Analysis of systems involving dry frictions such as ladders spheres etc., Belt Friction, Analysis of flat and v-belt.

Kinetics of Particles 5 Hours

Force & acceleration. Newton's laws of motion. D'Alembert's principle, Dependent motion of particles. Analysis for dependent motion of particles, Impulse and Momentum: Concept, Definition, Principle of linear impulse & momentum, Conservation of linear momentum of a system of particles.

Vibrations 5 Hours

Definitions, Equation of motion for single degree of freedom, Introduction to free & forced vibrations. Procedure for analysis of system involving free & forced vibrations, Example on free vibration, Example on forced vibration, Concept of earthquake induced waves & its effect on structures.

**Total Contact Time: 42 Hours** 

- 1. "Vector mechanics for engineers: Statics and Dynamics", Beer F.P. and Johnston E.R. Tata McGraw-Hill, New Delhi.
- 2. "Engineering Mechanics: Statics and Dynamics", Desai J.A. & Mistry B.B., Popular Prakashan, Surat.
- 3. "Engineering Mechanics: Statics and Dynamics", Hibbeler R.C., Prentice Hall of India, New Delhi.
- 4. "Engineering Mechanics: Statics and Dynamics", Meriam J.L., and Kraige L.G., John Wiley and sons, New York.
- 5. "Engineering Mechanics: Statics and Dynamics", Rajsekaran S., Vikas Publication, New Delhi.
- 6. "Applied Mechanics", Shah H. J. and Junarkar S. B., Charotar publication, Anand.
- 7. "Engineering Mechanics", Bhavikatti S. S. and Rajashekarappa K. G., Wiley 'Eastern Ltd.

B.TECH. I Semester-2	L	T	Р	С
EC 204: Digital Design	3	0	2	4

Unit - 1 12 Hours

<u>Digital Systems and Binary Numbers</u>: Digital Systems, Binary Numbers, Number-Base Conversions, Octal and Hexadecimal Numbers, Complements of Numbers, Signed Binary Numbers, Binary Codes, Binary Storage and Registers, Binary Logic

<u>Boolean Algebra and Logic Gates</u>: Introduction, Basic Definitions, Axiomatic Definition of Boolean Algebra, Basic Theorems and Properties of Boolean Algebra, Boolean Functions, Canonical and Standard Forms, Digital Logic Gates, Integrated Circuits

<u>Gate-Level Minimization</u>: Introduction, The Map Method, Two- and Three-Variable Maps, Four-Variable K-Map, Product-of-Sums Simplification, Don't-Care Conditions, NAND and NOR Implementation, Other Two-Level Implementations, Exclusive-OR Function

Unit - 2 8 Hours

<u>Combinational Logic</u>: Introduction, Combinational Circuits, Analysis Procedure, Design Procedure, Binary Adder-Subtractor, Decimal Adder, Binary Multiplier, Magnitude Comparator, Decoders, Encoders, Multiplexers

Unit - 3 14 Hours

<u>Synchronous Sequential Logic</u>: Introduction, Sequential Circuits, Storage Elements: Latches, Storage Elements: Flip-Flops, Analysis of Clocked Sequential Circuits, State Reduction and Assignment, Design Procedure

<u>Registers and Counters</u>: Registers, Shift Registers, Ripple Counters, Synchronous Counters, Other Counters

Unit-4 8 Hours

<u>Memory and Programmable Logic</u>: Introduction, Random-Access Memory, Memory Decoding, Error Detection and Correction, Read-Only Memory, Programmable Logic Array, Programmable Array Logic

<u>Design at the Register Transfer Level</u>: Introduction, Register Transfer Level Notation, Algorithmic State Machines (ASMs)

**Total Contact Time: 42 Hours** 

## **Recommended Books**

## **Text-Book**

1. "Digital Design", Mano Morris, 5th Edition, Pearson Education, 2005.

### Reference Books

- 1. "Digital Logic and Computer Design", Mano Morris, 3rd Edition, Pearson Education, 2005.
- 2. "Digital Electronics Practice Using Integrated Circuits", Jain and Anand, TMH, 2004
- 3. "Logic and Computer Design Fundamentals", Kime Charles, Pearson Education, 2004.
- 4. "Digital Circuits and Logic Design", Lee Samual, PHI, 1998.
- 5. "Digital Fundamentals", Floyd and Jain, Pearson Education, 2006.

B.TECH. II Semester-2	L	Т	Р	С
AS 205: Discrete Mathematics	3	1	0	4

## Graph Theory 8 Hours

Graphs, Definition & basic concepts of finite & infinite graph, Incidence & Degree, Isomorphism, Subgraph, Walk, Path & circuits, Operations on graphs, connected graph, Disconnected graph & components, Complete graph, Regular graph, Bipartite graph, Euler's graph, Hamiltonian paths & circuits, Weighted graphs, Applications, Directed & Undirected graphs, Connectivity of graphs.

Trees 6 Hours

Definition & properties of trees, Pendent vertices in a tree, Distance between two vertices Centre, Radius & diameter of a tree, Rooted & binary trees, Representation of Algebraic structure by Binary trees, Binary search trees, Spanning trees & fundamental circuits.

Relation & Lattices 8 Hours

Definition & Basic properties, Graphs of relation, Matrices of relation, Equivalence relation, Equivalence classes, Partition, Partial ordered relation, Posets, Hasse diagram, Upper bounds, Lower bound, GLB & LUB of sets, Definition & properties of Lattice, Sub lattice, Distributive & modular lattices, complemented & Bounded Lattices, complete lattices & Boolean algebra

Group Theory 8 Hours

Basic properties of Group, Groupoid, semigroup & monoid, Abelian group, Subgroup, Cosets, Normal subgroup, Lagrange's theorem, Cyclic group, Permutation group, Homomorphism & Isomorphism of groups, Basic properties, error correction & detection code.

## **Mathematical Logic & Program Verification**

12 Hours

Propositions, logical operators & propositional algebra, Predicates& quantifiers, Interaction of quantifiers with logical operators, Logical interference & proof techniques, Formal verification of computer programs (elements of Hoare logic).

Total Contact Time: (42 + 14) Hours

- 1. "Discrete Mathematics and Its Applications", Rosen K.H., McGraw Hill, 6th Ed., 2006.
- 2. "Discrete Mathematical Structure", Kolman B., Busby R.C. & Ross S., Prentice Hall of India Pvt. Ltd, 5thEd, 2003.
- 3. "Discrete Mathematical Structure with Applications to Computer Science", Tremblay J. P. & Manohar R., McGraw Hill, 1999.
- 4. "Graph theory with applications to Engineering & Computer Science", DeoNarsingh., Prentice Hall of India Pvt. Ltd., 2000.
- 5. "Elements of Discrete Mathematics", Liu C.L., McGraw Hill, 2000.

B.TECH. II Semester-2	L	Т	Р	С
EC 206: Signals & Systems	3	1	0	4

Unit - 1 8 Hours

<u>Introduction</u>: Signals, Systems, and Signal Processing, <u>Classification of Signals</u>, The Concept of Frequency in Continuous-Time and Discrete-Time Signals, Analog-to-Digital and Digital-to-Analog Conversion

<u>Discrete-Time Signals and Systems</u>: Discrete-Time Signals, Discrete-Time Systems, Analysis of Discrete-Time Linear Time-Invariant Systems, Discrete-Time Systems Described by Difference Equations, Implementation of Discrete-Time Systems, Correlation of Discrete-Time Signals

Unit - 2 10 Hours

<u>Laplace Transform</u>: Introduction, The Laplace Transform, Region of Convergence, Inverse Laplace Transform, Properties of Laplace Transform

<u>z-Transform</u>: The z-Transform (Direct & Inverse), Properties of the z-Transform, Rational z-Transforms, Inversion of the z-Transform, Analysis of Linear Time-Invariant Systems in the z-Domain, The One-sided z-Transform

Unit - 3 12 Hours

<u>Frequency Analysis of Signals</u>: Frequency Analysis of Continuous-Time Signals, Frequency Analysis of Discrete-Time Signals, Frequency-Domain and Time-Domain Signal Properties, Properties of the Fourier Transform for Discrete-Time Signals

<u>Frequency-Domain Analysis of LTI Systems</u>: Frequency-Domain Characteristics of Linear Time-Invariant Systems, Frequency Response of LTI Systems, Correlation Functions and Spectra at the output of LTI systems, Linear Time-Invariant Systems as Frequency-Selective Filters, Inverse Systems and Deconvolution

Unit - 4 12 Hours

<u>Sampling and Reconstruction of Signals</u>: Ideal Sampling and Reconstruction of Continuous-Time Signals, Discrete-Time processing of Continuous-Time Signals, Sampling and Reconstruction of Continuous-Time Bandpass Signals, Sampling of Discrete-Time Signals

<u>The Discrete Fourier Transform</u>: Frequency-Domain Sampling: The Discrete Fourier Transform, Properties of the DFT, Linear Filtering Methods Based on the DFT, Frequency Analysis of Signals Using the DFT

Total Contact Time: (42 + 14) Hours

### **Recommended Books**

## Text-Book

1. "Digital Signal Processing: Principle, Algorithms, and Applications", Prokis John G., Pearson Educations, 4th Ed.

- 2. "Signal and Systems", Oppenheim Alan V., Wilsky Alan S. and Nawab Hamid S., Pearson Educations, 3rd Ed., 2006.
- 3. "Linear Systems and Signals", Lathi B. P., Oxford University Press, 2nd Ed., 2007.
- 4. "Introduction to Signal and Systems", Stuller John Alan, Thomson India Edition, 1st Ed., 2007.
- 5. "Fundamental of Signals and Systems", Roberts M. J. and Govind Sharma, Tata McGraw- Hill, 2nd Ed., 2010.