

Transistor :-

↳ It is a semi conductor device & used as a transfer resistor.

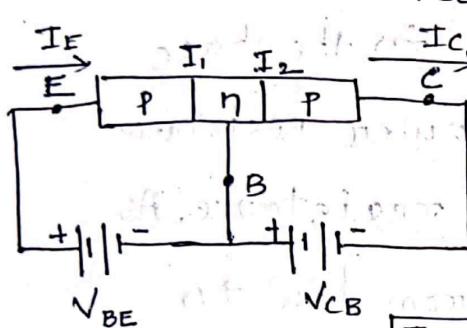
Two types :- (1) n-p-n transistor
(2) p-n-p transistor

Three terminals of transistor :-

→ Emitter

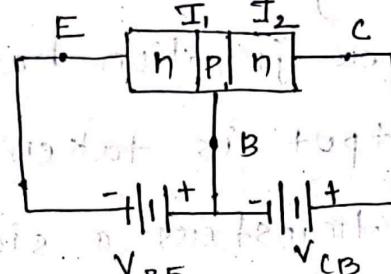
→ Base

→ Collector



$$I_E = I_B + I_C$$

$$I_C = I_{Major} + I_{Minority}$$



$$V_{BE}$$

$$V_{CB}$$

Width: $C > E > B$

I_1 = Base Emitter junction
(Forward biased)

Doping: $E > C > B$

I_2 = collector base junction
(Reverse biased)

→ In BJT (Bipolar junction transistor) both e^- & holes are responsible for conduction.

∴ It is called as bipolar junction transistor.

→ Hence base is thinner than collector.

emitter. Emitter is heavily doped so that it can inject a large no. of charge carriers i.e. e^- or holes in to the base.

→ Base is slightly doped & very thin.

so, it passes most of the emitter injected

- charge carriers to the collector.
- Collector is moderately doped.
- Size & width is max^m, bcoz it has to collect the e^- .
- For transistor operation, the emitter based junction is always forward biased & collector based junction is always reverse biased.
- Weak signals are introduced in the base emitter junction having lower resistance & output is taken high resistance. As BJT transfers a signal from low to high resistance, it is known as transistor.
- BJT is generally used in amplification of weak signals & in switching operations.

$$I_E = I_B + I_C$$

$$I_C = I_{Majority} + I_{Minority}$$

$$I_C = I_M + I_{Co}$$

I_{Co} = collector current with emitter open.

I_E & I_C is mA range. I_B is microampere range.

Types of transistor configuration:-

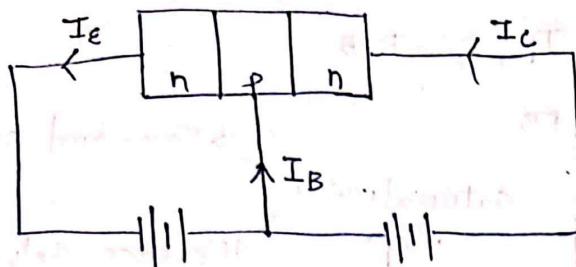
(1) Common base (CB)

(2) Common Emitter (CE)

(3) Common collector (CC)

COMMON BASE :-

19/09/19



$$I_E = I_B + I_C$$

$$\begin{array}{c} I_E \leftarrow \\ P \quad \downarrow \quad \rightarrow \\ I_C \\ I_B \end{array}$$

$$I_C = I_{C\text{maj}} + I_{C\text{min}}$$

$$I_C \approx I_{C\text{maj}}$$

$$I_C = I_{C\text{maj}} + I_{CB0} \quad [\text{when input side is open}]$$

Hence; $\alpha = \frac{I_C}{I_E}$

$$\Rightarrow I_{C\text{maj}} = \alpha I_E$$

$$\text{so, } I_C = \alpha I_E + I_{CB0}$$

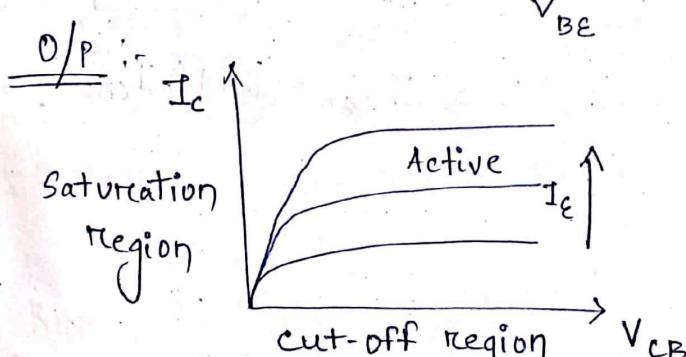
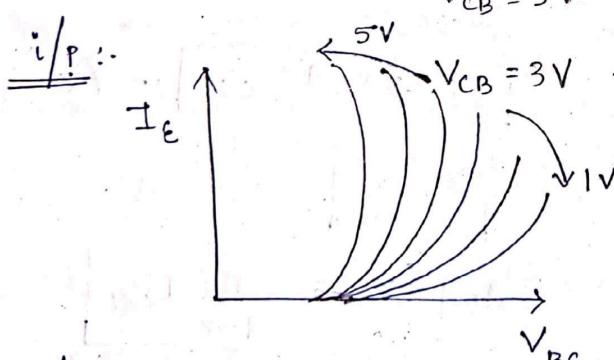
$$\Rightarrow I_C = \alpha (I_C + I_B) + I_{CB0}$$

$$\Rightarrow I_C = \alpha I_C + \alpha I_B + I_{CB0}$$

$$\Rightarrow I_C (1 - \alpha) = \alpha I_B + I_{CB0}$$

$$\Rightarrow I_C = \frac{\alpha}{1 - \alpha} I_B + \frac{1}{1 - \alpha} I_{CB0} \quad \text{--- (1)}$$

$$V_{CB} = 5V$$

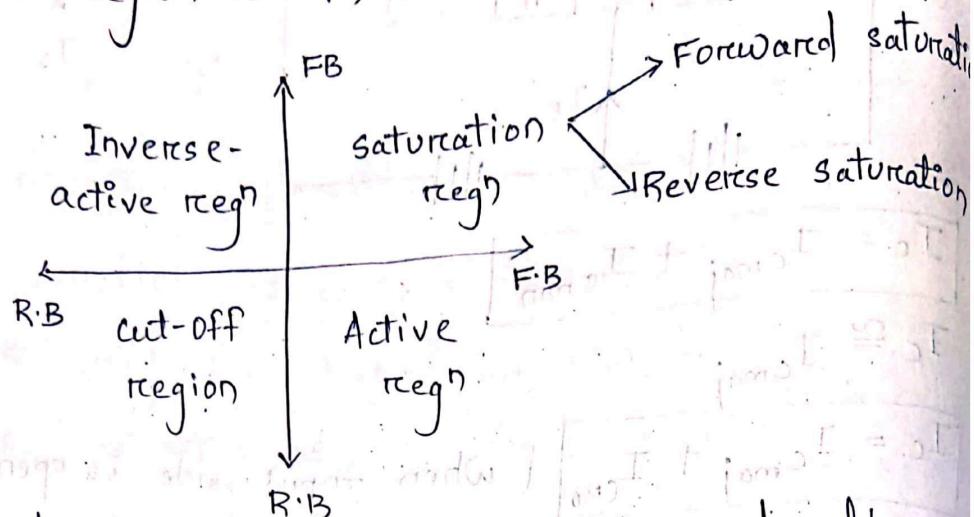


$I_E = \text{Constant}$
Output

Saturation region $\rightarrow J_i, J_o = F \cdot B$

Active region $\rightarrow J_i = F \cdot B, J_o = R \cdot B$

Cut-off region $\rightarrow J_i, J_o = R \cdot B$



Forward saturation ;

$$i_o = I_c \uparrow + I_c^o$$

when $V_{BE} > V_{BC}$

$$J_i > J_o$$

Reverse saturation ;

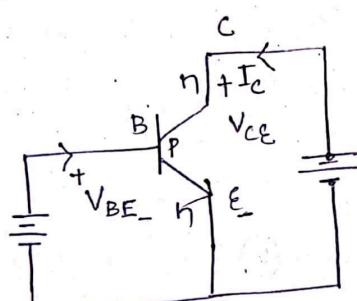
$$i_o = I_c \uparrow + I_c^o$$

when $V_{BC} > V_{BE}$

$$J_o > J_i$$

~~IMP~~

COMMON Emitter :



$$\beta = \frac{I_c}{I_B} = \frac{I_c/I_e}{I_e/I_e} = \frac{\alpha}{1-\alpha}$$

$$I_{cmaj} = \beta I_B$$

When β opens,

I_{CEO}

$$I_c = \beta I_B + I_{CEO}$$

$$\beta = \frac{\alpha}{1-\alpha}$$

$$\therefore \beta = \frac{\alpha}{1-\alpha}$$

$$\Rightarrow (\beta + 1) = \frac{\alpha}{1-\alpha} + 1$$

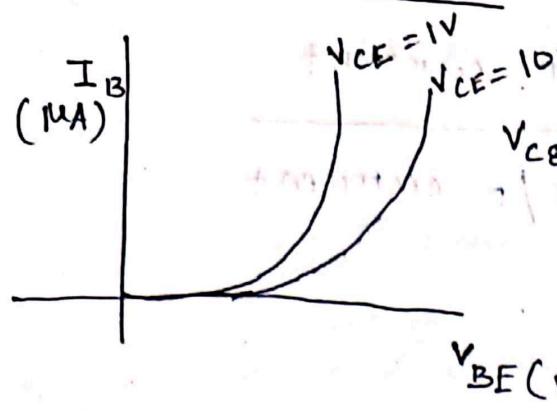
$$\Rightarrow (\beta + 1) = \frac{1}{1-\alpha}$$

$$\text{And } I_{CEO} = \frac{1}{1-\alpha} I_{CBO}$$

$$\Rightarrow I_{CEO} = (\beta + 1) I_{CBO}$$

Input V-I characteristic :-

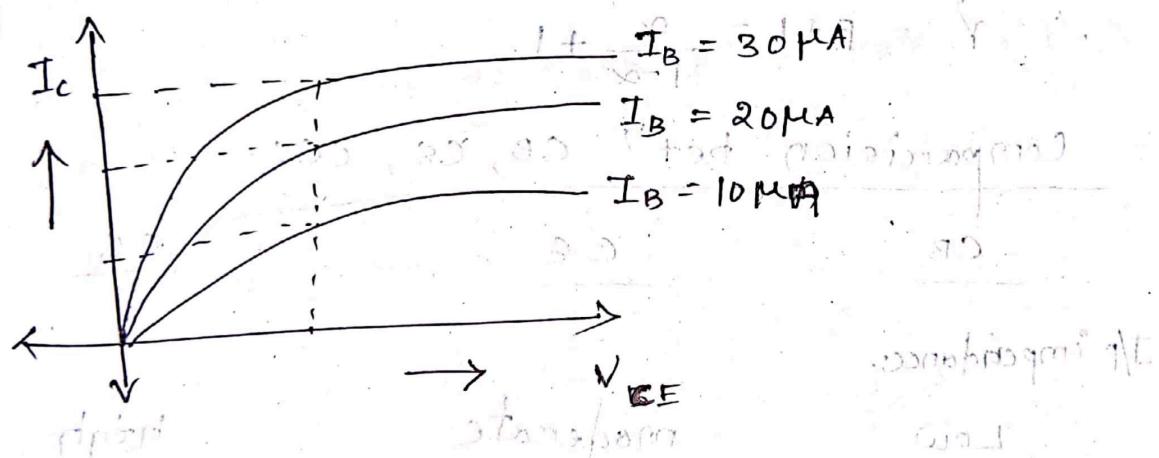
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- Input V-I characteristic curve is "curve bet"
- i/p not current I_B & o/p voltage V_{CE} constant.
- By increasing V_{CE} , V_{BE} is increasing.

Output V-I characteristic :-

- It is the characteristic betⁿ o/p voltage V_{CE} & output current I_C keeping input current I_B constant.



- By increasing I_B , I_C current is increasing also.
- In o/p characteristics, the bend shape is because of early effect.
- Common Collector (CC config):

Amplification factor:-

$$\gamma = \frac{\text{change in o/p current}}{\text{change in I/P current}}$$

$$= \frac{\Delta I_o}{\Delta I_B}$$

$$= \frac{\Delta (I_c + I_B)}{\Delta I_B}$$

$$= \frac{\Delta I_c + \Delta I_B}{\Delta I_B}$$

$$= \frac{\Delta I_c}{\Delta I_B} + 1$$

$$\Rightarrow \boxed{\gamma = \beta + 1}$$

$$\therefore \gamma = \beta + 1 = \frac{\alpha}{1-\alpha} + 1$$

Comparison b/w CB, CE, CC:-

CB

CE

CC

I/P impedance:-

Low

Moderate

High

O/P impedance:-

High

Moderate

Low

Voltage gain:-

High

Moderate

Low

Current gain:-

Low

Moderate

High

Power gain:-

No

Moderate

No

→ Out of this $\text{2}\beta\text{3}$ configuration, CE is mostly used. Because, CE has both voltage as well as current gain.

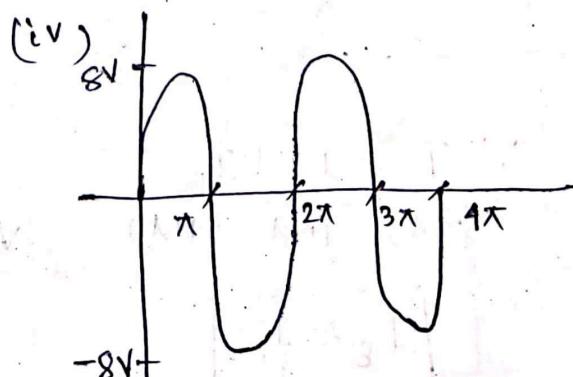
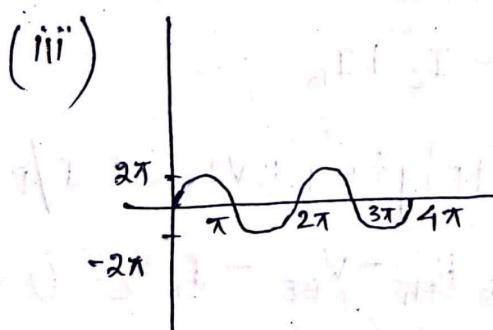
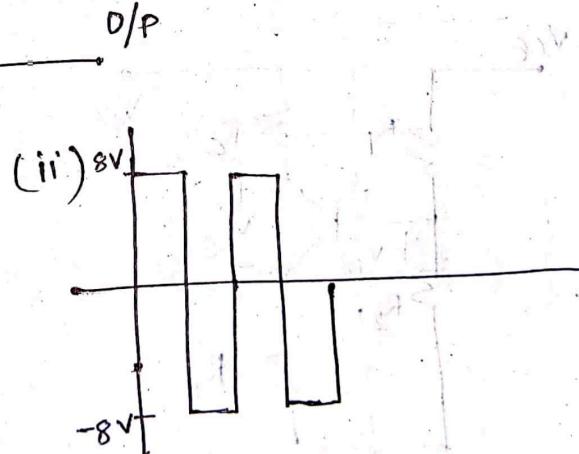
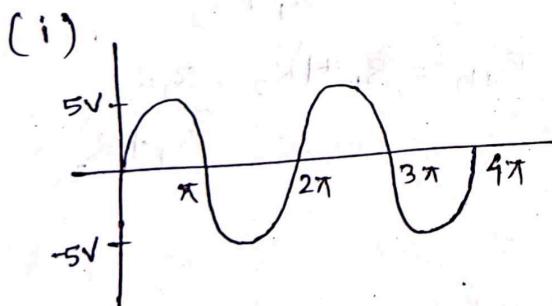
→ Any amplifier having both current & voltage gain. It is called power gain.

Region of Operation :-

- ① ~~On~~ Active (Amplifier) → Forward bias \rightarrow Reverse bias.
- ② Saturation (ON switch) → FB \rightarrow FB
- ③ Cut off (OFF switch) → RB \rightarrow FB
- ④ Inverse active (Attenuation) → RB \rightarrow FB

Biasing in BJT :-

→ Because, cond' is due to both e^- & poles. (both polarity).



For faithful amplification o/p of amplifier should be exact replica reproduction of i/p and input should be multiplied by factor, which is greater than 1. which is the output.

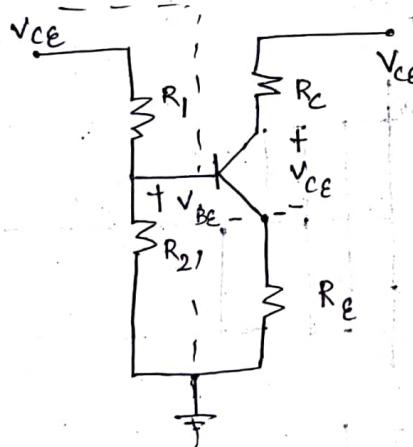
- Ex:- In 3rd case (iii) exact replica reproduction
- For faithful amplification, the operating point should be at the middle of active region.
 - For faithful amplification, I_c (dc) should be independent of B .

To achieve the above 2 condⁿ we need biasing.
There are 2 types of biasing.

(1) Fixed bias

(2) Voltage divider bias.

Voltage divider bias:-



$$V_{th} = \frac{V_{ce} R_2}{R_1 + R_2}$$

$$R_{th} = R_1 \parallel R_2 = \frac{R_1 R_2}{R_1 + R_2}$$

$$I_c = \beta I_B$$

$$I_E = I_c + I_B$$

So; Applying KVL at I/P loop

$$V_{th} - I_B R_{th} - V_{BE} - I_E R_E = 0 \rightarrow 0$$

$$I_E = I_c + I_B$$

$$= \beta I_B + I_B$$

$$= (\beta + 1) I_B$$

$$\Rightarrow I_B = \frac{I_E}{\beta + 1}$$

$$I_E \approx I_c$$

e.g. (1) can be written as:

$$V_{th} = \frac{I_E}{\beta+1} R_{th} - V_{BE} - I_E R_E = 0$$

$$\Rightarrow V_{th} - V_{BE} = I_E \left(R_E + \frac{R_{th}}{\beta+1} \right)$$

$$\Rightarrow I_E = \frac{V_{th} - V_{BE}}{R_E + \frac{R_{th}}{\beta+1}} \approx I_c$$

$$I_c = \frac{V_{cc} - V_{th}}{R_E + \frac{R_{th}}{\beta+1}}$$

$$R_E \gg \frac{R_{th}}{\beta+1}$$

$$I_c = \frac{V_{cc} - V_{th}}{R_E}$$

To make $I_c(\text{dc})$ independent of β ;

$$R_E \gg \frac{R_{th}}{\beta+1}$$

Applying KVL at output loop;

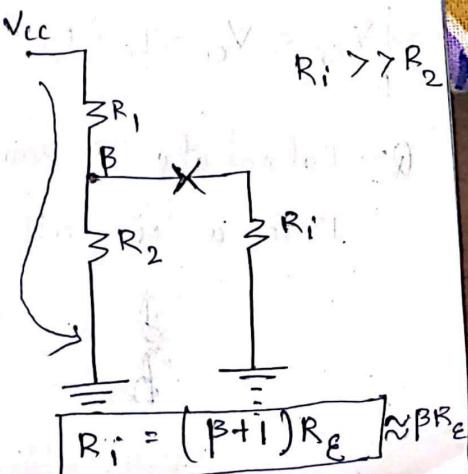
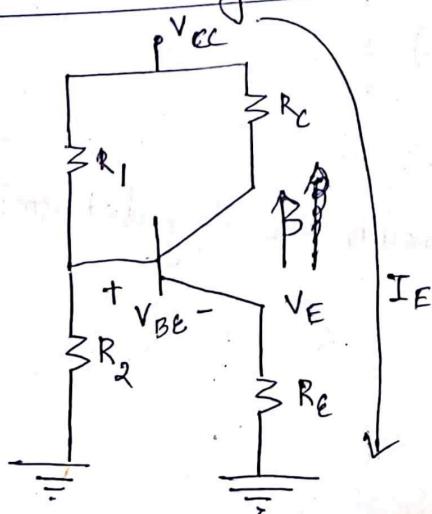
$$V_{cc} - I_c R_C - V_{ce} - I_E R_E = 0$$

$$V_{ce} = V_{cc} - I_c R_C - I_E R_E$$

$$= V_{cc} - I_c R_C - I_c R_E$$

$$V_{ce} = V_{ce} - I_c (R_C + R_E) = V_{ce0} = V_{ce(\text{dc})}$$

Approximate Analysis :-



For good accuracy;

$$R_i \gg 10R_2$$

$$\Rightarrow \beta R_E \gg 10R_2$$

$$\therefore V_B = \frac{V_{cc} R_2}{R_1 + R_2}$$

$$V_{BE} = V_B - V_E$$

$$V_E = V_B - V_{BE}$$

$$V_E = \frac{V_{cc} R_2}{R_1 + R_2} - V_{BE}$$

$$I_E = \frac{V_E - 0}{R_E} = \frac{V_{cc} R_2}{R_1 + R_2} - V_{BE}$$

$$I_E = I_C = \frac{V_{cc} R_2}{R_1 + R_2} - V_{BE}$$

$$I_E = I_C = \frac{V_{cc} R_2}{R_1 + R_2} - V_{BE}$$
$$= I_{CQ} = I_{C(d)} \quad (d.c)$$

Applying KVL at O/P :-

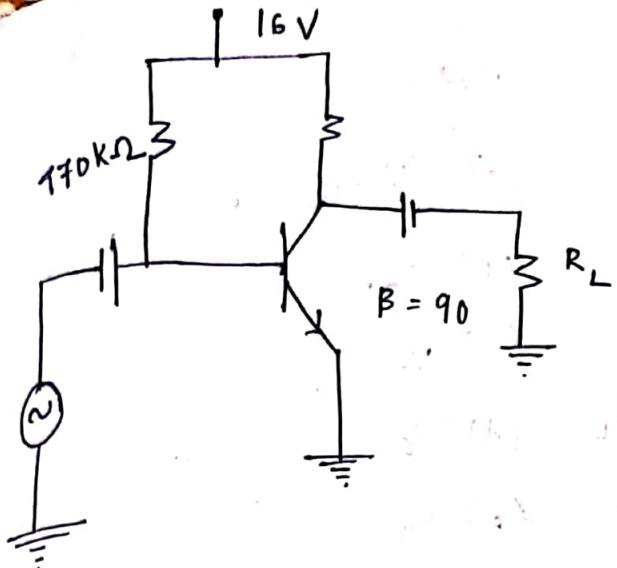
$$V_{cc} - I_C R_C - V_{CE} - I_E R_E = 0$$

$$\Rightarrow V_{CE} = V_{cc} - I_C (R_C + R_E)$$

Q:- Calculate Q point.

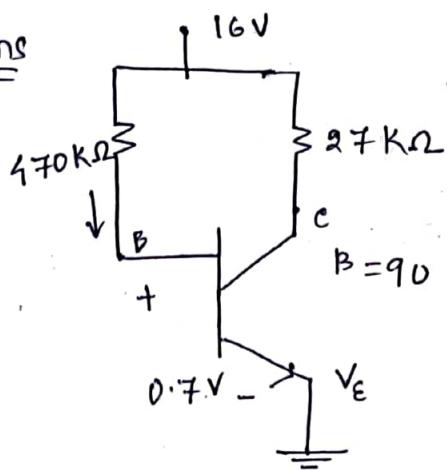
For a circuit shown in fig. determine.





Determine Q ($V_{CE}(dc)$, $I_C(dc)$), I_{BQ} , V_C , V_B , V_E ?

Ans



$$V_{BE} = 0.7V$$

$$\Rightarrow V_B - \cancel{V_E}^0 = 0.7V$$

$$\Rightarrow V_B = 0.7V$$

$$I_B = \frac{16 - 0.7}{470k} = \frac{15.3}{470} mA$$

$$\text{DC } 16 - 470k I_B - V_{BE} = 0$$

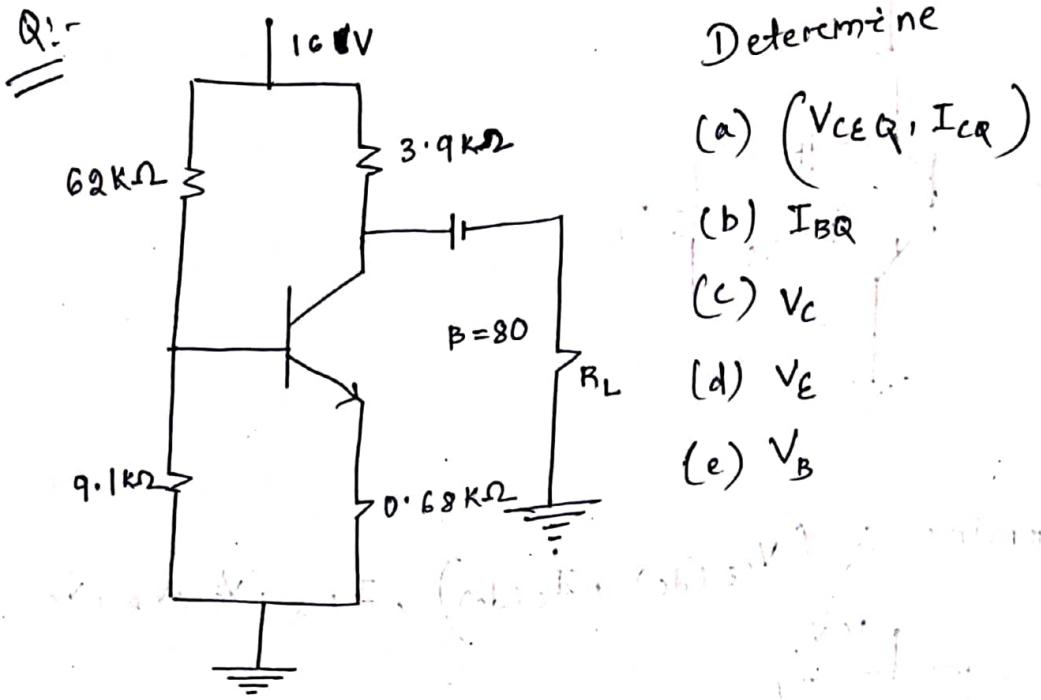
$$I_B = \dots$$

$$\boxed{I_C = \beta I_B}$$

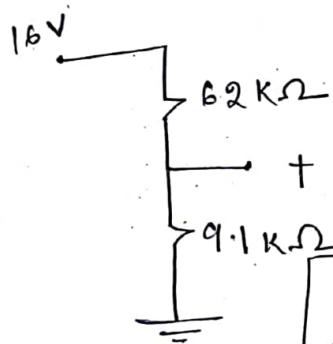
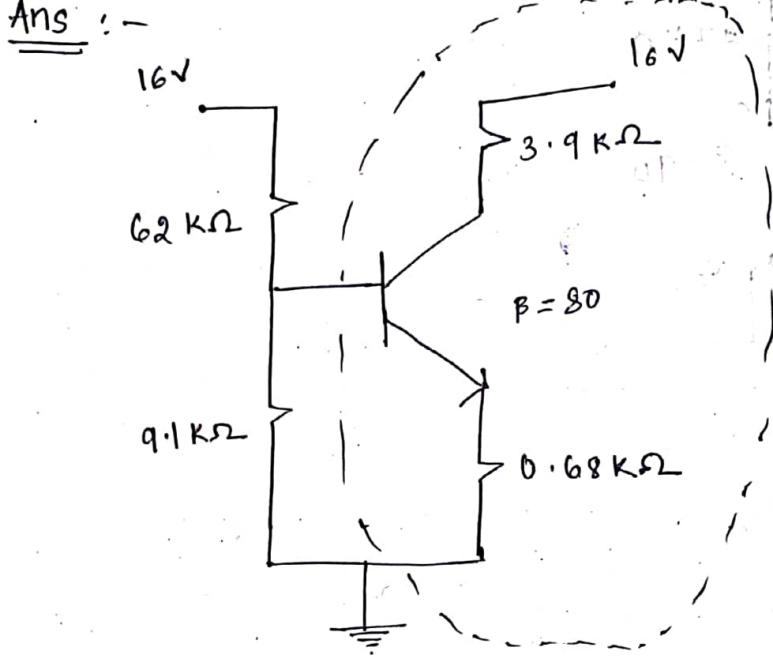
Apply KVL of O/P :-

$$16 - I_C \times 27k = V_{CE} - V_C = \cancel{0}^0$$

$$\Theta(V_{CE}, I_C)$$



Ans :-



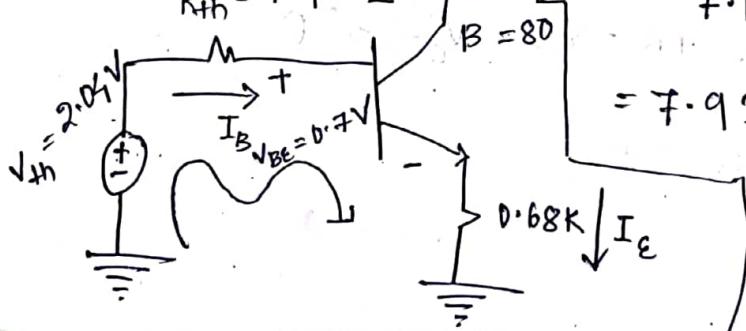
calculation of V_{th} :-

$$V_{th} = \frac{16 \times 9.1K}{62K + 9.1K} = 2.04V$$

Calculation of R_{th} :-

$$R_{th} = \frac{9.1K \times 62K}{7.1K + 62K}$$

$$= 7.936 K\Omega$$



$$2.09 - 7.9K I_B - 0.7 - I_E \times 0.68K = \frac{I_E}{\beta+1}$$

$$I_E \approx I_C$$

$$I_B = \frac{I_E}{\beta+1} \approx \frac{I_C}{\beta}$$

$$I_E = \frac{V_E - 0}{0.68K}$$

$$V_E = ?$$

$$V_{BE} = 0.7$$

$$\Rightarrow V_B - V_E = 0.7$$

$$\Rightarrow V_B = 0.7 + V_E$$

Applying KVL at O/P :-

$$16 - I_C \times 3.9K - V_{CE} - I_E \times 0.68K = 0$$

$$\cancel{V_{CE}} = ?$$

$$V_{CE} = V_C - V_E$$

$$V_C = V_{CE} + V_E$$

Q:- Determine α & β for $\alpha = 0.99$.

$$\alpha = 0.99$$

$$\beta = \frac{\alpha}{1-\alpha} = \frac{0.99}{1-0.99} = 99$$

$$\gamma = \beta + 1$$

$$= 99 + 1 = 100$$

Q:- Determine α & γ for $\beta = 99$.

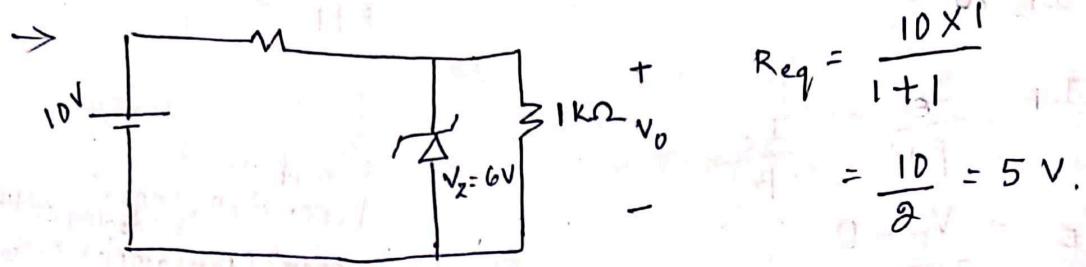
$$\text{Hence, } \gamma = 99 + 1 = 100$$

$$\alpha = \frac{\beta}{\beta+1} = \frac{99}{99+1}$$

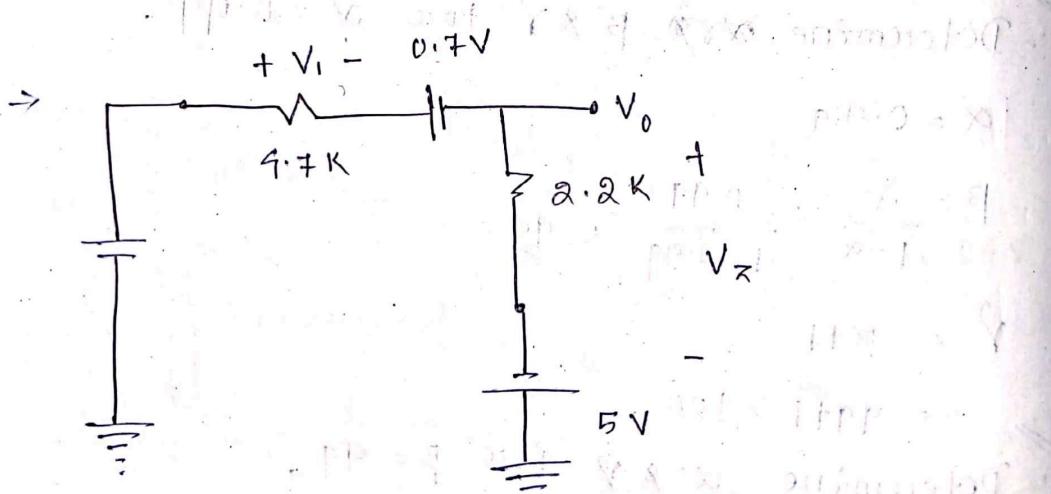
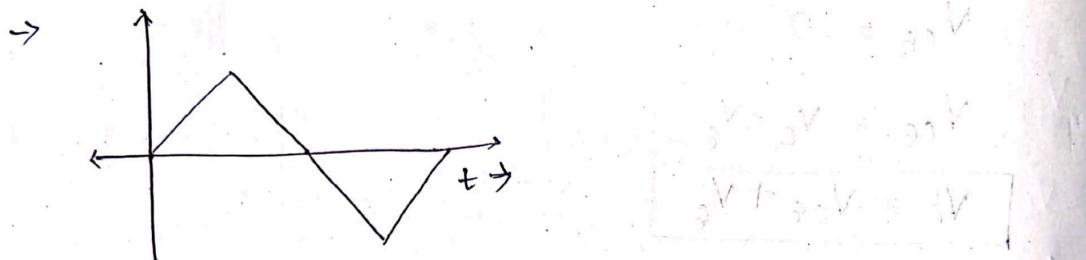
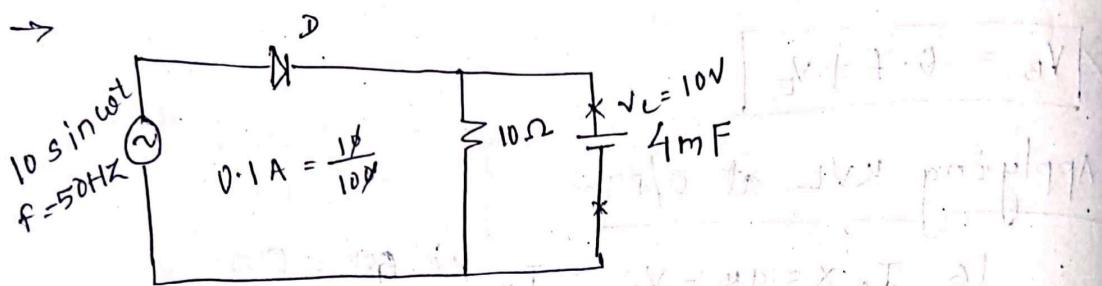
$$= \frac{99}{100} = 0.99$$

BJT :-
 operation, constⁿ (doping, width)
 operⁿ (for amplification)
 schematic diagram
 (npn & pnp)

Solution of mid term exam paper :-



- R_c is used to make the transistor safe.
- R_E is used to make $I_{c,\text{dc}}$ independent of β or used to produce -ve feedback.



$$10 - 4.7K I$$

(d)

