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	MC14001UBDR2G UB-suffix series CMOS gates: quad 2-input NOR Gate	CD40109BPWR CMOS quad low-to-high voltage level shifter (20V rating)	MC14011UBDG UB-suffix series CMOS gates: quad 2-input NAND Gate	CD4012BM96 CMOS dual 4-input NAND gate	MC14014BDG 8-Bit static shift register: synchronous parallel input/serial output 16-Lead SOIC, 3.9 mm 10
	CD4023BM96 CMOS triple 3-input NAND gate	CD4048BPW CMOS multifunction expandable 8-input gate	MC14585BDG 4-Bit magnitude comparator	M74HC133M1R 13-Input NAND gate	SN74HC157DR Quadruple 2-line to 1-line data selectors/multiplexers
	74HC175D Quad D-type flip-flop with reset; positive-edge trigger	74HC191D Presettable synchronous 4-bit binary up/down counter	CD74HC283ME4 High-speed CMOS logic 4-bit binary full adder with fast carry	CD74HC4067M96 High-speed CMOS logic 16-channel analog multiplexer/demultiplexer	MM74HCT00M Quad 2-input NAND gate
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	SN74LVC07AD Hex buffer and driver with open-drain outputs	SN74LVC07APWR Hex buffer and driver with open-drain outputs	SN74LVC1G00DBVR Single 2-input positive-NAND gate	SN74LVC1G14DBVR Single Schmitt-trigger inverter	SN74LVC1G17DBVR Single Schmitt-trigger buffer
	14-Lead SOIC, 3.9 mm 10	14-Lead TSSOP, 4.4 mm 10	5-Lead SOT-23 20	5-Lead SOT-23 20	5-Lead SOT-23 20
	SN74LVC1G32DBVR Single 2-input positive-OR gate 5-Lead SOT-23 20	SN74LVC1G86DBVR Single 2-input exclusive-OR gate	SN74LVC2G07DBVR Dual buffer and driver with open-drain outputs 6-Lead SOT-23 20	SN74LVC32AD Quadruple 2-input positive- OR gates	SN74LVC32APWR Quadruple 2-input positive- OR gates
_	SN74LVC4245APWR Octal bus transceiver and 3.3-V to 5-V shifter with 3-state outputs	74VHC00MTC Quad 2-input NAND gate	74VHC32MTCX Quad 2-input OR gate	74VHC595MTC 8-Bit shift register with output latches	74VHC86MTCX Quad 2-input exclusive-OR gate

14-Lead TSSOP, 4.4 mm

16-Lead TSSOP, 4.4 mm

14-Lead TSSOP, 4.4 mm

24-Lead TSSOP, 4.4 mm

14-Lead TSSOP, 4.4 mm

HEF4021BT	HEF4044BT	74HC109D	мм74НС132м
8-Bit static shift register;	Quadruple R/S latch with	Dual JK flip-flop with set and	Quad 2-input NAND Schmitt
asynchronous parallel data in-	3-state outputs	reset; positive-edge-trigger	trigger
put			
16-Lead SOIC, 3.9 mm 10	16-Lead SOIC, 3.9 mm 20	16-Lead SOIC, 3.9 mm 50	14-Lead SOIC, 3.9 mm 10
74HC257D	74HC27D	74HC30D	74HC4002D
Quad 2-input multiplexer;	Triple 3-input NOR gate	8-Input NAND gate	Dual 4-input NOR gate
3-state			
16-Lead SOIC, 3.9 mm 20	14-Lead SOIC, 3.9 mm 10	14-Lead SOIC, 3.9 mm 10	14-Lead SOIC, 3.9 mm 20
74HC4040D	CD74HC4046AM	74HC4050D	СD74НС4050м96
12-Stage binary ripple	High-speed CMOS logic	Hex non-inverting	High-speed CMOS logic hex
counter	phase-locked loop with VCO	HIGH-to-LOW level shifter	buffers, inverting and non-
			inverting
16-Lead SOIC, 3.9 mm 10	16-Lead SOIC, 3.9 mm 10	16-Lead SOIC, 3.9 mm 10	16-Lead SOIC, 3.9 mm 6
74HC4052D	74HC4053D	SN74HC4060DR	74HC4060D

14-Lead SOIC, 3.9 mm

plexer/demultiplexer

24-Lead SOIC, 7.5 mm

16-Lead SOIC. 3.9 mm C. 3.9 mm C4094D e shift-and-store bus OIC, 3.9 mm

16-Lead TSSOP, 4.4 mm

74HC173D

16-Lead SOIC, 3.9 mm

14-Lead SOIC, 5.3 mm

74HC4051D

20-Lead TSSOP, 4.4 mm

Quad D-type flip-flop;

TC74HC4024AF

7-Stage binary counter

8-Channel analog multi-

plexer/demultiplexer

positive-edge trigger; 3-state

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10 2000 0010; 015 111111	10 2000 0010, 0.5 11111	10 2000 0010, 013 11111	10 2000 0010, 013 111111	10 2000 0010, 0.5 11111
74HC4052D	74HC4053D	SN74HC4060DR	74HC4060D	74HC4094D
Dual 4-channel analog multi- plexer/demultiplexer	Triple 2-channel analog multi- plexer/demultiplexer	14-Stage asynchronous binary counter and oscillator	14-Stage binary ripple counter with oscillator	8-Stage shift-and-store bus register
16-Lead SOIC, 3.9 mm 40	16-Lead SOIC, 3.9 mm 30	16-Lead SOIC, 3.9 mm 5	16-Lead SOIC, 3.9 mm 10	16-Lead SOIC, 3.9 mm 10
74HC4514D	74HC4514D	M 74HC590 RM13TR	TC74HC7240AF	74HCT112 D
4-To-16 line de- coder/demultiplexer with in- put latches	Dual retriggerable precision monostable multivibrator	8-Bit binary counter register with 3-state output	Octal bus buffer (with Schmitt trigger inputs), in- verted, 3-state outputs	Dual JK flip-flop with set and reset; negative-edge trigger
24-Lead SOIC, 7.5 mm 10	16-Lead SOIC, 3.9 mm 10	16-Lead SOIC, 3.9 mm 10	20-Lead SOIC, 5.3 mm 10	16-Lead SOIC, 3.9 mm 10
ср74НСТ4067м	мм74НСТ74м	74LVC1G74DP	74VHC4040FT	TC74VHCT541AFT
High-speed CMOS logic 16-channel analog multi-	Dual D-type flip-flop with preset and clear	Single D-type flip-flop with set and reset; positive edge	12-Stage ripple carry binary counter	Octal bus buffer, non- inverted, 3-state outputs

trigger

8-Lead TSSOP, 3 mm