ECE 375 Lab 5

External Interrupts

Lab session: 015 Time: 12:00-13:50

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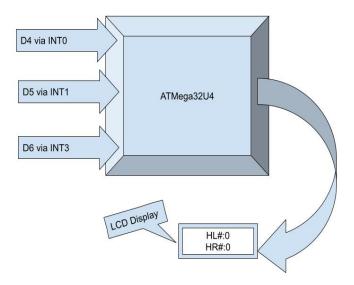
Programming partner: Lucas Plastid

1 Introduction

This is the Fifth lab in the ECE 375 series and it covers using hardware interrupts to preform predescribed "bump bot" operations. Additionally it incorporated use of the LCD Display to show the user how many times the bump bot had been triggered on its left or right side

2 Design

In this lab Lucas and I setup several different interrupt vectors that were able to trigger certain functions. These functions made the program function similarly to the Lab 1 and 2 bump bot script. Once these interrupts were created and working we moved to creating counters and displays for each of the buttons pressed. In the image seen below, one can see an example of what the LCD display would look like upon boot up.



3 Assembly Overview

As for the Assembly program an overview can be seen below.

3.1 Internal Register Definitions and Constants

The multipurpose register was setup as r16. At r0 and r1 any multiplication output will be set, such that the outputs of any multiplication operation are automatically assigned to them. A default zero register is set to r2. Two other generic variable registers are defined as r3 and r4. Finally two registers named oloop and iloop are used for counting within the assembly itself.

3.2 Interrupt Vectors

Vectors setup are; Hit right on interrupt 0, Hit left on interrupt 1, and Clear counters on interrupt 3.

3.3 Initialization Routine

Firstly the stack pointer is initialized

3.4 Main Routine

3.5 Subroutines

3.6 ClearCounters

This subroutine adds two 16 bit numbers together. It does this by taking each X Y and Z registers and setting them to operator 1 operator 2 and the location to save the result respectively. Next, using the A variable register as an intermediary the two inputs were added together and saved into the Z register location. Finally if the carry flag is set at the end of the whole operation then we can assume the most significant bit needs to be set to 1, so it is done.

3.7 toLCD

This subroutine takes two different inputs and subtracts the first from the second. The X Y and Z registers are initialized at the beginning of this subroutine first, the same way that they are in the add subroutine. In the actual operation part of this subroutine A and B registers are loaded with the lower values of X and Y and subtracted from each other. This is then preformed again to account for the second word. Due to the fact that we do not need to worry about signage these are each saved directly to the location pointed to by Z

3.7.1 HitRight

This subroutine takes 4 different data locations and multiplies 24 bits by 24 bits, resulting in a 48 bit number. It must be built differently to the MUL16 operation. Every time addition occurs we need to check for the carry bit and pass it forward if necessary. This will continue until there is no carry bit to pass upward. In reality this can only ever happen up to 4 times. In this subroutine the first operand is loaded into the Z pointer. Then the second operand is loaded into the Y pointer, finally the result is loaded into the X register. For each of these, they load the start of each because they will increment throughout the method. The data in Y an Z are multiplied and the result is stored in r0 and r1. ADDMUL2x is then called. This fixes the carry bit problem of multiplying by 24 bits and as long as we call ADDMUL2x after our multiplication then everything will work out.

3.7.2 HitLeft

This subroutine adds a partial multiplication result to the location x is pointing to. This presumes that x is already pointing to the location where the low result of the current multiplication needs to go. Essentially it takes the multiplication outputs and cycles the carry bit up until it cannot anymore. It utilized a loop moving the carry byte in and out of X when necessary.

3.7.3 Wait

Preforms the operation $((G-H)+I)^2$ Using multiplication, addition and subtraction.

4 Testing

Tested Each input value and compared to external calculations.

Case	Expected	Actual meet expected
\$FCBA + \$FFFF	\$01FCB9	✓
FCB9 - E420	\$1899	✓
\$00FFFFFFF * \$00FFFFFFF	\$FFFFE000001	✓
$((\$FCBA - \$2022) + \$21BB)^2$	\$FCA8CEE9	✓

Table 1: Assembly Testing Cases

5 Study Questions

- 1. As this lab, Lab 1, and Lab 2 have demonstrated, there are always multiple ways to accomplish the same task when programming (this is especially true for assembly programming). As an engineer, you will need to be able to justify your design choices. You have now seen the BumpBot behavior implemented using two different programming languages (AVR assembly and C), and also using two different methods of receiving external input (polling and interrupts). Explain the benefits and costs of each of these approaches. Some important areas of interest include, but are not limited to: efficiency, speed, cost of context switching, programming time, understandability, etc.
- 2. Instead of using the Wait function that was provided in BasicBumpBot.asm, is it possible to use a timer/counter interrupt to perform the one-second delays that are a part of the BumpBot behavior, while still using external interrupts for the bumpers? Give a reasonable argument either way, and be sure to mention if interrupt priority had any effect on your answer.
- 3. List the correct sequence of AVR assembly instructions needed to store the contents of registers R25:R24 into Timer/Counter1's 16-bit register, TCNT1. (You may assume that registers R25:R24 have already been initialized to contain some 16-bit value.

(because its an IO location) out TCNT1H r25 out TCNT1L r24

- 4. List the correct sequence of AVR assembly instructions needed to load the contents of Timer/Counter1's 16-bit register, TCNT1, into registers R25:R24
 - in r25 TCNT1H in r24 TNCT1L
- 5. Suppose Timer/Counter0 (an 8-bit timer) has been configured to operate in Normal mode, and with no prescaling (i.e., clkT 0 = clkI/O = 8 MHz). The decimal value "128" has just been written into Timer/Counter0's 8-bit register, TCNT0. How long will it take for the TOV0 flag to become set? Give your answer as an amount of time, not as a number of cycles

6 Difficulties

This lab was more challenging than the last, however it did not require us to learn anything outside of lecture. This is a good thing, due to the fact that we re only expected to know exactly what we are taught.

7 Conclusion

This lab cemented the ideas of logical operands and allowed the student to understand how computers operate with large numbers, especially larger numbers than they might be able to handle naively. Additionally the pencil and paper method described in the handout was not how I was taught how to do multiplication, so the solution may be more or less difficult depending on the students type of education.

8 Source Code

Listing 1: Assembly Bump Bot Script

```
: *********************
      This is the skeleton file for Lab 5 of ECE 375
2
3
  ;*
      Author: Astrid Delestine & Lucas Plaisted
4
  :*
         Date: 2/23/2023
5
6
  ;*
7
  8
  .include "m32U4def.inc"; Include definition file
9
10
  **********************
11
  :* Variable and Constant Declarations
13
  ; **********************
                                ; Multi-Purpose Register
14
  .def
          mpr = r16
15
  .def
          waitcnt = r17
                                   ; Wait Loop Counter
                                ; Inner Loop Counter
  .def
16
          ilcnt = r18
  .def
17
          olcnt = r19
                               ; Outer Loop Counter
  .def
                               ; Hit Left Counter
18
          hlcnt = r15
19 .def
                               ; Hit Right Counter
          hrcnt = r14
                                ; needed for LCD binToASCII
20
  ; def
          count = r20
21
22
          WTime = 50
                               ; Time to wait in wait loop
  .equ
23
          WskrR = 4
                               ; Right Whisker Input Bit
24
  .equ
          WskrL = 5
                               ; Left Whisker Input Bit
25
  .equ
                               ; Right Engine Enable Bit
26
          EngEnR = 5
  .equ
                               ; Left Engine Enable Bit
27
          EngEnL = 6
  .equ
          EngDirR = 4
                               ; Right Engine Direction Bit
28
  .equ
29
                                ; Left Engine Direction Bit
          EngDirL = 7
  .equ
30
  ; //TAKEN FROM LAB3
31
32
33
          lcdL1 = 0x00
                      ; Make LCD Data Memory locations constants
  .equ
34
  .equ
          lcdH1 = 0x01
35
  .equ
          lcdL2 = 0x10
                        ; lcdL1 means the low part of line 1's location
36
          lcdH2 = 0x01; lcdH2 means the high part of line 2's location
  .equ
          lcdENDH = 0x01 ; as it sounds, the last space in data mem
37
  .equ
          lcdENDL = 0x1F
38
                            ; for storing lcd text
  .equ
39
40
  ;//END TAKEN FROM LAB3
41
42
  .equ strSize = 4;
43
```

```
44
45
 : These macros are the values to make the TekBot Move.
46
  47
48
       MovFwd = (1 << EngDirR | 1 << EngDirL) ; Move Forward Command
49
 .equ
       50 .equ
                              ; Turn Right Command
51
 .equ
       TurnL = (1 << EngDirR)
                             ; Turn Left Command
52 .equ
       Halt = (1 << EngEnR | 1 << EngEnL)
53 .equ
                                 ; Halt Command
54
55
  ;* Start of Code Segment
56
  **********************
                         ; Beginning of code segment
58
  .cseg
59
60 :********************************
 ;* Interrupt Vectors
62
  63
       $0000
                         ; Beginning of IVs
  .org
64
       rimp
             INIT
                         ; Reset interrupt
65
66
        ; Set up interrupt vectors for any interrupts being used
67
68
69
        ; This is just an example:
                        ; Analog Comparator IV
        $002E
70
  ; . org
            HandleAC
71
        rcall
                        ; Call function to handle interrupt
72
        reti
                        ; Return from interrupt
        $0002 ; INTO
73
  .org
                       ; RIGHT WHISKER
74
        rcall
             HitRight
75
        reti
        $0004 : INT1
76
 .org
77
        rcall
            HitLeft ;LEFT WHISKER
        reti
78
        $0006 ; INT2
79 \quad ; org
       $0008 ; INT3
80
 .org
81
        rcall ClearCounters
                         ; CLEAR COUNTERS
82
        reti
83
 ; .org
       \$000E ; INT6
84
85
       $0056
                        ; End of Interrupt Vectors
  .org
86
 87
88
  ;* Program Initialization
89
```

```
90
   INIT:
91
        ; Initialize the Stack Pointer (VERY IMPORTANT!!!!)
           ldi
92
                   mpr, low (RAMEND)
93
           out
                   SPL, mpr
                                   ; Load SPL with low byte of RAMEND
94
           ldi
                   mpr, high (RAMEND)
                                   ; Load SPH with high byte of RAMEND
                   SPH, mpr
95
           out
96
97
        ; Initialize Port B for output
                   mpr, $FF
98
           ldi
                                   ; Set Port B Data Direction Register
                   DDRB, mpr
99
           out
                                   ; for output
100
           ldi
                   mpr, $00
                                   ; Initialize Port B Data Register
                                  ; so all Port B outputs are low
101
           out
                   PORTB, mpr
102
        ; Initialize Port D for input
103
           ldi
                   mpr, $00
                                   ; Set Port D Data Direction Register
104
                   DDRD, mpr
105
                                   ; for input
           out
           ldi
                   mpr, $FF
                                   ; Initialize Port D Data Register
106
                                  ; so all Port D inputs are Tri-State
107
           out
                   PORTD, mpr
108
109
110
        : init the LCD
111
           rcall LCDInit
112
113
           rcall LCDBacklightOn
114
           rcall LCDClr
115
           rcall toLCD
116
117
           rcall ClearCounters
118
119
120
        ; Initialize external interrupts
121
           ; Set the Interrupt Sense Control to falling edge
122
           ldi mpr, 0b10001010
123
           sts EICRA, mpr;
124
125
        ; Configure the External Interrupt Mask
126
           ldi mpr, 0b0000_-1011; x0xx_-0000; all\ disabled
127
           out EIMSK, mpr;
        ; Turn on interrupts
128
129
            ; NOTE: This must be the last thing to do in the INIT function
130
        sei; Turn on interrupts
131
132
   ************************
133
       Main Program
134
   135 MAIN:
                                   ; The Main program
```

```
136
137
           ldi
                   mpr, MovFwd
                                  ; Load Move Forward Command
138
           out
                   PORTB, mpr
139
140
           rjmp
                   MAIN
                                   ; Create an infinite while loop to
141
                                   ; signify the end of the program.
142
143
    144
       Functions and Subroutines
   146
147
148
       You will probably want several functions, one to handle the
       left whisker interrupt, one to handle the right whisker
149
       interrupt, and maybe a wait function
150
151
152
153
154
    ; Func: Template function header
155
    : Desc: Cut and paste this and fill in the info at the
           beginning of your functions
156
157
    ClearCounters:
                                          ; Begin a function with a label
158
159
160
           ; Save variable by pushing them to the stack
161
           ; Execute the function here
162
163
           clr
                   hrcnt
                                  ; sets hlent and hrent to zero by
164
           clr
                   hlcnt
                                   ; doing an xor operation with itself
165
166
           push ZL
                              ; Save vars to stack
167
           push ZH
           push XL
168
           push XH
169
170
           push mpr
171
           push ilent
172
173
           ldi
                ZL , low(STRING_BEG<<1) ; Sets ZL to the low bits
174
                                   ; of the first string location
                ZH , high (STRING_BEG<<1) ; Sets ZH to the first
175
           ldi
                                  ; of the first string location
176
                XH , lcdH1
177
           ldi
178
           ldi
                XL , lcdL1
179
           ldi
                ilcnt, 16
180
181
   CCl1: ; While ilcnt != zero 1
```

```
182
             lpm mpr, Z+
183
              \mathbf{st}
                   X+ , mpr
184
                   ilcnt
              \operatorname{dec}
185
              brne CCl1
186
              ldi ZL, low(STRING2_BEG<<1)
187
188
              ldi ZH, high(STRING2_BEG<<1)
              ; z \ is \ already \ pointing \ at \ the \ second
189
              ; string due to how memory is stored
190
191
                   XH, lcdH2
192
              ldi
                   XL, lcdL2
              ldi
                   ilent, 16
193
194
    CCl2: ; While ilcnt != zero 2
195
196
             lpm mpr, Z+
                   X+ , mpr
197
              \mathbf{st}
198
              dec
                  ilcnt
199
              brne CCl2
200
201
              rcall LCDWrite
202
203
             pop ilent
204
             pop mpr
205
             pop XH
206
             pop XL
207
             pop ZH
208
             pop ZL
                                          ; Pop vars off of stack
              ; Restore variable by popping them from the stack
209
              ; in reverse order
210
211
212
             \mathbf{ret}
                                          ; End a function with RET
213
214
215
216
    ; Func: toLCD
217
    ; Desc: Takes various info and pushes it to the LCD
218
              *HL#:0
219
              *HR#:0
220
221
    toLCD:
222
             push ZL
                                     : Save vars to stack
223
             push ZH
224
             push XL
225
             push XH
226
             push mpr
227
             push ilcnt
```

```
228
229
             ; Sets ZL to the low bits of the first string location
230
                  ZL , low(STRING_BEG<<1)
             ldi
231
             ldi
                  ZH , high (STRING_BEG<<1)
             ; points to the data location where LCD draws from
232
233
                  XH , lcdH1
                  XL, lcdL1
234
             ldi
235
             ldi
                  ilent, 4
236
237
    Line1Loop: ; While ilcnt != zero
238
             lpm
                  mpr, Z+
                  X+ , mpr
239
             \mathbf{st}
240
             dec ilcnt
241
             brne Line1Loop
242
             //end loop
243
244
            mov mpr, hlent; copies the counter to mpr
245
246
             rcall Bin2ASCII
247
             ; Takes a value in MPR and outputs
248
             ; the ascii equivilant to XH:XL
             ; convineintly X is currently pointing where
249
250
             ; I would like this number to go
251
252
253
             ldi ZL, low(STRING2_BEG<<1)
254
             ldi ZH, high (STRING2_BEG<<1)
255
                  XH, lcdH2
256
             ldi
257
             ldi
                  XL, lcdL2
258
             ldi
                  ilent, 4
259
    Line2Loop: ; While ilcnt != zero 2
260
261
             lpm mpr, Z+
                  X+ , mpr
262
             \mathbf{st}
             dec ilcnt
263
264
             brne Line2Loop
265
266
            mov mpr, hrent;
267
             rcall Bin2ASCII
268
269
270
             rcall LCDWrite
271
272
273
```

```
274
275
276
277
            pop ilent
278
            pop mpr
279
            pop XH
280
            pop XL
281
            pop ZH
282
            pop ZL
                                      ; Pop vars off of stack
283
284
285
            ret
286
    ; Sub:
287
            HitRight
288
    ; Desc: Handles functionality of the TekBot when the right whisker
289
             is triggered.
290
291
    HitRight:
292
            push
                     mpr
                                  ; Save mpr register
293
                                     ; Save wait register
            push
                     waitcnt
                                  ; Save program state
294
            in
                     mpr, SREG
295
            push
                     mpr
296
297
             ; Move Backwards for a second
298
             ldi
                     mpr, MovBck; Load Move Backward command
299
                     PORTB, mpr ; Send command to port
            out
                     waitcnt, (WTime<<1); Shifted bit back by 1,
300
             ldi
301
                                           ; making the wait time two seconds
302
             rcall
                     Wait
                                      ; Call wait function
303
304
             ; Turn left for a second
305
             ldi
                     mpr, TurnL ; Load Turn Left Command
                     PORTB, mpr ; Send command to port
306
            out
307
             ldi
                     waitent, WTime; Wait for 1 second
308
             rcall
                     Wait
                                      ; Call wait function
309
310
             ; Move Forward again
311
             ldi
                     mpr, MovFwd; Load Move Forward command
312
                     PORTB, mpr ; Send command to port
            out
313
314
                             ; Restore program state
            pop
                     mpr
315
                     SREG, mpr
            out
316
                     waitcnt
                                  ; Restore wait register
            pop
317
                     mpr ; Restore mpr
            pop
318
319
            inc
                     hrcnt;
```

```
320
             rcall toLCD;
321
             ; fix debounce
322
            ldi mpr , 0b0000\_0011
323
            out EIFR, mpr
                              ; Return from subroutine
324
            \mathbf{ret}
325
326
    : Sub:
327
             HitLeft
328
    ; Desc: Handles functionality of the TekBot when the left whisker
329
             is triggered.
330
331
    HitLeft:
332
            push
                                  ; Save mpr register
                     mpr
333
                                     ; Save wait register
            push
                     waitcnt
334
            in
                     mpr, SREG
                                  ; Save program state
335
            push
                     mpr
336
337
             ; Move Backwards for a second
                     mpr, MovBck; Load Move Backward command
338
             ldi
339
                     PORTB, mpr ; Send command to port
            out
                     waitcnt, (WTime<<1); Wait for 1 second
340
            ldi
341
             rcall
                     Wait
                                  ; Call wait function
342
343
             ; Turn right for a second
344
             ldi
                     mpr, TurnR ; Load Turn Left Command
345
                     PORTB, mpr ; Send command to port
            out
                     waitent, WTime ; Wait for 1 second
             ldi
346
                     Wait
347
             rcall
                                      ; Call wait function
348
349
             ; Move Forward again
                     mpr, MovFwd; Load Move Forward command
350
             ldi
351
            out
                     PORTB, mpr ; Send command to port
352
353
                           ; Restore program state
            pop
                     mpr
354
                     SREG, mpr
            out
355
                     waitcnt
                                 ; Restore wait register
            pop
356
                            ; Restore mpr
            pop
                     mpr
357
358
            inc
                     hlcnt
359
             rcall
                     toLCD;
                     ; fix debounce
360
361
             1di mpr , 0b0000_{-}0011
362
            out EIFR, mpr
363
                             ; Return from subroutine
            \mathbf{ret}
364
365
```

```
: Sub:
366
            Wait
367
    ; Desc: A wait loop that is 16 + 159975*waitcnt cycles or roughly
                          Just initialize wait for the specific amount
368
            waitcnt*10ms.
            of time in 10ms intervals. Here is the general equation
369
370
            for the number of clock cycles in the wait loop:
                (((((3*ilcnt)-1+4)*olcnt)-1+4)*waitcnt)-1+16
371
372
373
    Wait:
374
            push
                    waitcnt
                                    ; Save wait register
375
            push
                    ilcnt
                                    ; Save ilent register
376
            push
                    olcnt
                                    ; Save olcnt register
377
378 Loop:
            ldi
                    olcnt, 224
                                    ; load olent register
                                    ; load ilcnt register
            ldi
379 OLoop:
                    ilcnt, 237
380 ILoop:
            \mathbf{dec}
                    ilcnt
                                    ; decrement ilcnt
                                    : Continue Inner Loop
381
            brne
                    ILoop
382
            dec
                    olcnt
                                ; decrement olcnt
383
            brne
                    OLoop
                                    ; Continue Outer Loop
                                ; Decrement wait
384
            dec
                    waitcnt
385
                                    : Continue Wait loop
            brne
                    Loop
386
                                ; Restore olcnt register
387
                    olcnt
            pop
                                ; Restore ilcnt register
388
                    ilcnt
            pop
389
                                ; Restore wait register
            pop
                    waitcnt
390
            ret
                            ; Return from subroutine
391
392
393
394
395
    ; Func: Template function header
396
    ; Desc: Cut and paste this and fill in the info at the
397
            beginning of your functions
398
399
   FUNC:
                                    ; Begin a function with a label
400
401
            ; Save variable by pushing them to the stack
402
403
            ; Execute the function here
404
405
            ; Restore variable by popping them from the stack in reverse order
406
407
                                    ; End a function with RET
            ret
408
409
    ************************
410
        Stored Program Data
    411
```

```
412
413 ; Enter any stored data you might need here
414 ; . org
415 STRING_BEG:
        "HL#:0...."; Declaring data in ProgMem
416 .DB
417 STRING2_BEG:
       "HR#:0----"
418 .DB
419 STRING_END:
420
421
422
  ; ********************
|423; * Additional Program Includes
425 include "LCDDriver.asm" ; Include the LCD Driver
```