ECE 375 Lab 5

External Interrupts

Lab session: 015 Time: 12:00-13:50

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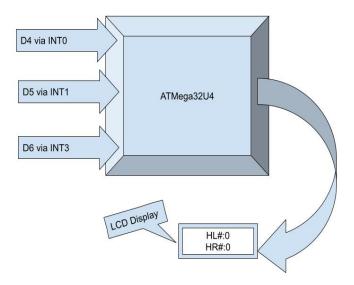
Programming partner: Lucas Plastid

1 Introduction

This is the Fifth lab in the ECE 375 series and it covers using hardware interrupts to preform predescribed "bump bot" operations. Additionally it incorporated use of the LCD Display to show the user how many times the bump bot had been triggered on its left or right side

2 Design

In this lab Lucas and I setup several different interrupt vectors that were able to trigger certain functions. These functions made the program function similarly to the Lab 1 and 2 bump bot script. Once these interrupts were created and working we moved to creating counters and displays for each of the buttons pressed. In the image seen below, one can see an example of what the LCD display would look like upon boot up.



3 Assembly Overview

As for the Assembly program an overview can be seen below.

3.1 Internal Register Definitions and Constants

The multipurpose register was setup as r16. At r0 and r1 any multiplication output will be set, such that the outputs of any multiplication operation are automatically assigned to them. A default zero register is set to r2. Two other generic variable registers are defined as r3 and r4. Finally two registers named oloop and iloop are used for counting within the assembly itself.

3.2 Interrupt Vectors

Vectors setup are; hit right on interrupt 0, hit left on interrupt 1, and clear counters on interrupt 3.

3.3 Initialization Routine

Firstly the stack pointer is initialized then ports B and D are initialized for output and input respectively. The LCD is then initialized in its own subroutines as we set it to turn its backlight on and clear any remaining text on the screen. Then we set it such that it displays clear delimiters for each of our button presses. Next we load up the interrupt control for falling edge detection, and configure the interrupt mask for just the 3 interrupts we had setup earlier. Finally we run the sei command to set the interrupt flag in SREG so that the interrupts can work at all.

3.4 Main Routine

The main routine is very simple due to the fact that most operations are handled outside of the main routine by interrupts.

3.5 Subroutines

3.6 ClearCounters

This subroutine clears the counters for each button press, then clears the LCD of any overflowing numbers, and resets it back to its initial state. This is done by loading all 16 characters into the data memory that the display looks at for its characters.

$3.7 ext{ toLCD}$

The toLCD subroutine is quite simple with regards to what we have already completed. It sets the first four bits of each row to the characters in data memory then uses the built in Bin2ASCII command to take the mpr register and print it to the LCD display. It then enables the LCD to write the characters to the screen.

3.7.1 HitRight

This subroutine takes 4 different data locations and multiplies 24 bits by 24 bits, resulting in a 48 bit number. It must be built differently to the MUL16 operation. Every time addition occurs we need to check for the carry bit and pass it forward if necessary. This will continue until there is no carry bit to pass upward. In reality this can only ever happen up to 4 times. In this subroutine the first operand is loaded into the Z pointer. Then the second operand is loaded into the Y pointer, finally the result is loaded into the X register. For each of these, they load the start of each because they will increment throughout the method. The data in Y an Z are multiplied and the result is stored in r0 and r1. ADDMUL2x is then called. This fixes the carry bit problem of multiplying by 24 bits and as long as we call ADDMUL2x after our multiplication then everything will work out.

3.7.2 HitLeft

This subroutine adds a partial multiplication result to the location x is pointing to. This presumes that x is already pointing to the location where the low result of the current multiplication needs to go. Essentially it takes the multiplication outputs and cycles the carry bit up until it cannot anymore. It utilized a loop moving the carry byte in and out of X when necessary.

3.7.3 Wait

Preforms the operation $((G-H)+I)^2$ Using multiplication, addition and subtraction.

4 Testing

Tested Each input value and compared to external calculations.

| Case | Expected | Actual meet expected |
|----------------------------------|---------------|----------------------|
| \$FCBA + \$FFFF | \$01FCB9 | ✓ |
| \$FCB9 - \$E420 | \$1899 | ✓ |
| \$00FFFFFFF * \$00FFFFFFF | \$FFFFE000001 | ✓ |
| $((\$FCBA - \$2022) + \$21BB)^2$ | \$FCA8CEE9 | ✓ |

Table 1: Assembly Testing Cases

5 Study Questions

- 1. As this lab, Lab 1, and Lab 2 have demonstrated, there are always multiple ways to accomplish the same task when programming (this is especially true for assembly programming). As an engineer, you will need to be able to justify your design choices. You have now seen the BumpBot behavior implemented using two different programming languages (AVR assembly and C), and also using two different methods of receiving external input (polling and interrupts). Explain the benefits and costs of each of these approaches. Some important areas of interest include, but are not limited to: efficiency, speed, cost of context switching, programming time, understandability, etc.
- 2. Instead of using the Wait function that was provided in BasicBumpBot.asm, is it possible to use a timer/counter interrupt to perform the one-second delays that are a part of the BumpBot behavior, while still using external interrupts for the bumpers? Give a reasonable argument either way, and be sure to mention if interrupt priority had any effect on your answer.
- 3. List the correct sequence of AVR assembly instructions needed to store the contents of registers R25:R24 into Timer/Counter1's 16-bit register, TCNT1. (You may assume that registers R25:R24 have already been initialized to contain some 16-bit value.

(because its an IO location) out TCNT1H r25 out TCNT1L r24

- 4. List the correct sequence of AVR assembly instructions needed to load the contents of Timer/-Counter1's 16-bit register, TCNT1, into registers R25:R24 in r25 TCNT1H in r24 TNCT1L
- 5. Suppose Timer/Counter0 (an 8-bit timer) has been configured to operate in Normal mode, and with no prescaling (i.e., clkT 0 = clkI/O = 8 MHz). The decimal value "128" has just been written into Timer/Counter0's 8-bit register, TCNT0. How long will it take for the TOV0 flag to become set? Give your answer as an amount of time, not as a number of cycles it will take 16 microseconds

6 Difficulties

This lab was more challenging than the last, however it did not require us to learn anything outside of lecture. This is a good thing, due to the fact that we re only expected to know exactly what we are taught.

7 Conclusion

This lab cemented the ideas of logical operands and allowed the student to understand how computers operate with large numbers, especially larger numbers than they might be able to handle naively. Additionally the pencil and paper method described in the handout was not how I was taught how to do multiplication, so the solution may be more or less difficult depending on the students type of education.

8 Source Code

Listing 1: Assembly Bump Bot Script

```
: *********************
      This is the skeleton file for Lab 5 of ECE 375
2
3
  ;*
      Author: Astrid Delestine & Lucas Plaisted
4
  :*
         Date: 2/23/2023
5
6
  ;*
7
  8
  .include "m32U4def.inc"; Include definition file
9
10
  **********************
11
  :* Variable and Constant Declarations
13
  ; **********************
                                ; Multi-Purpose Register
14
  .def
          mpr = r16
15
  .def
          waitcnt = r17
                                   ; Wait Loop Counter
                                ; Inner Loop Counter
  .def
16
          ilcnt = r18
  .def
17
          olcnt = r19
                               ; Outer Loop Counter
  .def
                               ; Hit Left Counter
18
          hlcnt = r15
19 .def
                               ; Hit Right Counter
          hrcnt = r14
                                ; needed for LCD binToASCII
20
  ; def
          count = r20
21
22
          WTime = 50
                               ; Time to wait in wait loop
  .equ
23
          WskrR = 4
                               ; Right Whisker Input Bit
24
  .equ
          WskrL = 5
                               ; Left Whisker Input Bit
25
  .equ
                               ; Right Engine Enable Bit
26
          EngEnR = 5
  .equ
                               ; Left Engine Enable Bit
27
          EngEnL = 6
  .equ
          EngDirR = 4
                               ; Right Engine Direction Bit
28
  .equ
29
                                ; Left Engine Direction Bit
          EngDirL = 7
  .equ
30
  ; //TAKEN FROM LAB3
31
32
33
          lcdL1 = 0x00
                      ; Make LCD Data Memory locations constants
  .equ
34
  .equ
          lcdH1 = 0x01
35
  .equ
          lcdL2 = 0x10
                        ; lcdL1 means the low part of line 1's location
36
          lcdH2 = 0x01; lcdH2 means the high part of line 2's location
  .equ
          lcdENDH = 0x01 ; as it sounds, the last space in data mem
37
  .equ
          lcdENDL = 0x1F
38
                            ; for storing lcd text
  .equ
39
40
  ;//END TAKEN FROM LAB3
41
42
  .equ strSize = 4;
43
```

```
44
45
 : These macros are the values to make the TekBot Move.
46
  47
48
       MovFwd = (1 << EngDirR | 1 << EngDirL) ; Move Forward Command
49
 .equ
       50 .equ
                              ; Turn Right Command
51
 .equ
       TurnL = (1 << EngDirR)
                             ; Turn Left Command
52 .equ
       Halt = (1 << EngEnR | 1 << EngEnL)
53 .equ
                                 ; Halt Command
54
55
  ;* Start of Code Segment
56
  **********************
                         ; Beginning of code segment
58
  .cseg
59
60 :********************************
 ;* Interrupt Vectors
62
  63
       $0000
                         ; Beginning of IVs
  .org
64
       rimp
             INIT
                         ; Reset interrupt
65
66
        ; Set up interrupt vectors for any interrupts being used
67
68
69
        ; This is just an example:
                        ; Analog Comparator IV
        $002E
70
  ; . org
            HandleAC
71
        rcall
                        ; Call function to handle interrupt
72
        reti
                        ; Return from interrupt
        $0002 ; INTO
73
  .org
                       ; RIGHT WHISKER
74
        rcall
             HitRight
75
        reti
        $0004 : INT1
76
 .org
77
        rcall
            HitLeft ;LEFT WHISKER
        reti
78
        $0006 ; INT2
79 \quad ; org
       $0008 ; INT3
80
 .org
81
        rcall ClearCounters
                         ; CLEAR COUNTERS
82
        reti
83
 ; .org
       \$000E ; INT6
84
85
       $0056
                        ; End of Interrupt Vectors
  .org
86
 87
88
  ;* Program Initialization
89
```

```
90
   INIT:
91
        ; Initialize the Stack Pointer (VERY IMPORTANT!!!!)
           ldi
92
                   mpr, low (RAMEND)
93
           out
                   SPL, mpr
                                    ; Load SPL with low byte of RAMEND
94
           ldi
                   mpr, high (RAMEND)
                                   ; Load SPH with high byte of RAMEND
                   SPH, mpr
95
           out
96
97
        ; Initialize Port B for output
                   mpr, $FF
98
           ldi
                                   ; Set Port B Data Direction Register
                   DDRB, mpr
99
           out
                                   ; for output
100
           ldi
                   mpr, $00
                                   ; Initialize Port B Data Register
                                  ; so all Port B outputs are low
101
           out
                   PORTB, mpr
102
        ; Initialize Port D for input
103
           ldi
                   mpr, $00
                                   ; Set Port D Data Direction Register
104
                   DDRD, mpr
105
                                   ; for input
           out
           ldi
                   mpr, $FF
                                   ; Initialize Port D Data Register
106
                                  ; so all Port D inputs are Tri-State
107
           out
                   PORTD, mpr
108
109
110
        : init the LCD
111
           rcall LCDInit
112
113
           rcall LCDBacklightOn
114
           rcall LCDClr
115
           rcall toLCD
116
117
           rcall ClearCounters
118
119
120
        ; Initialize external interrupts
121
           ; Set the Interrupt Sense Control to falling edge
122
           ldi mpr, 0b10001010
123
           sts EICRA, mpr;
124
125
        ; Configure the External Interrupt Mask
126
           ldi mpr, 0b0000_-1011; x0xx_-0000; all\ disabled
127
           out EIMSK, mpr;
        ; Turn on interrupts
128
129
            ; NOTE: This must be the last thing to do in the INIT function
130
        sei; Turn on interrupts
131
132
   *************************
133
       Main Program
134
   135 MAIN:
                                   ; The Main program
```

```
136
137
           ldi
                   mpr, MovFwd
                                  ; Load Move Forward Command
138
           out
                   PORTB, mpr
139
140
           rjmp
                   MAIN
                                   ; Create an infinite while loop to
141
                                   ; signify the end of the program.
142
143
    144
       Functions and Subroutines
   146
147
148
       You will probably want several functions, one to handle the
       left whisker interrupt, one to handle the right whisker
149
       interrupt, and maybe a wait function
150
151
152
153
154
    ; Func: Template function header
155
    : Desc: Cut and paste this and fill in the info at the
           beginning of your functions
156
157
    ClearCounters:
                                          ; Begin a function with a label
158
159
160
           ; Save variable by pushing them to the stack
161
           ; Execute the function here
162
163
           clr
                   hrcnt
                                  ; sets hlent and hrent to zero by
164
           clr
                   hlcnt
                                   ; doing an xor operation with itself
165
166
           push ZL
                              ; Save vars to stack
167
           push ZH
           push XL
168
           push XH
169
170
           push mpr
171
           push ilent
172
173
           ldi
                ZL , low(STRING_BEG<<1) ; Sets ZL to the low bits
174
                                   ; of the first string location
                ZH , high (STRING_BEG<<1) ; Sets ZH to the first
175
           ldi
                                  ; of the first string location
176
                XH , lcdH1
177
           ldi
178
           ldi
                XL , lcdL1
179
           ldi
                ilcnt, 16
180
181
   CCl1: ; While ilcnt != zero 1
```

```
182
              lpm mpr, Z+
183
              \mathbf{st}
                   X+ , mpr
184
                   ilcnt
              \operatorname{dec}
185
              brne CCl1
186
              ldi ZL, low(STRING2_BEG<<1)
187
188
              ldi ZH, high(STRING2_BEG<<1)
              ; z \ is \ already \ pointing \ at \ the \ second
189
              ; string due to how memory is stored
190
191
                   XH, lcdH2
192
              ldi
                   XL, lcdL2
                   ilcnt, 16
193
              ldi
194
    CCl2: ; While ilcnt != zero 2
195
196
              lpm mpr, Z+
                   X+ , mpr
197
              \mathbf{st}
198
              \operatorname{dec}
                  ilcnt
199
              brne CCl2
200
201
              rcall LCDWrite
202
              ldi mpr , 0b0000\_0111
203
204
              out EIFR, mpr
205
206
              pop ilcnt
207
              pop mpr
208
              pop XH
209
              pop XL
210
              pop ZH
                                          ; Pop vars off of stack
211
              pop ZL
212
              ; Restore variable by popping them from the stack
              ; in reverse order
213
214
215
                                          ; End a function with RET
              \mathbf{ret}
216
217
218
219
     : Func: toLCD
220
    ; Desc: Takes various info and pushes it to the LCD
221
              *HL#:0
222
              *HR#:0
223
224
    toLCD:
225
              push ZL
                                     ; Save vars to stack
226
              push ZH
227
              push XL
```

```
228
             push XH
229
             push mpr
230
             push ilcnt
231
232
             ; Sets ZL to the low bits of the first string location
                  ZL , low(STRING_BEG<<1)
233
                  ZH , high (STRING_BEG<<1)
234
             ldi
             ; points to the data location where LCD draws from
235
236
                  XH , lcdH1
             ldi
237
             ldi
                  XL, lcdL1
238
             ldi
                  ilent, 4
239
240
    Line1Loop: ; While ilcnt != zero
241
             lpm mpr, Z+
                  X+ , mpr
242
             \mathbf{st}
243
             dec ilcnt
244
             brne Line1Loop
245
             //end loop
246
247
            mov mpr, hlent; copies the counter to mpr
248
             rcall Bin2ASCII
249
250
             ; Takes a value in MPR and outputs
251
             ; the ascii equivilant to XH:XL
             ; convineintly X is currently pointing where
252
             ; I would like this number to go
253
254
255
256
             ldi ZL, low(STRING2_BEG<<1)
             ldi ZH, high(STRING2_BEG<<1)
257
258
259
             ldi
                  XH, lcdH2
             ldi
                  XL, lcdL2
260
261
             ldi
                  ilent, 4
262
    Line2Loop: ; While ilcnt != zero 2
263
264
             lpm mpr, Z+
                  X+ , mpr
265
             st.
266
             dec ilcnt
             brne Line2Loop
267
268
269
270
            mov mpr, hrent;
271
             rcall Bin2ASCII
272
273
             rcall LCDWrite
```

```
274
275
276
277
278
279
280
             pop ilent
281
             pop mpr
282
             pop XH
283
             pop XL
284
             pop ZH
285
             pop ZL
                                         ; Pop vars off of stack
286
287
288
             \mathbf{ret}
289
    ; Sub:
290
             HitRight
291
    ; Desc: Handles functionality of the TekBot when the right whisker
             is triggered.
292
293
294
    HitRight:
295
                                    ; Save mpr register
             push
                      mpr
296
                                        ; Save wait register
             push
                      waitcnt
                                    ; \quad Save \quad program \quad state
297
             in
                      mpr, SREG
298
             push
                      mpr
299
300
             ; Move Backwards for a second
301
             ldi
                      mpr, MovBck; Load Move Backward command
302
             out
                      PORTB, mpr ; Send command to port
                      waitcnt, (WTime<<1); Shifted bit back by 1,
303
             ldi
304
                                             ; \ \ making \ \ the \ \ wait \ \ time \ \ two \ \ seconds
305
             rcall
                      Wait
                                         ; Call wait function
306
307
             ; Turn left for a second
                      mpr, TurnL ; Load Turn Left Command
308
             ldi
309
             out
                      PORTB, mpr ; Send command to port
310
             ldi
                      waitent, WTime ; Wait for 1 second
311
             rcall
                      Wait
                                         ; Call wait function
312
313
             ; Move Forward again
             ldi
                      mpr, MovFwd; Load Move Forward command
314
                      PORTB, mpr; Send command to port
315
             out
316
317
                               ; Restore program state
             pop
                      mpr
318
                      SREG, mpr
             out
                                    ; Restore\ wait\ register
319
                      waitcnt
             pop
```

```
320
                      mpr ; Restore mpr
             pop
321
322
                      hrcnt;
             inc
323
             rcall
                      toLCD;
324
             ; fix debounce
             ldi mpr , 0b0000_0111
325
326
             out EIFR, mpr
                                ; Return from subroutine
327
             \mathbf{ret}
328
329
    ; Sub:
330
             HitLeft
331
    ; Desc: Handles functionality of the TekBot when the left whisker
332
             is triggered.
333
    HitLeft:
334
335
             push
                      mpr
                                    ; Save mpr register
336
             push
                      waitcnt
                                       ; Save wait register
337
             in
                      mpr, SREG
                                    ; Save program state
338
             push
                      mpr
339
340
             ; Move Backwards for a second
                      mpr, MovBck; Load Move Backward command
341
             ldi
342
                      PORTB, mpr ; Send command to port
             out
343
             ldi
                      waitcnt, (WTime<<1); Wait for 1 second
344
             rcall
                      Wait
                                   ; Call wait function
345
             ; Turn right for a second
346
                      mpr, TurnR ; Load Turn Left Command
347
             ldi
348
             out
                      PORTB, mpr ; Send command to port
             ldi
                      waitcnt, WTime ; Wait for 1 second
349
350
             rcall
                      Wait
                                        ; Call wait function
351
352
             ; Move Forward again
                      \operatorname{mpr},\ \operatorname{MovFwd}\ ;\ \operatorname{Load}\ \operatorname{Move}\ \operatorname{Forward}\ \operatorname{command}
353
             ldi
                      PORTB, mpr ; Send command to port
354
             out
355
356
                               ; Restore program state
             pop
                      mpr
357
                      SREG, mpr
             out
358
                      waitcnt
                                 ; Restore wait register
             pop
359
             pop
                      mpr ; Restore mpr
360
361
             inc
                      hlcnt
362
             rcall
                      toLCD;
363
                      : fix \ debounce
             ldi mpr , 0b0000\_0111
364
365
             out EIFR, mpr
```

```
366
                               ; Return from subroutine
             \mathbf{ret}
367
368
    ; Sub:
369
             Wait
370
    ; Desc: A wait loop that is 16 + 159975*waitcnt cycles or roughly
371
             waitcnt*10ms. Just initialize wait for the specific amount
372
             of time in 10ms intervals. Here is the general equation
             for the number of clock cycles in the wait loop:
373
                  (((((3*ilcnt)-1+4)*olcnt)-1+4)*waitcnt)-1+16
374
375
376
    Wait:
377
             push
                      waitcnt
                                        ; Save wait register
378
             push
                      ilcnt
                                        ; Save ilent register
             push
                                        ; Save olent register
379
                      olent
380
381 Loop:
             ldi
                      olcnt, 224
                                        ; load olent register
382 OLoop:
             ldi
                      ilcnt, 237
                                        ; load ilcnt register
383 ILoop:
             \mathbf{dec}
                      ilcnt
                                        ; decrement ilcnt
                                        : Continue Inner Loop
384
             brne
                      ILoop
385
                                    ; decrement olcnt
             \operatorname{dec}
                      olcnt
386
             brne
                      OLoop
                                        ; Continue Outer Loop
387
             dec
                      waitcnt
                                    : Decrement wait
                                        ; Continue Wait loop
388
             brne
                      Loop
389
                                   ; Restore olcnt register
390
                      olent
             pop
391
                      ilcnt
                                    ; Restore ilcnt register
             pop
392
                                    ; Restore wait register
             pop
                      waitcnt
393
                               ; Return from subroutine
             \mathbf{ret}
394
395
396
397
    ; Func: Template function header
398
399
     ; Desc: Cut and paste this and fill in the info at the
             beginning of your functions
400
401
    FUNC:
402
                                        ; Begin a function with a label
403
404
             ; Save variable by pushing them to the stack
405
406
             : Execute the function here
407
408
             ; Restore variable by popping them from the stack in reverse order
409
410
             \mathbf{ret}
                                        ; End a function with RET
411
```

```
413 ;* Stored Program Data
415
416 ; Enter any stored data you might need here
|417 ; org|
418 STRING_BEG:
      "HL#:0...."; Declaring data in ProgMem
419 .DB
420 STRING2_BEG:
421 .DB
      "HR#:0...."
422 STRING_END:
423
424
426 ; * Additional Program Includes
427
 428 .include "LCDDriver.asm"
               ; Include the LCD Driver
```