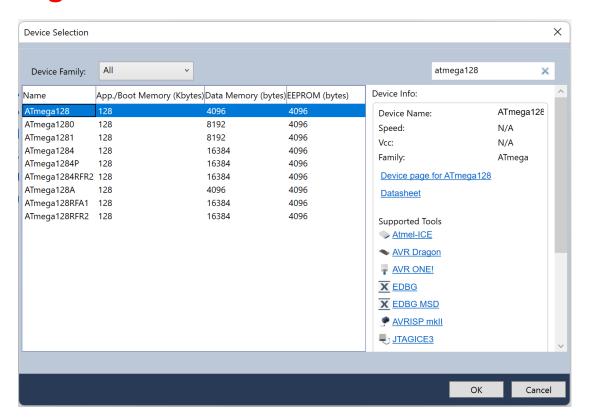


TA:

School of Electrical Engineering and Computer Science Oregon State University

Supported Architecture

- Only Windows support the Microchip Studio
- Microchip Studio currently does not support a simulator for ATmega32U4. Select the ATmega 28 device instead.



Goal of this Pre Lab

 Learn how to use the Microchip Studio simulator

Set Break Points

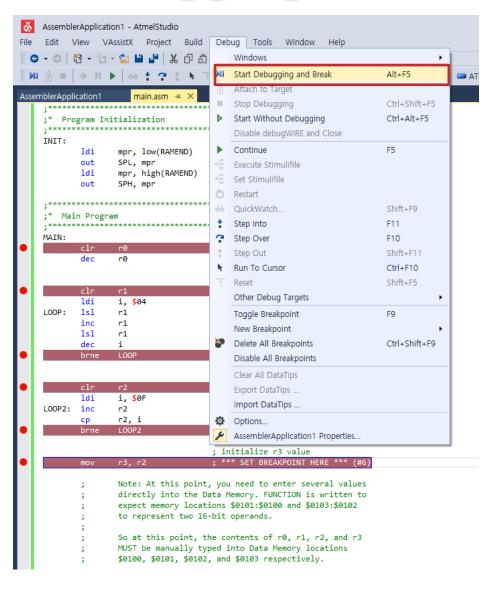
Start Debugging and Break

 Observe data using Processor, I/O port, and Memory window

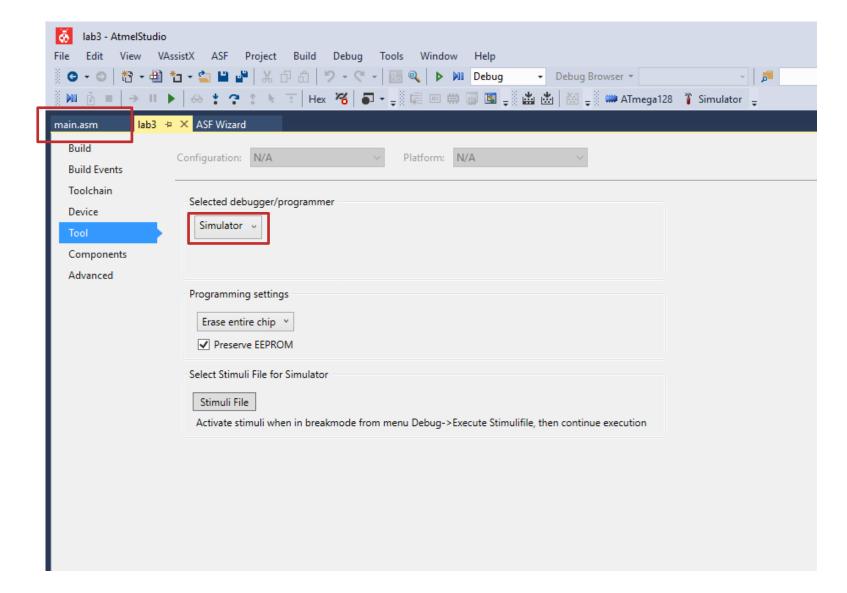
Break Points

```
lab3 - AtmelStudio
 Edit View VAssistX ASF Project Build Debug Tools Window
     ▼ Debug B
 INIT:
                             ; The initialization routine
       ldi
             mpr, low(RAMEND)
                             ; initialize Stack Pointer
             SPL, mpr
       ldi
             mpr, high(RAMEND)
             SPH, mpr
       out
;* Main Program
MAIN:
                             ; *** SET BREAKPOINT HERE *** (#1)
       dec
             r0
                             : initialize r0 value
                             ; *** SET BREAKPOINT HERE *** (#2)
       ldi
             i, $04
LOOP:
       1s1
             r1
                             ; initialize r1 value
       inc
       1s1
             r1
       dec
                              ; *** SET BREAKPOINT HERE *** (#3)
                             ; *** SET BREAKPOINT HERE *** (#4)
       ldi
             i, $0F
LOOP2: inc
             r2
                             ; initialize r2 value
             r2, i
       CD
                              ; *** SET BREAKPOINT HERE *** (#5)
                             ; initialize r3 value
                             ; *** SET BREAKPOINT HERE *** (#6)
             Note: At this point, you need to enter several values
             directly into the Data Memory. FUNCTION is written to
             expect memory locations $0101:$0100 and $0103:$0102
             to represent two 16-bit operands.
             So at this point, the contents of r0, r1, r2, and r3
             MUST be manually typed into Data Memory locations
```

Start Debugging and Break



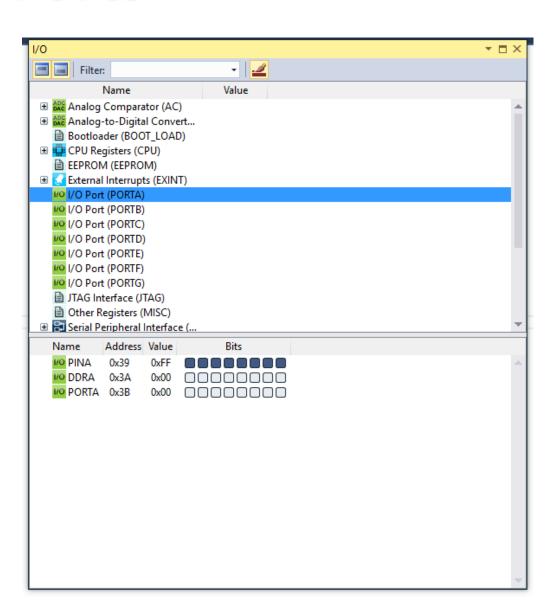
Simulator



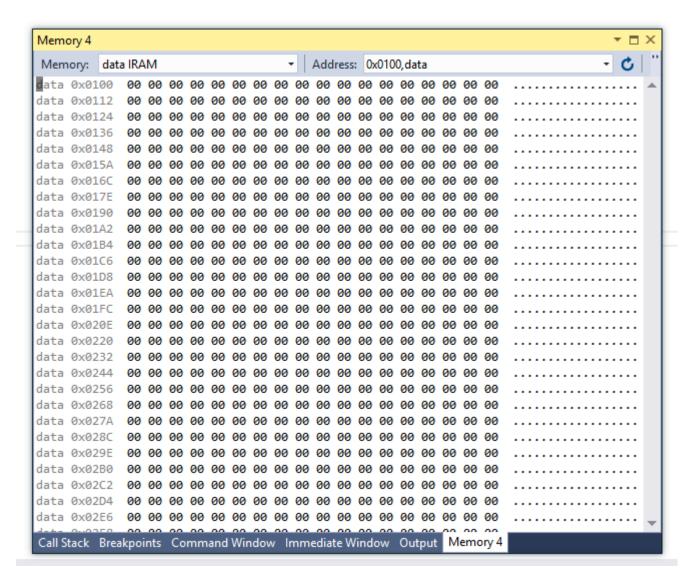
Processor Status

Processor Status		→ □ ×
Name	Value	
Program Counter	0x00000000	<u></u>
Stack Pointer	0x0000	
X Register	0x0000	
Y Register	0x0000	
Z Register	0x0000	
Status Register	UTHSVNZC	
Cycle Counter	0	
Frequency	1.000 MHz	
Stop Watch	0.00 μs	
■ Registers		
R00	0x00	
R01	0x00	
R02	0x00	
R03	0x00	
R04	0x00	
R05	0x00	
R06	0x00	
R07	0x00	
R08	0x00	
R09	0x00	
R10	0x00	
R11	0x00	
R12	0x00	
R13	0x00	
R14	0x00	
R15	0x00	
R16	0x00	
D17		

I/O Ports



Memory



Announcements

• There is a tutorial video for this assignment.

Questions?

