# ECE 375 Lab 7

Remotely communicated Rock Paper Scissors

Lab session: 015 Time: 12:00-13:50

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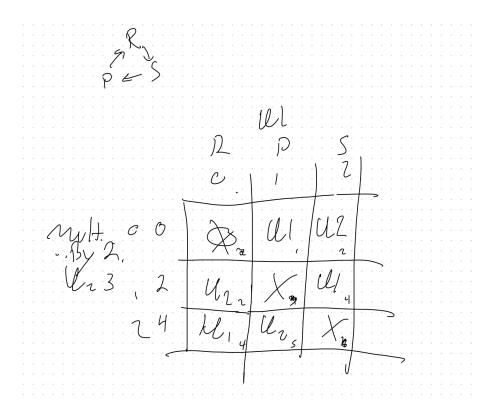
Programming partner: Lucas Plastid

### 1 Introduction

This is the final lab for ECE 375 and as such it necessitates a challenge. The major task for this lab was to, with 2 Benny boards, have them communicate over USART to play a game of rock paper scissors with each other. This allowed the students to apply certain knowledge gained about timers/counters, and how they can apply when sending or receiving data.

# 2 Design

The design for this Lab can be seen below in the flow chart and the derision tree that we built.



# 3 Assembly Overview

As for the Assembly program an overview can be seen below.

## 3.1 Internal Register Definitions and Constants

many different internal registers were used for this lab. Those include a multi-purpose register, 2 count registers (inner and outer) for counting lines on the LCD, a zero register to compare to, userChoice to hold the user rock paper scissors choice, tmrcnt a timer count register, button; a register to denounce the button, and oldbut, somewhere to hold the old button signal label. Additionally send ready was initialized to \$FF and the LCD data memory locations were saved to their corresponding names.

Disp wedere sessee

| wait for Pet 7
| Lo Sent Ready Sout >0 Want Register -0 The Set

Clear Relief

Case 0:

Dispany Road, writing In the open.

Wait for Rell and Rethered Sorts game Strong

Case 1: PRX la is Set

go to gave Strong

Lo Grant Clear for them

if ver Pusses PD4

Sor Choice = Choice-1

if Clone == 2

Set Choice to B

Disp Choice

Tungh General

Send Currus Choice

Relline Bp Clone

Disp opp Chare on Los 2

Walt was I Show = 4

Coppue Choices

### 3.2 Interrupt Vectors

no interrupt vectors were included in this assignment.

#### 3.3 Initialization Routine

The initialization routine was quite complex as it had to setup the USART registers and enable the use of USART. All of the standard initialization parts are there, such as the initialization of the stack pointer, port B for output, Port D for input and the LCD. In addition to setting the baud rate for the USART, an additional timer counter was used to count on the LEDs.

### 3.4 Main Routine

The main program is quite simple as it first writes the welcome text to the LCD, next it querys for button 7. Once button 7 is pressed it makes sure it is connected to another board over USART. Once the key has been sent and received by both parties the mainprogram jumps into the game start routine:

#### 3.5 Subroutines

### 3.6 USART\_TX

This subroutine is responsible for sending the USART signal cleanly

#### 3.7 USART RX

This subroutine is responsible for receiving a sent USART signal.

#### 3.7.1 GAMESTART

This subroutine takes care of most of the actual gameplay that occurs while the two boards are communicating. this includes getting the users choice, looping trough timers. Changing the LEDs, and checking to see who won.

#### 3.7.2 WRITESCREEN

The Writescreen subroutine writes the queried data to the LCD. the input for this subroutine is 2 registers, those being ilcnt and olcnt.

#### 3.7.3 STARTTIMER

This function starts the 1.5 second timer and will end and set the timer flag when it finishes.

#### 3.8 SMALLWAIT

This function waits a small amount of time to allow for debouching of the button input of the methods outlined above.

### 4 Testing

Tested Each button press and compared to external calculations.

Case	Expected	Actual meet expected
d4	rock-į paper-į scissors	✓
d5	nothing	✓
d6	nothing	✓
d7	starts the game	✓

Table 1: Assembly Testing Cases

# 5 Study Questions

1. NONE!

### 6 Difficulties

This lab was quite difficult, mainly due to the fact of how verbose the manual was, and also the fact that the manual has some example code that does not work when applied directly. This caused major problems for us however we eventually found what we needed to do to fix it.

# 7 Conclusion

In conclusion, this lab allowed the student to experiment with sending data over USART and allowed us to play a game using 2 different micro controllers. It was challenging however it also taught many students how not to procrastinate and plan their projects out so they can turn them in on time.

### 8 Source Code

Listing 1: Assembely Script

```
1
2
  3
4
     This is the TRANSMIT skeleton file for Lab 7 of ECE 375
  ;*
5
        Rock Paper Scissors
6
  ; *
7
  ;*
     Requirement:
  ;*
     1. USART1 communication
     2. Timer/counter1 Normal mode to create a 1.5-sec delay
9
  10
11
      Author: Astrid Delestine & Lucas Plaisted
12
  :*
13
        Date: 3/13/2023
  ; *
14
  15
16
17
  .include "m32U4def.inc"
                            ; Include definition file
18
19
  : ************************************
20
  ;* Internal Register Definitions and Constants
  ; *********************
21
22
  ;DO NOT USE 20-22
  .def
23
        mpr = r16
                            ; Multi-Purpose Register
  .def
24
        ilcnt = r18
25
  .def
        olcnt = r19
26
  .def
        zero = r2
27
  .def
        userChoice = r17
  .def
28
        tmrcnt = r15
29
  .def
        button = r13
30 . def
        oldbut = r14
31; Use this signal code between two boards for their game ready
32 .equ
        SendReady = 0b111111111
33 .equ
        lcd1L = 0x00
                            ; Make LCD Data Memory locations constants
34 .equ
        lcd1H = 0x01
35 .equ
        lcd2L = 0x10
                            ; lcdL1 means the low part of line 1's location
        lcd2H = 0x01
                            ; lcdH2 means the high part of line 2's location
36
  .equ
37
  : ************************************
  ;* Start of Code Segment
38
  : ***********************************
39
40
                            ; Beginning of code segment
  .cseg
41
  43
  ;* Interrupt Vectors
```

```
44
  45
          $0000
                                ; Beginning of IVs
  .org
                 INIT
                                    ; Reset interrupt
46
          rjmp
47
48
49
  .org
          $0056
                                ; End of Interrupt Vectors
50
51
  52
  ;* Program Initialization
  54
  INIT:
55
      ; Most important thing possible!!!!!
56
          clr
                 zero
                 userChoice
57
          clr
58
          clr tmrcnt
59
      : Initialize the Stack Pointer (VERY IMPORTANT!!!!)
60
61
          ldi
                 mpr, low (RAMEND)
62
          out
                 SPL, mpr
                                ; Load SPL with low byte of RAMEND
                 mpr, high (RAMEND)
63
          ldi
64
          out
                 SPH, mpr
                                ; Load SPH with high byte of RAMEND
65
      ; Initialize Port B for output
66
67
          ldi
                 mpr, $F0
                               ; Set Port B Data Direction Register
                               ; for output
68
          out
                 DDRB, mpr
69
          ldi
                 mpr, $00
                                ; Initialize Port B Data Register
                               ; so all Port B outputs are low
70
                 PORTB, mpr
          out
71
72
      ; Initialize Port D for input
          ldi
                 mpr, $00
                               ; Set Port D Data Direction Register
73
                 DDRD, mpr
                               ; for input
74
          out
75
          ldi
                 mpr, $FF
                                ; Initialize Port D Data Register
                               ; so all Port D inputs are Tri-State
76
                 PORTD, mpr
          out
77
78
      ; init the LCD
79
          rcall LCDInit
80
          rcall LCDBacklightOn
81
          rcall LCDClr
82
83
     I/O Ports
84
  /*
      ; USART1
85
          Need to set USCR1B and C
86
87
          B: x00xxx00 \rightarrow 0b0_00_1_1_0_0
88
              2:
                  USCZ12
                  TXEN1: Transmitter enable
89
```

```
90
                       RXEN1: Receiver enable
                  4:
91
                  7:
                       RXCIE1: Receive complete interrupt enable flag,
                                enable if using interrupts
92
             C: xxxxxxxx \rightarrow 0b00_00_1_11_0
93
94
                  0:
                       UPOL1: Clock Polarity
                  2-1: USCZ11 and USCZ10
95
96
                       USBS1 stop bit select
                  5-4: UPM1 parity mode
97
98
                  7-6: UMSEL1 USART mode select
99
             x's_are_bits_that_need_to_be_set
    ____0's are status bits, no setting, only reading
100
101
                 011 for 8 bit
        USCZ1:
102
        UMSEL1: 00 for asynchronous
        UMP1:
                 00 for disbled
103
        USBS1: 1 for 2-bit
104
105
        USPOL1: 0 for rising edge
106 */
107
             ; Set baudrate at 2400bps, double data rate
             ; Asynchronous Double Speed mode eq:
108
109
110
        UBRR1 = fOSC/(8*BAUD)
111
             fOSC is just the system clock, so 8MHz
             BAUD is 2400
112
113
        UBRR1 = (8*10^{6})/(8*2400) = 10^{6}/2400 = 416.66
         about 417 or 0b1_10100001
114
115
   */
                  ldi mpr, 0b00000001
116
                  sts UBRR1H, mpr
117
118
                  ldi mpr, 0b10100001
                  sts UBRR1L, mpr
119
120
121
                 ldi mpr, 0b0_00_1_1_0_00
                  sts UCSR1B, mpr
122
                 ldi \ mpr \,, \ 0\,b\,0\,0\,{}_{-}0\,0\,{}_{-}1\,{}_{-}1\,1\,{}_{-}0
123
                  sts UCSR1C, mpr
124
125
126
         ; TIMER/COUNTER1
127
             ; Set\ Normal\ mode, WGM13:0 = 0b000
128
    /*
129
    TIMER MATH
130
        Need 1.5sec delay
        Max count of 2^16-1 = 65,535
131
         65,535/1.5 = 43690 counts/sec ideal, lower is okay
132
133
        CPU @ 8MHz = 8*10^6 \text{ counts/sec}
134
        8*10^6/\text{prescale} \le 43690
135
         prescale >= 8*10^6/43690
```

```
136
        prescale >= 183
137
        prescale should be 256:)
138
        WGM1 = 0b100
139
        at 256 prescale how much we counting?
140
        x/(8MHz/256) = 1.5s
        x = 1.5s (8Mhz/256) = 46,875
141
        so we need to load 65535-46875 = 18660
142
        into the counter in order to have it count for the
143
144
        correct amount of time
145
146
        In two 8-bit numbers, that value is
147
        High: 0b01001000
148
        Low:
              0b11100100
149 */
        ; Configure 16-bit Timer/Counter 1A and 1B
150
                 ; TCCRIA Bits:
151
152
                     ; 7:6 - Timer/CounterA compare mode, 00 = disabled
153
                     ; 5:4 - Timer/CounterB compare mode, 00 = disabled
                     ; 3:2 - Timer/CounterC \ compare \ mode, 00 = disabled
154
                     : 1:0 - Wave gen mode low half, 00 for normal mode
155
                 1di mpr, 0b00_{-}00_{-}00_{-}00
156
                 sts TCCR1A, mpr
157
158
                 ; TCCRIB Bits:
159
                     ; 7:5 - not \ relevant, 0's
160
                     ; 4:3 - Wave gen mode high half, 00 for normal
161
                     ; 2:0 - Clock \ selection, 100 = 256 \ prescale
162
                 ldi mpr, 0b000_00_100
                 sts TCCR1B, mpr
163
164
165
        ; Load text data from program mem to data mem for easy access
166
        ldi ZH, high(STRING1)
167
        ldi ZL, low(STRING1)
                    ; shift for program mem access
        lsl ZH
168
        lsl ZL
169
        adc ZH, zero ; shift carry from lower byte to upper byte
170
171
        ldi YH, high (welcome)
172
        ldi YL, low (welcome)
173
             ; Z has the loading address, Y the offloading address
             ; Need to load 16*number of phrases letters
174
175
                 16*11 = 176
        ldi ilcnt, 176
176
177 LOADLOOP:
178
            lpm mpr, Z+; load letter into mpr
179
            st Y+, mpr ; store letter into data meme
180
            dec ilcnt ; count 1 more done
181
            cp ilcnt, zero ; are we done yet
```

```
182
         brne LOADLOOP
183
184
185
186
188 ;* Main Program
189
  190 MAIN:
191
      ldi ilcnt, 0
      ldi olcnt, 1
192
      rcall WRITESCREEN
193
194 MAIN2:
195
      sbic PIND, 7; wait for 7 button
      rjmp MAIN2
196
197
      clr mpr
      clr olent
198
199
200
      ldi mpr, $FF
      {\tt rcall\ USART\_TX\ ;\ send\ confirmation}
201
202
      ldi ilcnt, 2
203
      ldi olent, 3
204
      rcall WRITESCREEN
205
      rcall USART_RX; Wait until receive, placed in mpr
206
      cpi mpr, $FF
207
      brne MAIN
      rcall GAMESTART
208
209
210
211
             MAIN
      rjmp
212
213
   **********************
214
   ;* Functions and Subroutines
215
   216
217
218 USART_TX: ; transmits mpr
219
      push mpr
      lds mpr, UCSR1A
220
221
      sbrs mpr, UDRE1
222
      rjmp USART_TX
223
      pop mpr
224
      sts UDR1, mpr
225
      \mathbf{ret}
226
227 USART_RX:
```

```
228
        lds mpr, UCSR1A
229
        sbrs mpr, RXC1; received = skip
230
        rimp USART_RX
        ; get data from usart into mpr
231
232
        lds mpr, UDR1
233
        ret
234
235
236
237
238 GAMESTART:
239
        ldi olcnt, $FF; start screen
240
        ldi ilcnt, 4
        rcall WRITESCREEN
241
242
        ; start clock for timer
        rcall STARTTIMER; start 1.5sec timer
243
244
        clr userChoice
245
        inc userChoice
246
        inc userChoice
247
        ldi mpr, 0b11110000
248
        mov tmrcnt, mpr
249
        out PORTB, mpr
250
        clr oldbut ; button has never had value checked!
251 GAMELOOP:
252
        ; check if timer is over
253
        sbis TIFR1, TOV1 ; if timer overflowed
254
        rimp NOTIMER
255
            1sl tmrcnt
256
            mov mpr, tmrcnt
            out PORTB, mpr
257
258
            cpi mpr, 0
            breq GAMESTART2; if all 4 done next
259
260
            rcall STARTTIMER; start a new timer
261
        NOTIMER:
262
        mov mpr, oldbut
263
        cpi mpr, 0; if we weren't pressing the button already
264
        brne ALREADYPRESSED
265
            sbic PIND, 4; if button pressed
266
            rjmp ALREADYPRESSED
                 ldi mpr, 1
267
                mov oldbut, mpr; mark down for next loop that its pressed
268
                 inc userChoice; cycle to next choice
269
270
                 cpi userChoice, 3
271
                 brne BUTSKIP; if we rolled over
272
                     clr userChoice; reset to rock
273
                BUTSKIP:
```

```
274
                 ; Now we need to write the screen
275
                 ldi ilcnt, 4
276
                 ldi olcnt, 5
                 add olcnt, userChoice
277
                 rcall WRITESCREEN
278
279
        ALREADYPRESSED: ; button not pressed or was already pressed landing spot
280
        rcall SMALLWAIT
281
        sbic PIND, 4; if button 4 not pressed
282
            clr oldbut
283
        rimp GAMELOOP
284
285 GAMESTART2:
286
        mov mpr, userChoice
287
        rcall USART_TX
288
        rcall USART_RX
289
        push mpr
290
        ldi olent, 5
291
        add olcnt, userChoice
        ldi ilcnt, 5
292
        add ilent, mpr
293
294
        rcall WRITESCREEN
295
296
        rcall STARTTIMER; start 1.5sec timer
297
        ldi mpr, 0b11110000
        mov tmrcnt, mpr
298
299
        out PORTB, mpr
300 GAMELOOP2:
301
        ; check if timer is over
302
        sbis TIFR1, TOV1 ; if timer overflowed
        rjmp NOTIMER2
303
            1s1 tmrcnt
304
305
            mov mpr, tmrcnt
306
            out PORTB, mpr
307
            cpi mpr, 0
            breq GAMEEND ; if all 4 done next
308
309
            rcall STARTTIMER; start a new timer
310
        NOTIMER2:
311
        rjmp GAMELOOP2
312 GAMEEND:
313
        pop mpr ; load mpr with p2 val
314
        cp userChoice, mpr
315
        breq uDraw
316
317
        lsl mpr; effective mul 2
318
        add userChoice, mpr
319
        cpi userChoice, 1
```

```
320
        breq uWin
321
        cpi userChoice, 2
322
        breq theyWin
        cpi userChoice, 4
323
324
        breq uWin
        cpi userChoice, 5
325
        breq theyWin
326
327
328
        rimp GAMEEND; THIS HSOULD NO THPPEN
329
330
331
332 uWin:
333
        ldi ilcnt, 8
334
        rcall WRITESCREEN
335
        rjmp ENDEND
336
337
    theyWin:
338
        ldi ilcnt, 9
339
        rcall WRITESCREEN
340
        rjmp ENDEND
341
342 uDraw:
343
        ldi ilcnt, 10
344
        rcall WRITESCREEN
345
        rjmp ENDEND
346
347 ENDEND:
348
        reall STARTTIMER; start 1.5sec timer
349
        ldi mpr, 0b11110000
350
        mov tmrcnt, mpr
351
        out PORTB, mpr
352 ENDLOOP:
353
        ; check if timer is over
354
        sbis TIFR1, TOV1 ; if timer overflowed
        rimp NOTIMER3
355
356
             lsl tmrcnt
357
            mov mpr, tmrcnt
358
            out PORTB, mpr
             cpi mpr, 0
359
360
             breq ENDENDEND ; if all 4 done next
361
             rcall STARTTIMER; start a new timer
        NOTIMER3:
362
363
        rjmp ENDLOOP
364 ENDENDEND:
365
        ret
```

```
366
367
368
369
370
   ***********************
371
            Write Screen
372
        Writes two words to the screen, assuming that they are
   ; *
373
        stored in ilent and olent, il being the top line and
   ; *
374
        ol being the bottom line
   ;*
375
   ; *
376
       If the register has $FF written to it, write a blank line
   ; *
377
   ; *
378
       The number stored in ilcnt will be from 0 to 10, referring
   ; *
379
       to the words in the order shown at the bottom of the program
   :*
380
381
   382 WRITESCREEN:
383
       push XH
384
       push XL
385
       push YH
386
       push YL
387
       push ZH
388
       push ZL
389
       push mpr
390
       push r0
391
       push r1
392
393
       push ilent
394
       push olent
395
396
       ldi XH, $03
397
       ldi XL, $00
398
399
        rcall LCDClr
400
401
                      ; mpr has lower byte (olcnt)
       pop mpr
                       ; if mpr != FF
402
        cpi mpr, $FF
403
       breq SKIPWRITE1
404
           ldi YH, lcd2H
                         ; load Y with line 2 location
405
           ldi YL, lcd2L
406
           ldi ilcnt, 16
           mul mpr, ilcnt
407
           mov ZH, r1
408
           mov ZL, r0; Z loaded with offset from $0300 of data
409
410
           add ZH, XH
                      ; offset ZH by 3
411 WRITELOOP1: ; moves one letter from data mem to screen data mem
```

```
412
            ld mpr, Z+; does this until 16 are moved
413
            st Y+, mpr
            dec ilcnt
414
415
            cp ilcnt, zero
416
            brne WRITELOOP1
            rcall LCDWrLn2
417
   SKIPWRITE1:
418
419
420
        pop mpr ; mpr has lower byte of top line phrase (ilcnt)
421
        cpi mpr, FF ; if mpr != FF
422
        breq SKIPWRITE2
423
            ldi YH, lcd1H
                          ; load Y with line 1 location
424
            ldi YL, lcd1L
425
            ldi ilcnt, 16
            mul mpr, ilcnt
426
            mov ZH, r1
427
428
                       ; Z loaded with offset from $0300 of data
            mov ZL, r0
429
            add ZH, XH ; offset ZH by 3
430 WRITELOOP2: ; moves one letter from data mem to screen data mem
431
            ld mpr, Z+; does this until 16 are moved
432
            st Y+, mpr
            dec ilcnt
433
            cp ilcnt, zero
434
435
            brne WRITELOOP2
436
            rcall LCDWrLn1
437
   SKIPWRITE2:
438
439
        pop r1
440
        pop r0
441
        pop mpr
442
        pop ZL
443
        pop ZH
444
        pop YL
        pop YH
445
446
        pop XL
447
        pop XH
448
        \mathbf{ret}
449
450
   : ***********************************
451
   :*
            Start Timer
452
        Starts the timer for 1.5 seconds and clears the
453
        overflow flag
   : ************************************
454
455 STARTTIMER:
456
        push mpr
       ; TIFR1 bit 0 has overflow flag
457
```

```
458
       /* Timer Value:
459
       High: 0b01001000
       Low: 0b11100100*/
460
       ldi mpr, 0b01001000 ; Must write H first
461
462
       sts TCNT1H, mpr
       ldi mpr, 0b11100100; If reading, L first
463
       sts TCNT1L, mpr ; timer\ reset
464
       ldi mpr, $01
465
466
       out TIFR1, mpr; clear overflow flag
467
       ; Timer is running for 1.5 sec now,
468
       ; just wait for bit 0 of TIFR1 to be set for the
       ; timer to be done
469
470
       pop mpr
471
       \mathbf{ret}
   ; **********************************
472
473
           Small Wait
474
       Waits for some amount of time. How much? Only god knows.
   ;*
475
   ; *
476
   ; *
       Useful for debouncing
477
478
   479 SMALLWAIT:
480
       push ilent
481
       ldi ilcnt, $FF
482 SMALLWAITLOOP:
483
       dec ilcnt
484
              ; if the switch is bouncing add more nops
       nop
485
       nop
486
       nop
487
       cpi ilcnt, 0
488
       brne SMALLWAITLOOP
489
       pop ilent
490
       \mathbf{ret}
491
   492
      Stored Program Data
   : ***********************************
493
494
495
   ; An example of storing a string. Note the labels before and
496
   ; after the .DB directive; these can help to access the data
497
498
499 STRING1:
500 .DB
           "Welcome! ____"
501 STRING2:
502 .DB
          "Please _ press _PD7"
503 STRING3:
```

```
504 .DB
         "Ready. _ Waiting _ _ "
505 STRING4:
506 .DB
         "for the opponent"
507 STRING5:
508 .DB
         "Game_start___"
509 STRING6:
         "Rock
510 .DB
511 STRING7:
512 .DB
         "Paper..."
513 STRING8:
         "Scissor ...."
514 .DB
515 STRING9:
         "You_won!____"
516 .DB
517 STRING10:
         "Youllost ...."
518 .DB
519 STRING11:
       "Draw....."
520 .DB
521
522
  523
  ; * Data Memory Allocation
524
  525
  .dseg
526
  .org
         $0300
527 welcome:
            .byte 16
            .byte 16
528 press:
529 ready:
            .byte 16
530 for:
            .byte 16
            .byte 16
531 start:
532 rock:
            .byte 16
533 paper:
            .byte 16
            .byte 16
534 scissor:
535 win:
            .byte 16
536 lose:
            .byte 16
            .byte 16
537 draw:
538
539
  540
  ;* Additional Program Includes
541
   ; *********************
542
  .include "LCDDriver.asm"
                            ; Include the LCD Driver
```