

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



of 178457

VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE First Semester January 2005 Computer Science & Engineering

Name of the Student

ANOOPA

Father's / Mother's Name : NARAYANA DEVADIGA

Seat No.: 4JC04CS009

Name of the College

: SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING, MYSORE

SI. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	Result
1	MAT11	Engineering Mathematics - I	100	35	36	25		25	125	50	61	Pass
2	CHE12	Engineering Chemistry	100	35	56	25		22	125	50	78	Pass
3	CCP13	Computer Concepts & 'C' Programming	100	35	52	25	•	24	125	50	76	Pass
4	MES14	Elements of Mechanical Engineering	100	35	55	25		20	125	50	75	Pass
5	ELN15	Basic Electronics	100	35	59	25		24	125	50	83	Pass
6	CPL16	Computer Programming Lab	50	20	45	25	12	17	75	32	62	Pass
7	CHEL17	Engineering Chemistry Lab	50	20	48	25	12	24	75	32	72	Pass
						GRAN	ID TOT	AL.	775.	314	507	

Total Marks Obtained (in words) : FIVE HUNDRED AND SEVEN ONLY

Result of the Semester

: FIRST CLASS

DATE

: JUN 07,2005

Registrar (Evaluation)

NE - Not Eligible A - Absent





ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

246475

STATEMENT OF MARKS

BE Second Semester July 2005 Computer Science & Engineering

Name of the Student

ANOOPA

Father's / Mother's Name : NARAYANA DEVADIGA

Seat No.: 4JC04CS009

Name of the College

: SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING MYSORE

SI. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	MAT21	Engineering Mathematics - II	100	35	75	25		24	125	50	99	Pass
2	PHY22	Engineering Physics	100	35	76	25	-	20	125	50	96	Pass
3	CIV23	Elements of Civil Engineering	100	35	58	25	-	23	125	50	81	Pass
4	GRA24	Engineering Graphics	100	35	79	25	-	23	125	50	102	Pass
5	ELE25	Basic Electrical Engineering	100	35	55	25	-	20	125	50	75	Pass
6	WSL26	Workshop Practice	50	20	38	25	12	22	75	32	60	Pass
7	PHYL27	Engineering Physics Lab	50	20	39	25	12	24	75	32	63	Pass
											4.5	
						GRAN	ID TOTA	۱L	775	314	576	

Total Marks Obtained (in words) : FIVE HUNDRED AND SEVENTY SIX ONLY

Result of the Semester

: FIRST CLASS WITH DISTINCTION

DATE

: SEP 01,2005

Registrar (Evaluation)

A - Absent

NE - Not Eligible



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VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

06 042118

BE Third Semester January 2006 Computer Science & Engineering

Name of the Student

ANOOPA

Father's / Mother's Name : NARAYANA DEVADIGA

Seat No.: 4JC04CS009

Name of the College

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING, MYSORE

SI. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	MAT31	Engineering Mathematics - III	100	35	80	25		25	125	50	105	Pass
2	EC32	Electronics Circuits	100	35	49	25		21	125	50	70	Pass
3	CS33	Logic Design	100	35	56	25	-	23	125	50	79	Pass
4	C534	Discrete Mathematical Structures	100	35	66	25		20	125	50	86	Pass
5	CS35	Data Structures with C	100	35	47	25		16	125	50	63	Pass
6	CS36	OOP with C++	100	35	52	25		25	125	50	77	Pass
7	CSL37	Data Structures Lab	50	20	40	25	12	16	75	32	56	Pass
8	CSL38	Analog & Digital Electronics Lab	50	20	45	25	12	20	75	32	65	Pass
						GRAN	ID TOTA	AL .	900	364	601	

Total Marks Obtained (in words) : SIX HUNDRED AND ONE ONLY

Result of the Semester

: FIRST CLASS

DATE

: MAY 15,2006

Registrar (Evaluation)

NE - Not Eligible A - Absent