



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester January 2006

Computer Science & Engineering

J 06 042118

Name of the Student : ANOOPA

Father's / Mother's Name : NARAYANA DEVADIGA

Seat No. : 4JC04CS009

Name of the College : SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING, MYSORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	MAT31	Engineering Mathematics - III	100	35	80	25	-	25	125	50	105	Pass
2	EC32	Electronics Circuits	100	35	49	25	-	21	125	50	70	Pass
3	CS33	Logic Design	100	35	56	25	-	23	125	50	79	Pass
4	CS34	Discrete Mathematical Structures	100	35	66	25	-	20	125	50	86	Pass
5	CS35	Data Structures with C	100	35	47	25	-	16	125	50	63	Pass
6	CS36	OOP with C++	100	35	52	25	-	25	125	50	77	Pass
7	CSL37	Data Structures Lab	50	20	40	25	12	16	75	32	56	Pass
8	CSL38	Analog & Digital Electronics Lab	50	20	45	25	12	20	75	32	65	Pass
GRAND TOTAL									900	364	601	

Total Marks Obtained (in words) : SIX HUNDRED AND ONE ONLY

Result of the Semester : FIRST CLASS

DATE : MAY 15, 2006

A - Absent NE - Not Eligible



V. S. S. S.

Registrar (Evaluation)