

Instruction Timeline

>> Demo 2 explanation for the behavior for perf-test-dep-ldst.asm

>> instruction retired would be either one of the instructions from the test program or a NOP if dependencies necessitate any stall cycles. Reason is a brief for why a stall was needed in that instance.

Cycle	Instruction Retired	Reason
4	lbi r0, 0	Expected execution
5	lbi r5, 43	Expected execution
6	lbi r6, 43	Expected execution
7	lbi r7, 43	Expected execution
8	ld r1, r0, 0	Expected execution
9	Nop for st r5, r1, 0	Load Hazard(stall)
10	st r5, r1, 0	Expected execution
11	ld r1, r0, 2	Expected execution
12	Nop for st r6, r1, 1	Load Hazard(stall)
13	st r6, r1, 1	Expected execution
14	ld r1, r0, 4	Expected execution
15	st r7, r1, 1	Load Hazard(stall)
16	Nop for st r7, r1, 1	Expected execution
17	halt	Halt!!