

IDE ASSIGMNENT

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IITH - Future Wireless Communications (FWC)

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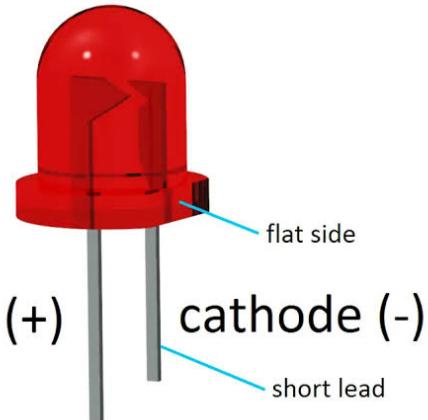
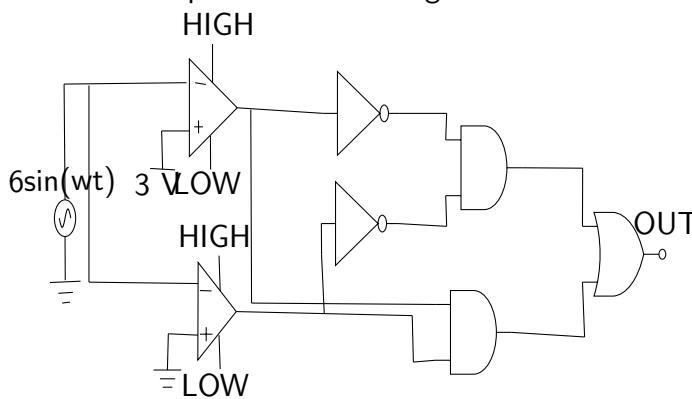


Fig. 1: led

1 QUESTION

In the circuit shown below , assume that the comparators are ideal and all components have zero propagation delay . In one period of the input signal $V_{in}=6\sin(wt)$,the fraction of the time which the output OUT is in login state HIGH is



3 LED CONNECTION

4 IMPLEMENTATION

Arduino PIN	Lcd
D2	led 1
D3	led 2
D8	led 3
GND	LED GRD

Connections

a) Procedure

2 COMPONENTS

Component	Values	Quantity
Arduino	UNO	1
JumperWires	M-F	4
Resistance	220-ohms	3

1. Connect the circuit as per the above table.

2. Connect the led to arduino

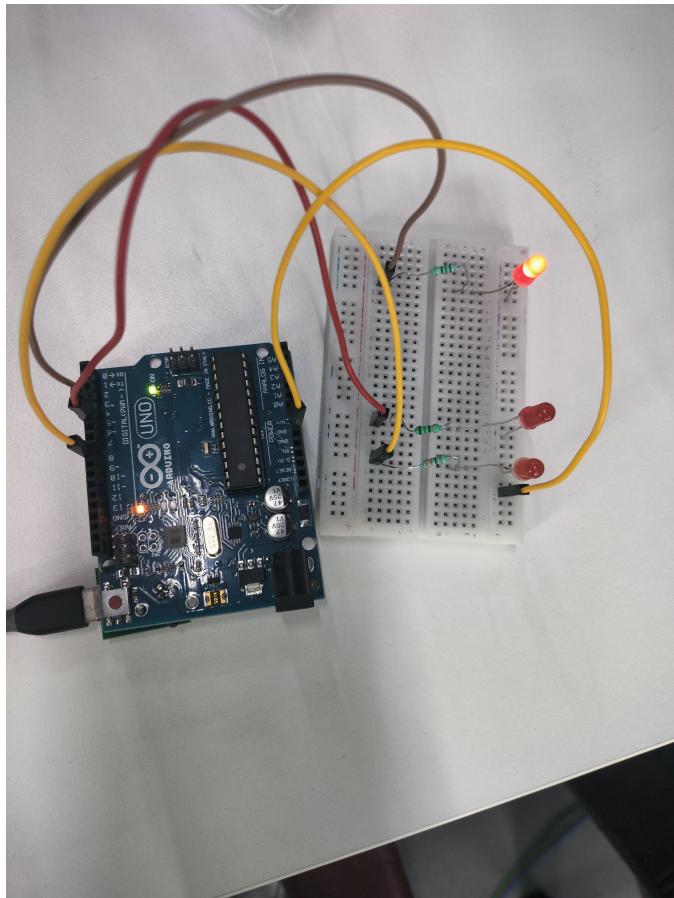


Fig. 2: Arduino connection with led

3. XOR GATE thruth table

A	B	OUTPUT
0	0	1
0	1	0
1	0	0
1	1	1

[https://github.com/adhil2003/fwc
/blob/main/platformio/code/code.cpp](https://github.com/adhil2003/fwc/blob/main/platformio/code/code.cpp)

5 LED OUTPUT