



Adhitha Dias

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Research Interests

I am broadly interested in compilers, programming languages and high performance computing research. My current research focuses on developing compile-time optimizations for sparse tensor algebra computations through kernel distribution and loop fusion in domain-specific languages.

Education

- PhD, Purdue University**, West Lafayette, IN. Jan 2021 - Dec 2025 (expected)
Major: Electrical and Computer Engineering
Advised by: Prof. Milind Kulkarni
Thesis: Sparse Tensor Algebra Compiler Optimizations
- MS, Purdue University**, West Lafayette, IN. Jan 2021 - May 2023 (expected)
Major: Electrical and Computer Engineering
GPA - 4.00/4.00
Courses: Data Structures & Algorithms, Compilers, Computer Architecture, Programming Languages, Intro. to Operating Systems, Parallel Programming, Linear Algebra, Graph Theory, Random Variables and Signals.
- B.Sc. Engineering (Hons), University of Moratuwa**, Katubedda, Sri Lanka. Sep 2014 - Dec 2018
Major: Electronic and Telecommunication Engineering
GPA - 4.05/4.20 [First Class Honours]
- G.C.E.(O/L) and (A/L)**, Richmond College, Galle, Sri Lanka. Jan 2001 - Aug 2013
Subjects: Combined(Pure and Applied) Mathematics, Physics and Chemistry
Ranked 1st in the university entrance examination in the country.

Publications

- **Adhitha Dias**, Kirshanthan Sundararajah, Charitha Saumya and Milind Kulkarni "SparseLNR: Accelerating Sparse Tensor Computations Using Loop Nest Restructuring" in *International Conference on Supercomputing*, ICS 2022.  **Best Paper Award** [ACM DL]
- **Adhitha Dias**, Hasitha Prashan, Yasod Rasanka, Menusha Munasinghe and Ranga Rodrigo and Peshala Jayasekara "Deep Learning of Augmented Reality Based Human Interactions for Automating a Robot Team" in *International Conference on Control, Automation, and Robotics*, ICCAR 2020. [IEEE Xplore]

Experience

- Graduate Research Assistant, Purdue University** Jan 2021 - Present
- **SparseLNR**: Compile time optimizations for sparse tensor algebra operations using loop nest restructuring. The paper from the project won the best paper award at the International Conference on Supercomputing (ICS) '22. (C/C++, DSL, Tensor Algebra Compiler)
 - **SparseTDA**: Compile time optimizations for complex sparse tensor algebra multiplications using tensor kernel dis-association/distribution and fusion (on-going).
- Software Engineer, Sysco LABS, Sri Lanka (Branch of Sysco Corporation, Houston, TX)** Feb 2019 - Dec 2020
- **Enterprise Menu**: A graph based menu management system for multi-location restaurants. (JanusGraph, Neo4J, NodeJS, React/Redux, Java Spring MVC, SonarQube, GrayLog, AWS SNS, AWS S3)
 - **Merchandising User Management** Implemented the first cur version of the application allowing administrators to manage user access to the merchant portal. (JavaScript, NodeJS, React/Redux, Java, Spring Boot, PostgreSQL)

- **Merchandising Authorization and Authentication** API implementation for handling secure access to the merchant portal. (Microsoft Active Directory, Amazon Cognito, JWT authentication, Single sign-on (SSO), Introspection, Redis)
- **Cloud Reports** Feature developments in cloud reports for restaurant point-of-sale devices. (React/Redux, Jenkins, Docker, AWS ECS, AWS S3, Java Spring Web Flux)
- **Communicator API** Introduced a lock mechanism to distribute load among a cluster of docker instances for sending emails, SMS, and voice messages, and improvements like dockerizing the component and deploying to AWS ECS. (MySQL, Twilio integration, SendGrid integration, Redis, Graylog, Symfony, PHP)

Research Intern, School of Information Systems, Singapore Management University, Singapore June - Dec 2016

- **Follow My Lead:** Video based indoor navigation system. Automation of checkpoints acquisition from the video streams recorded. (Android, AR, Sensor Fusion, OpenCV based Image Processing).
- **Wi-Fi based Indoor Localization using Distributed Antennas:** Worked on finding a localization solution using different Wi-Fi access-points using the angle of arrival of the Wi-Fi packets. (FPGA, GNU Radio, IEEE 802.11 PHY, OFDM, WARP devices, MATLAB, Music and SAGE algorithms)

Skills

- Programming Languages: Java, C/C++, Python, JavaScript, Scala, Coq, MATLAB and Bash.
- Operating Systems: Unix/Linux, Android, ROS.
- Frameworks, IDE, DBs: OpenMP, MPI, PyTorch, TensorFlow, Docker, AWS Web Services, GEM5, MySQL, CouchDB, Neo4J, Janus Graph, NetBeans, Visual Studio Code.

Achievements

- **Awards, Honors and Grants**
 - *The Best Paper Award* at the International Conference on Supercomputing 2022.
 - ACM Grant to Attend PLDI 2021 and 2022.
 - *Sri Lanka Telecom Scholarship* 2016 for best Academic Performance.
 - *Mahapola Merit Scholarship* and *Dialog Merit Scholarship* 2014-2018 for Undergraduate Studies.
 - *People's bank scholarship* 2014 for G.C.E.(A/L) performance.
 - Ranked 1st (out of 32k students) in Sri Lanka at the university entrance examination 2013.
 - *The Best Student* at senior high school 2012 in Richmond College.
 - Ranked 15th (out of 400k students) in Sri Lanka at the G.C.E.(O/L) examination 2010.
- **Competitions**
 - Finalists in *International Robotics Challenge (IRC)* 2016-2017 at TECHFEST, IIT Bombay, India.
 - Winners in *Sri Lankan Robotics Challenge (SLRC)* 2016.
 - Runners-up in *MoraXtreme Coding Competition* 2016 organized by University of Moratuwa, Sri Lanka.
 - Placed 178th and 273th correspondingly in *IEEEExtreme 12.0 2018 and 11.0 2017* in the world.
 - Placed 4th in *Sri Lanka Mathematics Olympiad Competition* 2013.
 - Runners-up in *The Australian National Chemistry Quiz* 2012 organized by the Royal Australian Chemical Institute.

Other Education

- **Comprehensive Master Java Developer (CMJD) 2014**, Institute of Java and Software Engineering (IJSE), Sri Lanka.
- **CIMA Diploma in Management Accounting 2014-2015**, Chartered Institute of Management Accountants, UK.

Activities

- Seminar Coordinator for Purdue Programming Languages group.
- Attended Programming Language Mentoring Workshops at PLDI '21, OOPSLA '21 and PLDI '22.