Errors in cmodel

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1 Introduction

Following given are the errors after running the cmodel tests.

1.1 Reduce

- decodeInstruction is correct

- red8 and red16, defined as different functions.

- control not reaching to red16

1.2 Float

- Tested vfadd, vfsub, vfmul.

- Higher register (e.g. f0, of f0 f1) is always zero.

- In the third and fourth iteration of - vectorHalfPrecisionOpInU64Form(in code/half-precision-float/src), result does not chnage.

- The new 16 bit result is not added (shift and OR) in the third iteration, remaining constant. (for I=2.3).