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SERIAL DEVICE

1. Register Addresses

• Control Register (32b): 0x3200

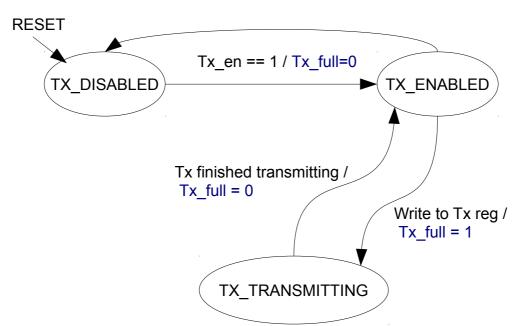
Tx Register (8b): 0x3210Rx Register (8b): 0x3220

2. Control Word:

31-5	4	3	2	1	0
unused	Rx_full	Tx_full	Rx_int_en	Rx_en	Tx_en

3. Tx State Transitions:

$$Tx_en == 0$$



4. Rx State Transitions:

