Trace differences in C and Aaa model on 32 bit tests.

This document gives the differences in the traces of the C and AA models in th 32 bit instruction tests.
The name of the test of which trace is incorrect is given in bold, followed by the reg where the difference was noted. The Instruction is also noted.
Below each instruction is also given the bits where the difference is noted and what it means.

STBAR_UNIMP_NOP_SETHI

INST	AA	C
STBAR	Store-log= 00000000	Store-log= 80000000

Bit 31 – Store is completed.

FLUSH

INST	AA	C
FLUSH	Store-log= 00000000	Store-log= 93006600

Bit 31 – Store is completed.
Bit 29:24 -asi (6 bits)
Bit 15:8 – address-signature, xor of all bytes in address.

Fsubs, Fsubd, Fsqrts, Fsqrtd, Fmuls, Fmuld, invalid_ops, fstod, fdtos, fstoi, fdtoi, fdivs, fdivd, fcmps, fcmpes, fcmpd, fcmped, fadds, faddd,Fsmuld,invalid_ops

INST	AA	C
ldfsr	Fp-Reg-log=28000000	Fp-Reg-log=0000000

Bit 29 - write-fsr-from-fp
Bit 27 - write-fsr

Masked_invalid_ops

INST	AA	C
fsqrts	Fp-Reg-log=800700e9	Fp-Reg-log=80070000
fdivs	Fp-Reg-log=800700bf	Fp-Reg-log=8007003f
fmuls	Fp-Reg-log=800700bf	Fp-Reg-log=8007003f
fadds	Fp-Reg-log=800700bf	Fp-Reg-log=8007003f
fsubs	Fp-Reg-log=800700bf	Fp-Reg-log=8007003f
Fstoi	Fp-Reg-log=80070000	Fp-Reg-log=80070080
fcmps	Fp-Reg-log=22000300	Fp-Reg-log=22001300

Fcmps – written-fsr-signature
all others - Bit 7:0 - written-gpr-signature