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# INTERRUPT CONTROLLER DEVICE

1. **Addresses of the INterrupt Controller (IRC) Control Register** (32b) :
   defined in AJIT_PROJECT_HOME/processor/Aa_v3/sys/parameters/parameters.aa
   as parameter ADDR_INTERRUPT_CONTROLLER_CONTROL_REGISTER

**2. IRC Control Word :**

| 31:1 | 0 |
|------|---|
| Unused | IRC_en |

**4. IRC State Transitions :**

IRC_en == 0 /
INT = 0

No external interrupt /
INT=0

RESET

IRC_DISABLED

IRC_ENABLED

IRC_en == 1 /
INT = 0

IRC_en == 0 /
INT = 0

Interrupt signal received from device/
INT = Interrupt number of highest priority interrupting source

IRC_INTERRUPTING