

Secondary Reference of TSic™

TSic 3 0 6 TO92 -30/70

а	Accuracy Typo
а	Accuracy Type 1 = ± 0.5°C @ RT; one point calibration
	$2 = \pm 0.5^{\circ}$ C at 80°C range
	3 = ± 0.3°C at 80°C range 4 = not defined
	1 1101 01011110
	5 = ± 0.1°C at 40°C range; limited measuring range (-10°C to 60°C)
	6 = not defined
	7 = ± 0.05°C at 20°C range; limited measuring range (-10°C to 60°C)
b	Bit Size
	0 = 11 Bit
	1 = 14 Bit
С	Output Signal
	1 = analog, 0-1V
	3 = ratiometric, 10% - 90% V ⁺
	6 = digital, ZACWire Protocol
d	Housing
	SOP-8 = 150mil, Standard SMT Technology, SOIC-8, after IEC 191-2Q:
	Type 076E35 B
	SOIC8 = 150mil, Standard SMT Technology, SOIC-8, after IEC 191-2Q
	TO92 = new THT housing (EP3)
	e-line = old THT housing
	KGD = Bare dice at waffle pack
	Wafer = Bare dice at wafer, not sawing
е	Special
	Standard: no value
	e.g. "250 Hz" for high sampling rate
	-30/70 = Temperature and tolerance range

