

ADITYA C DOPPALAPUDI

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EDUCATION

Old Dominion University Master of Science in Computer Science (MSCS)	Norfolk, VA, USA Expected May 2026
Jawaharlal Nehru Technological-UCEST Bachelors of Technology in Electronics and Communication Engineering (ECE)	Hyderabad, TS, India Nov 2020 - Jul 2024

WORK EXPERIENCE

GRADIOUS TECHNOLOGIES (venture-backed recruitment startup) Python Developer Intern	Hyderabad, India Dec 2023 – February 2024
<ul style="list-style-type: none">Built a website with various dashboards (which can be filtered out by state) for election tracking by getting data off multiple government and survey pages and filtered repeating data.Used Python along with HTML/CSS, Bootstrap, Beautiful Soup, Flask, SQL, Git.Developed the required XML schema documents and framework required to parse the XML Documents gathered from various election data gathering organizations.Scheduled tasks in windows task scheduler to run python scripts to gather and generate reports in fixed intervals.	
Institute of Electrical and Electronics Engineers - Circuits and Systems Society (IEEE-CAS) VLSI intern	Bangalore, KA, India October, 2023 – December, 2023
<ul style="list-style-type: none">Improved implementation of RSA Algorithm in FPGA for Quantum Cryptography.The hardware implementation over software allowed a more secure and better performance of implementation.Improvement in Rivest-Shamir-Adleman (RSA) algorithm, using Kogge stone adder (KSA) and Montgomery multiplier.Implementation of 4-bit KSA reduced the delay by 3X and the use of Montgomery Multiplier improved efficiency by ~34%.	

UNIVERSITY PROJECTS

UNDERGRADUATE – Audio Denoising using Wavelet Transform	Jan 2024
<ul style="list-style-type: none">Implemented a denoising application using MATLAB and SIMULINK, where we used wavelet transform for reducing noises (like white noise) in the received audio signal.Deployed a GUI for interacting with the application, where the noise sample is uploaded into the app and the denoised version of the sample is received as output.	
UNDERGRADUATE – AES Implementation using Verilog	Jun 2024
<ul style="list-style-type: none">Developed and deployed AES algorithm modules for 128, 192, and 256 key variants, implementing in Verilog for simulation and synthesis to compare their performance.Researched the future prospects of AES, finding AES-256 to be resilient against brute force attacks, even with quantum computing.	

LEADERSHIP & ACTIVITIES

Global Student Friendship Group Student Leader	Norfolk, VA Oct 2024 – Present
Association for Computing Machinery [ACM] – Student Chapter @ODU Event Coordination - Officer Managing and Organizing events for students in ODU, along with outreach and communication across various groups and departments.	Norfolk, VA Sept 2024 – Present
Institute of Electrical and Electronics Engineers Member	Norfolk, VA Sep 2022 – Present

ADDITIONAL

Technical Skills: Advanced in SQL, HTML/CSS; Proficient in MATLAB, Python, GIT; Bash Shell, Flask
Languages: Fluent in English, Telugu, Hindi
Certifications & Training: Python for Everybody Specialization, Bash Shell Scripting, GIT Essentials