

• The standard solution to this problem is to use a special, small, fast lookup hardware cache called a translation look-aside buffer (TLB).

• The TLB is associative, high-speed memory.

· Each entry in the TLB consists of two parts : a key (or tag) and a

· When the associative memory is presented with an item, the item · Access to a page marked invalid causes a page fault. is compared with all keys simultaneously.

 $\bullet$  If the item is found, the corresponding value field is returned.

• The search is fast; the hardware, however , is expensive. Therefore operating system.

the number of entries in a TLB is small.



• The TLB is used with page tables in the following way.

• The TLB contains only a few of the page-table entries. When a logical address is generated by the CPU, its page number is presented to the TLB.

The page table must have valid/invalid bit A swap area must need for performing swap out/swap in

If the page number is found, its frame number is immediately available and is used to access memory.

If the page number is not in the TLB (known as a TLB miss), a memory reference to the page table must be made.

· When the frame number is obtained, corresponding changes are made in the TLB , so that they will be found next time very quickly · If the TLB is already full of entries, an existing entry must be

selected for replacement. Replacement policies range from least recently used (LRU) through round-robin to random.

# DEMAND PAGING

A demand-paging system is similar to a paging system with swapping where processes reside in secondary memory (usually disk).

· Any page execution is started on a page fault

· In demand paging firstly no programs are in memory

 When CPU generate an address, a page fault will occur. When a page fault occurs, we can load the entire program in to main memory or we can load only the needed program. Module4(7)

## PAGING

· Paging is a memory management technique.

 In this approach, physical memory is divided in to fixed sized block called frames and logical memory is also divided in to the fixed sized blocks called pages.

• The size of the page is same as that of frame.

• The key idea of this method is to place the pages of a process in to the available frames of memory , whenever this process is to be executed

### PAGE REPLACEMENT

· Page replacement takes the following approach.

1. Find the location of the desired page on the disk

2. Find a free frame:

a. If there is a free frame, use it.

b. If there is no free frame, use a page-replacement algorithm to

c . Write the victim frame to the disk; change the page and frame tables accordingly.

3. Read the desired page into the newly freed frame; change the page and frame tables.

4. Continue the user process from where the page fault occurred.

page out victim page 2 change to invalid

◆PLACEMENT STRATEGY – It determines where in main memory to page frames place the fetch program or job. Different placement strategies are First fit :- The unused or free space in main memory is known as

holes. A hole list is provided and it is in the form of linked list. The first fit places the program in the first storage hole which is large enough to hold it.

>Best fit:- Best fit places the program in the tightest fitting hole. Here minimum waste of space is occurred.

>Worst fit:- It places the program or data in the largest available hole that will hold it. Here more memory space wastage is occurred.

20 21 22 23

. The paging hardware, in translating the address through the page

The procedure for handling this page fault is straightforward

table, will notice that the invalid bit is set, causing a trap to the

1. We check an internal table (usually kept with the process control

block)for this process to determine whether the reference was a

valid but we have not yet brought in that page, we now page it in.

3. We find a free frame (by taking one from the free-frame list, for

6. We restart the instruction that was interrunted by the tran. The

The efficiency of demand paging is increased by using locality of reference, because continuous hit is occurred. Hardware support

3 page is on backing store

2

> A FIFO replacement algorithm associates with each page the time when that page was

1

6 restar

First-In-First-Out (FIFO) Algorithm

Optimal Page Replacemen

(5)

> When a page must be replaced, the oldest page is chosen.

Reference string: 7.0.1.2.0.3.0.4.2.3.0.3.0.3.2.1.2.0.1.7.0.1

o 3 frames (3 pages can be in memory at a time per process)

7 0 1 2 0 3 0 4 2 3 0 3 2 1 2 0 1 7 0 1

> Replace page that will not be used for longest period of time

7 0 1 2 0 3 0 4 2 3 0 3 2 1 2 0 1 7 0 1

Replace page that has not been used in the most amount of time

7 0 1 2 0 3 0 4 2 3 0 3 2 1 2 0 1 7 0

Belady's anomaly-hence known as OPT or MIN.

Itse nast knowledge rather than future

Associate time of last use with each page

Module4(8)

> It has the lowest page-fault rate of all algorithms and will never suffer from

> It is difficult to implement, because it requires future knowledge of the reference string

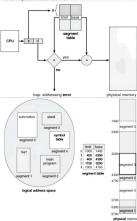
this programmer view of memory.

Here each job is divided in to several segments of different sizes.

SEGMENTATION

. So each segment has a base and limit

· Limit is provided for avoiding segment overlapping. · A program segment contains the program's main function, utility functions, data structures and so on.



## FRAGMENTATION

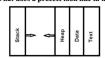
•As processes are loaded and removed from memory, the free \*PHYSICAL ADDRESS memory space is broken in to little pieces.

It happens after sometimes that processes cannot be allocated to memory blocks considering their small size and memory blocks. The user can never view the physical address of program. The user cannot directly across the physical address for the process of the process of the process of the physical address. In the user cannot directly across the physical address for the process of the process o

**◆EXTERNAL FRAGMENTATION** 

• External fragmentation is the unused area between two used > Physical address space:- set of all physical addresses request is available, but is not contiguous. Module4(9)<sup>space</sup>.

What does a process look like in memory? Process states



Data Section: Contains the global variable.

Heap Section: Dynamically allocated memory to process during its run time. Stack: The stack contains the temporary data, such as function parameters,

o The PCB is identified by an integer process ID (PID).

 $\circ$  There is a Process Control Block for each process, enclosing all the information  $~_{\textbf{New}}$  :

 Program counter: The program counter lets you know the address of the next
 From the Running state, a process can make the transition to the block instruction, which should be executed for that process.

o CPU registers: This component includes accumulators, index and

 CPU scheduling information: This component includes a process priority. pointers for scheduling queues, and various other scheduling parameters.

O Accounting and business information: It includes the amount of CPU and time utilities like real time used, job or process numbers, etc. Memory-management information: This information includes the value of

the base and limit registers, the page, or segment tables. This depends on the memory system, which is used by the operating system

Module2(10)

INTERNAL FRAGMENTATION Consider the following figure. Suppose a 25K request is coming. • Segmentation is a memory-management scheme that supports then 30K is fully allocated because it is a fixed partition. Here 5k is

. That is the wasted space contained in a partition that is allocated

• It is a wasted space with in a partition.

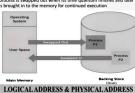
for the process.

• Internal fragmentation can be reduced effectively Free Space 30k assigning the smallest partition but large enough

SWAPPING

Swapping is a mechanism in which a process can be swappe temporarily out of main memory or move to seconda storage(disk) and make that memory available to other processes. At some later time, the system swaps back the processes from the secondary storage to main memory. Though performance is usually affected by swapping process but it

helps in running multiple and big processes in paralle Swapping may happen in the case of Round Robin scheduling. A process is swapped out when its time quantum finishes and later it is brought in to the memory for continued execution



OLOGICAL ADDRESS

It is the virtual address generated by the CPU that can be viewed by the user

· Logical address is generated by the CPU during a program

• The logical address is virtual as it does not exist physically. Hence it

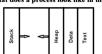
. This address is used as a reference to access the physical memory location(physical address) Logical address space:- set of all logical addresses generated by the CPU in reference to a program is referred as logical address

space.

· Physical address is a location in a memory unit.

. The user cannot directly access the physical address. Instead, the physical address is accessed by its corresponding logical address by

areas. It is a serious problem. Here memory space to satisfy a corresponding to the logical address is called physical address



Text Section: A Process, sometimes known as the Text Section, also includes the current activity represented by the value of the Program Counter.

returns addresses, and local variables. Process Control Block (PCB) A Process Control Block is a data structure maintained by the Operating System

for every process.

about the process

o It is also known as the task control block. It is a data structure, which contains

o Process ID: Unique identification for each of the processes in the operating

o Process state: A process can be new, ready, running, waiting, etc.

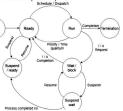
general-purpose registers, and information of condition code.

from the main memory due to lack of the resources (mainly primary memory) is called in the suspended ready state. o I/O status information: This block includes a list of open files, the list of I/O devices that are allocated to the process, etc.

 When a process executes, it passes through different states. o These stages may differ in different operating systems, and the names of these

states are also not standardized. A process state is a condition of the process at a specific instant of time.

o It also defines the current position of the process.



 A program which is going to be picked up by the OS into the main memory is called a new process.

 Whenever a process is created, it directly enters in the ready state, in which it waits for the CPU to be assigned.

The OS picks the new processes from the secondary memory and put

■ The processes which are ready for the execution and reside in the main

mory are called ready state processes

depending upon the scheduling algorithm.

Hence, if we have only one CPU in our system, the number of running processes for a particular time will always be one . If we have n processors in the system then we can have n processe

### Block or wait:

or wait state depending upon the scheduling algorithm or the intrinsic

■ When a process waits for a certain resource to be assigned or for the input from the user then the OS moves this process to the block or wait state and assigns the CPU to the other processes

When a process finishes its execution, it comes in the termination state.

 All the context of the process (Process Control Block) will also be deleted and the process will be terminated by the Operating system. A process in the ready state, which is moved to secondary memory

 If the main memory is full and a higher priority process comes for the memory by throwing the lower priority process out into the secondary

A thread is also known as a lightweight process.

ommunication between two processes.

INTER PROCESS COMMUNICATION

\* IPC communication using showed my sequite commu

processes to establish a segion of shared memory.

\* A shared roly stegion sterids in the address space }

the process Caeating the shaped my segment.

Shared mly segment must attach it to their

They can then exchange information by reading &

weiting date in the shared area.

uses processes not the os.

5.2.1 File Attribute

5.2.2 File Operations

> A file is an abstract data type

The communication is under the control of the

A file's attributes vary from one operating system to another but typically cons

Name: The symbolic file name is the only information kent in human readable

o Identifier: This unique tag, usually a number, identifies the file within the file

Type: This information is needed for systems that support different types of files.

Location: This information is a pointer to a device and to the location of the file

Size: The current size of the file (in butes, words, or blocks) and possibly the

Protection: Access-control information determines who can do reading, writing

Time, date, and user identification: This information may be kept for creation

last modification, and last use. These data can be useful for protection, security,

> To define a file properly, it needs to consider the operations that can be

Creating a file: Two steps are necessary to create a file. First, space in th

Name of the file and the information to be written to the file. The system must

keep a write pointer to the location in the file where the next write is to take

o Reading a file: To read from a file, we use a system call that specifies the name of

The operating system should recognize and support file types. If an operating

system recognizes the type of a file, it can then operate on the file in reasonable

maximum allowed size are included in this attribute.

of activities among cooperating processes

\* Two modes of IPC:

· Shared Mamory
. Message Passing

1 Shared Memory Systems

\* Inter-porcess Communication (IPC) regas to the coordinate

2. Faster context switch: Context switch time between threads is lower

A process can contain multiple threads.

What is a Thread?

Why Multithreading?

Advantages of Thread over Pro

Bounded Buller - Shared Memory Solution A thread is a path of execution within a process. \* Shared data.

typedel stauet ■ The idea is to achieve parallelism by dividing a process into multiple

item bujger [BUFFER\_SIZE]; "at in = 0; int out = 0; 1. Responsiveness: If the process is divided into multiple threads, if

# deline BUFFER\_SIZE 10 .

The shared buffer to implemented as a circular one thread completes its execution, then its output can be immediately array with two logical pointer : in a out.

compared to process context switch. Process context switching requires more overhead from the CPU. Producen a 3. Effective utilization of a multiprocessor system: If we have multiple threads in a single process, then we can schedule multiple threads on multiple processors. This will make process execution while (tour) /\* produce an Item in next produced \*/

shared among the threads. Each thread has its own stack and registers.)

5. Communication: Communication between multiple threads is 8- (n+1) % BUTFER. 512 E

while (in a sout); out = (out+1) % Buffer size, naume (tem in neet consumed \*)

processes to communicate & to synchronize their actions without shaving the same address spay.

\* A mercage - paring facility provides 2 operations

· send (merrage) . Hecove (merrage)

send merrages to & necessar messages from each other. A communication link must asist blue them.

Message passing may be eithen blocking on hon-blocking also known as experhenous daynchronous.

Non-Blocking Good - The Fending proces sends the mag & neumes operation

Blocking Receive - The Receives blocks until a mag is available.

Module 2(11) either a valid may on a null.

O FCFS Alg (First Come Rist Soved) - Simplest of all disk scheduling alg. - In FCFS, with the requests core addressed in the order they consided in the disk queue. 

> \* Arm in placed in Placed in platters from platier → surface → Frack → Sector

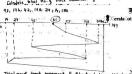
Advantage (FCFS) sequest gets a fair chance No independe postponment

Disadvantage (FCFS)

May not provide the best possible service

arrowent position of Rho head = 55 Writing a file: To write a file, we make a system call specifying both the

Calculate total no. & track movement & Rhu 93, 176, 42, 148, 27, 14, 180



= (176-55) + (176-42) + (148-42) + (148-41)+

> The system uses the extension to indicate the type of the file and the type of SSTF ( Showtest Seek Time Fret) operations that can be done on that file.

5.2.4 File Structure

Module5(12)

\* Overhead & calculate the seek time to advance x Causes of stanvation (waiting for long time) for a sequent if it has higher seek time .

The huffer is sull when ((in+1) to Burren-size) = = 0

# 4. Resource sharing: Resources like code, data, and files can be shared while ((Cin+1) % BUFFER\_SIZE) = = OUE);
/\* do nothing \*/
buffer [in] = next-produced; among all threads within a process.(Note: stack and registers can't be

easier, as the threads share common address space. While in process item next-consumer; while (thue) 6. Enhanced throughput of the system: If a process is divided into multiple threads, and each thread function is considered as one job, then the number of jobs completed per unit of time is increased, thus

Message Passing Systems

Message Passing provides a mechanism to allow

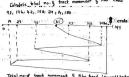
\* If processes P&Q want to communicate, they must

Synchamization .

Other processes that with to communicate using this egnismonous and - The Sending process to blocked lanks the mag is received by the Receiving process on by the mailbor.

Links '

. Does not try to optimize seek time.



(180 - 14) = 661

\* In SSTF, request having showlest Seek time are executed first-\* So the best time of every nequest to calculated. In advance in queue & schedule according to their seet line

As a result, request near the disk arm will get executed first Advantages (SSTF)

\* Aug response time will be decreased \* Throughput increased Disadvantage (SSTF)

