

Compilers Research

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1 Preface

This resource aims to document and formalize aspects of the compilers research project that I am undertaking. It will be updated regularly as the project evolves so as to reflect the most recent and relevant decisions in order to serve as a reference.

2 Overview

This research project is an attempt to develop a programming language for describing hardware and an accompanying compiler. Currently, hardware description languages lack features that would improve ease of expression and modularity, and reduce the potential for errors during execution. Such features include, but are not limited to, strong type systems, parametric polymorphism, and user-defined datatypes. Although there exist languages that strive to provide these primitives, they do not fully consider the semantics of hardware design. The compiler developed through this research project will use the newly developed language as the source and compile to Verilog as the target.

3 Formalizing the Dichotomy

As mentioned in Section 2, there is a need to formalize the dichotomy between the software and hardware components so as to develop a language that respects both entities appropriately. As such, in this section, we will distinguish the types, semantics, and syntax of the two.

3.1 Types

We will first formalize the definitions of the two categories of types: software (SW) and hardware (HW).

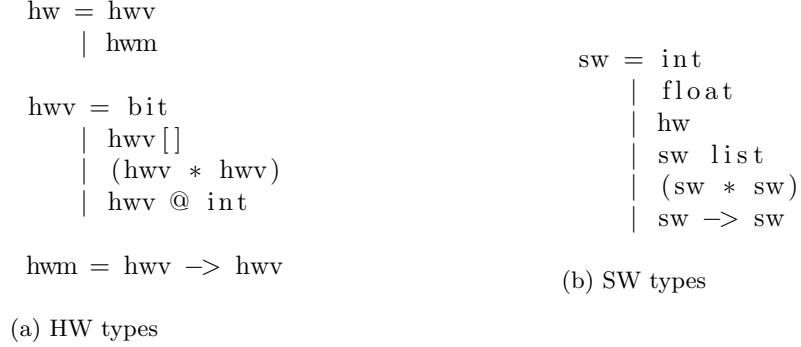


Figure 1: Type definitions

As can be seen in Figure 1(a) above, the hardware type is defined as the combination of two smaller types; hardware values and hardware modules.

The hardware value type represents data that can be manifest as values in hardware. Specifically is defined as a bit, a fixed-sized array of a hardware value type, a tuple of hardware value types, or a timed hardware value type. In order to justify this, we argue that all data is represented by bits in hardware, and as such the fundamental data representation of a hardware value type must be a bit. We augment the expressive power by introducing fixed-sized arrays, in order to represent bit vectors or bit patterns. Next, we permit the declaration of tuples in order to group hardware value types into one value. Finally, we admit that certain hardware values only make sense in conjunction with some temporal type, such as the value of a DFF, and thus introduce a timed hardware value type.

Note that the type declarations for the fixed-size array, tuple, and timed value are recursive, in that any hardware value type can populate these types. This allows for the hardware expression of higher-dimensional arrays, arrays of tuples of bits, tuples of arrays of bits, et cetera.

The hardware module type represents a general system implemented in hardware that accepts a hardware value type as input and produces a hardware value type as output. This may be considered as a "hardware function", although it is specifically designed to prohibit the existence of higher-order functions, which do not comply with the semantics of hardware design.

In Figure 1(b), the software type is defined as an int, a float, a hardware type (either hardware values or hardware modules), a dynamically-sized list of software types, a software tuple, or a software-to-software function. The primitive types we provide in this declaration are integers, floating point numbers, dynamically-sized lists, tuples, and functions.

The software type is again defined recursively, allowing more complex types to be expressed. We also note that the set of software types is a superset of the set of hardware types. This is so that any hardware component can be described and represented in the software, which is essential in designing a hardware description language.

3.2 Declarations

Given that the hardware and software types are distinct, we must formalize the syntax to declare such types. Later, we will discuss the mechanisms by which to perform explicit conversions, as there is no implicit data conversion.

3.2.1 Hardware

A bit is declared in literal syntax using an apostrophe, the letter 'b', followed by the bit value:

```
val a := 'b0
```

A fixed-size array of hardware types can be declared in multiple ways, each of which must explicitly state the size of the array.

To declare an array literal, one can use the following syntax:

```
val b := #['b0, 'b1, 'b0, 'b0]
```

In the above declaration, the type of the variable `b` is `bit[4]`. In other words, the size of the array parameterizes the type. With the concept of generics, which will be discussed later, functions may strictly permit certain sized arrays, or accept arbitrarily sized arrays. In general the type of an array of size `n` for some hardware value type `'hv` is `'hv[n]`.

An array may also be declared in index-functional form. In this form, the programmer specifies the size of the array and a function mapping from the bit index to a value. For example:

```
val c := #[32, i -> if(i % 3 = 0) then 'b1 else 'b0]
val d := #[32, 0 -> 'b0
           | i -> d[i - 1] ^ c[i]]
```

Here we employ a special syntax for declaring the index-to-value function that allows the expression of pattern-matching succinctly. This differs from conventional pattern-matching syntax, which will be discussed later.

The final syntactical specification for the hardware types is for the tuple. These represent pairs or groups of other hardware values, and are declared in one way as follows:

```
val e := ('b0, #['b0, 'b1, 'b1, 'b0])
```

In the above declaration, the type of the variable `e` is `(bit * bit[4])`. In general the type of a tuple with hardware types `'hv1` and `'hv2` is expressed as `('hv1 * 'hv2)`.

Finally, one may express a hardware value with temporal type using the following syntax:

```
val f := 'b0 with {latency = 4}
```

In the above declaration, the type of the variable `f` is `bit @ 4`. The above declaration leverages the annotation syntax, which will be elaborated upon in a later section.

This concludes the syntax for declaring hardware value types, and their representation. Since the definitions are recursive, one can define arbitrarily complex value types if desired while still forming a type that is expressible in hardware.

The final hardware type is the hardware module, which has the following general syntax:

```
module g x := ...; y
```

There are a few things to note. Firstly, `g` is declared as a `module` instead of a `val`. Secondly, it is supplied a single argument, in this case named `x`. Lastly, after performing any number of operations, it returns a single value, in this case named `y`. The type of any module is `'hv1 -> 'hv2` where `'hv1` and `'hv2` are arbitrary hardware value types.

3.2.2 Software

One of the software types is the hardware type. This means that any of the aforementioned types can be represented in software which is obviously necessary in order to write programs in this language.

The first pure software type is the integer. Although integers are represented as arrays of bits in hardware, they are not explicitly interpreted as such. The semantics of an integer do not exist in the realm of hardware, and as such this type is defined solely in software. Integers adhere to the same semantics as in most conventional programming languages. The syntax for declaring an integer is as follows:

```
val f := 1024
val g := -42
```

Another pure software type is the floating point decimal. These are also represented as sequences of bits in hardware, although that is not always their interpretation. Floats adhere to the same semantics as in most conventional programming languages, with the following syntax:

```
val h := 5601.23
val i := -101.
```

Our first augmentation of the software type introduces the dynamically-sized list. There is no strict size specification for such entities, as they can contain software types and do not need to be translated directly to hardware. The syntax to declare a list is as follows:

```
val j := [2, 3, 5, 7, 11, 13, 17]
val k := 2::[3, 5, 7, 11, 13, 17]
```

Note that it is possible to declare a dynamically-sized list of hardware value types, since the notation for a fixed-sized array includes the pound sign, but such a list will not be expressible as a hardware value. In the above syntax, the first expression is a literal declaration of a list of the first 7 prime numbers. The second expression results in the same list, but leverages the `::` operator in order to append an element to the list. This operator, and more, will be discussed in a later section.

Similarly to the tuples seen in section 3.2.1, there exist software tuples, which are declared in the same syntax:

```
val l := (2, 10.5)
val m := ([1, 2, 3], 'b0')
```

Note that tuples are declared in the same way for software and hardware value types, and so the compiler performs type inference to determine the kind of tuple. A tuple composed purely of hardware value types is a hardware value tuple, whereas a tuple containing any software type is a software tuple. We stray from the fact that a hardware type is a software type in this case when determining the type category of tuple members.

The last software type is the function. This is a construct that allows some function to be performed on a software-typed input to produce a software-typed output. The general declaration for a function is as follows:

```
fun f arg0 arg1 . . . argn = res
```

If `arg0` has type `'sw0`, `arg1` has type `'sw1`, and so on, and `res` has type `'swx`, then the function `f` has type `'sw0 -> 'sw1 -> . . . -> 'swn -> 'swx`.

Since functions themselves are values, a function may be passed as a parameter to a different function. This is demonstrated by the `map` function, which has signature `('a -> 'b) -> 'a list -> 'b list`. Similarly, tuples can be passed as values, as demonstrated by the `#1` function, which has the signature `('a * . . .) -> 'a`.

3.3 Operations

With a specification of the syntax responsible for declaring various types, we must now discuss the syntax for performing operations on these. Given our language is strongly typed, it does not support operator overloading as type inference would then fail. Instead, there are distinct operators that only permit certain types to be provided. These operators can be thought of as functions.

3.3.1 Bitwise operators

The first kind of operator we introduce is the bitwise operator. These operators accept any hardware type. It should be noted that the binary operators in this class must be supplied hardware types of the same exact structure. That is, an `and` operation cannot be performed between a tuple of bits and an array of bits.

1. `&`: bitwise and
2. `|`: bitwise or
3. `^`: bitwise xor
4. `~`: bitwise complementation
5. `<<`: logical left shift
6. `>>`: logical right shift
7. `>>>`: arithmetic right shift

The first three operators in the above list are binary and (in prefix form) have the functional type `'h -> 'h -> 'h`. The fourth operator in the above list is unary and has the functional type `'h -> 'h`. The last three operators are binary and have the functional type `bit[n] -> bit[m] -> bit[n]`. The first argument is the bit pattern to shift, the second argument is the bit pattern representing the amount to shift, and the result is the shifted bit pattern. Often, calls to these shift operators will require conversions from integers to bit patterns (which will be discussed in a later section).

3.3.2 Arithmetic operators

The second kind of operator we introduce is the arithmetic operator. These operators only accept the software constructs integer and float. Furthermore, as mentioned earlier, there is no operator overloading so arithmetic between integers and floats differ in syntax.

The operators for integer arithmetic are shown below.

1. `+`: integer addition
2. `-`: integer subtraction
3. `/`: integer division
4. `*`: integer multiplication
5. `%`: integer modulo

All of these operators (in prefix form) have the type `int -> int -> int`.

The operators for floating point arithmetic are shown below

1. `+.:` floating point addition
2. `-.:` floating point subtraction
3. `/.:` floating point division
4. `*.:` floating point multiplication

All of these operators (in prefix form) have the type `float -> float -> float`.

3.3.3 Conditional operators

In this language, integers possess boolean semantic value. An integer value of 0 denotes a false condition, whereas any other integer value denotes a true condition. Using this system, we define three conditional operators.

1. `&&`: logical conjunction
2. `||`: logical disjunction
3. `!`: logical negation

The first two operators are binary and (in prefix form) have functional type `int -> int -> int`. The last operator is unary and has type `int -> int`.

3.3.4 Comparison operators

The language also provides operators to perform comparisons of various types. These operators are generic, and when declaring types they may be overridden to specify the semantics of comparisons for that type (this will be discussed later).

1. `==`: equal
2. `<>`: not equal
3. `>`: greater-than
4. `>=`: greater-than-or-equal-to
5. `<`: less-than
6. `<=`: less-than-or-equal-to

All of these operators (in prefix form) have type `'a -> 'a -> int`, where the returned integer represents the boolean result.

The core library provides definitions of these comparison operators for bits, bit arrays, integers, and floats.

3.3.5 Tuple operators

The language provides built-in operators (which, recall, are no different from functions) intended for use with tuples.

1. `#i`: element retrieval (`'a1 * ... * 'ai * ... * 'an`) `-> 'ai`

3.4 Conversions

With the above established syntax for declaring and operating on types, there is a need to further establish a system by which one can convert from a given type to another. For example, when performing a shift, programmers must specify the shift amount as a bit pattern. It may be desired to shift according to the value of some integer, in which case a conversion must be made explicitly before calling the operator. This further enforces the dichotomy by making programmers explicitly cast from one type to another.

3.4.1 Hardware-to-software

The two most common representation of data in hardware is a bit and an array of bits. We therefore provide mechanisms to convert from either of these data types to software types.

The function `bit_to_int` converts a bit to an integer value. Namely, `'b0` converts to 0 and `'b1` converts to 1. Since integers are used to represent boolean

condition values, this mechanism helps in performing conditional logic based on bit values.

The function `bits_to_int` converts a bit array to an integer value. This function has type `bit[n] -> int`, and returns the integer value represented by a sequence of bits. Note that the input bit array can be of any size, although arrays that exceed size 32 will not produce valid outputs.

The function `bits_to_float<s, e, m>` converts a bit array to a floating point value according to the convention supplied by parameters `s`, `e`, and `m`. In particular, these represent the number of sign bits, exponent bits, and mantissa bits respectively, used to interpret the input bit array. This function has type `bit[n] -> float` where $s + e + m = n$ must hold, and returns the floating point value represented by the input sequence of bits.

3.4.2 Software-to-hardware

The conversions above may also be performed in reverse. We specify such conversions in this section.

The function `int_to_bit` converts an integer to a bit value. Namely, 0 converts to `'b0` and any other integer converts to `'b1`. Since integers are used to represent boolean condition values, this mechanism helps in converting a boolean condition into a bit value.

The function `int_to_bits<n>` converts an integer to a bit array. This function has type `int -> bit[n]`, and returns the a sequence of bits corresponding to the input integer. Note that the output bit array can be of any size. The supplied integer may not be expressible in the provided number of bits, in which case the remaining bits will simply be neglected.

The function `float_to_bits<s, e, m>` converts a floating point value to a bit array according to the convention supplied by parameters `s`, `e`, and `m`. In particular, these represent the number of sign bits, exponent bits, and mantissa bits respectively, used to assemble the output bit array. This function has type `float -> bit[n]` where $s + e + m = n$ must hold, and returns the bit array representing the input floating point value.

3.5 Temporal typing

The semantics of temporal typing are discussed in this section. As an example, consider the built-in function `dff` which has type `('a -> 'a) @ 1`. This indicates that the output of the function is only valid after 1 clock cycle.

In the case that temporal types are not explicitly defined, the compiler infers the temporal type of a function. For example, consider the following function:

```
fun f a b c =
  let
    val x := dff(dff(a))
    val y := dff(dff(b))
    val z := dff(dff(c))
  in
    (x, y, z)
  end
```

This function has the type $(\text{'a} * \text{'b} * \text{'c}) \rightarrow (\text{'a} * \text{'b} * \text{'c}) @ 2$. This is because all outputs have the same clock delay.

In the event where there are differing clock delays, we specify temporal type for each output. For example, consider the modified function:

```
fun f a b c =
  let
    val x := dff(a)
    val y := dff(dff(b))
    val z := dff(dff(dff(c)))
  in
    (x, y, z)
  end
```

This function has the type $(\text{'a} * \text{'b} * \text{'c}) \rightarrow (\text{'a} @ 1 * \text{'b} @ 2 * \text{'c} @ 3)$.

In the event where outputs are formed as a result of different temporal types, we state that the temporal type is undefined. For example, consider the further modified function:

```
fun f a b c =
  let
    val x := dff(a)
    val y := dff(dff(b))
    val z := dff(dff(dff(c)))
  in
    (x > y, y = z, z)
  end
```

Now, function `f` has type $(\text{'a} * \text{'b} * \text{'c}) \rightarrow (\text{'a} @ -1 * \text{'b} @ -1 * \text{'c} @ 3)$.

Functions can enforce their temporal type, and also the temporal type of parameters. Temporal subtyping operates in that `'a @ n` is a subtype of `'a @ m` if and only if $n \leq m$. This allows shorter-timed cycles to be passed and padded in order to meet the temporal constraints of the supertype.