

# Models of Computation: Assessed Coursework I

This is the first assessed coursework for Models of Computation. You should submit your answers to these questions in hard copy by 12 noon on Friday, the 18th of November, 2016. This work may be submitted individually or completed in pairs. If you opt to work as a pair, please set up appropriate groups in CaTE in the usual way.

## While with Dynamically Allocated Memory

The WHILE language that we have considered so far in this course lacks a number of features that are common to most programming languages. For this coursework, we consider an extension of WHILE that supports dynamically allocated memory.

The language WHILEDM extends WHILE by allowing pairs of values to be dynamically created and manipulated. A pair is analogous to an object in Java with two fields, **fst** and **snd**. Pairs are constructed by the **newpair** expression, which returns the address of a freshly allocated pair. If  $E$  is an expression that evaluates to the address of a pair,  $E.\text{fst}$  returns the contents of its **fst** field and  $E.\text{snd}$  returns the contents of its **snd** field. The command  $\text{fst}[E] \leftarrow E'$  stores the value of  $E'$  in the **fst** field of the pair addressed by  $E$ ; similarly, the command  $\text{snd}[E] \leftarrow E'$  stores the value of  $E'$  in the **snd** field of  $E$ .

The syntax of the language WHILEDM is defined by:

$$\begin{aligned} E \in \text{Expr} &::= x \mid n \mid E + E \mid \text{newpair} \mid E.\text{fst} \mid E.\text{snd} \\ B \in \text{Bool} &::= \text{true} \mid \text{false} \mid E = E \mid E < E \mid B \ \& \ B \mid \neg B \\ C \in \text{Comm} &::= \text{skip} \mid x := E \mid C; C \mid \text{if } B \text{ then } C \text{ else } C \\ &\quad \text{while } B \text{ do } C \mid \text{fst}[E] \leftarrow E \mid \text{snd}[E] \leftarrow E \end{aligned}$$

where

- $x \in \text{Var}$  ranges over the set of program variables, and
- $n \in \mathbb{N}$  ranges over natural numbers.

Expressions in WHILEDM can evaluate either to numbers or to addresses of pairs. To represent the results obtained from evaluating an expression, which include values that may be stored in variables, we define the set of WHILEDM *values*, denoted  $\text{Val}$ , as follows:

$$v \in \text{Val} ::= n \mid \lceil a \rceil$$

where  $a \in \text{Addr}$  ranges over addresses and  $\lceil a \rceil$  denotes the literal address corresponding to  $a$ . The set of addresses is defined to be the set of positive natural numbers:  $\text{Addr} = \mathbb{N}^+ = \{1, 2, 3, \dots\}$ . A value is, by definition, either a number  $n$  or a literal address  $\lceil a \rceil$ . In particular 7 and  $\lceil 7 \rceil$  denote *different* values, the first being a number and the second a literal address.

In WHILE, the program state was simply a partial function from variables to numbers. For WHILEDM, the program state is extended in two ways. Firstly, variables can store literal addresses as well as numbers. Secondly, the state must record the values stored in pairs that have been allocated. To this end, *states* of WHILEDM are pairs  $(s, h)$ , where

- $s : Var \rightarrow Val$  is a *variable store*, which is a partial function mapping program variables to values, and
- $h : Addr \rightarrow_{fin} Val$  is a *heap*, which is a finite partial function mapping addresses to values.

For a partial function  $f : A \rightarrow B$ , we write  $\text{dom}(f)$  for the subset of  $A$  on which  $f$  is defined. We write  $\emptyset$  for the partial function which is undefined everywhere.

If  $a$  is the address of a pair in the heap  $h$ , then we define the **fst** field of the pair to be  $h(a)$  and the **snd** field of the pair to be  $h(a + 1)$ . That is, a pair is represented by two consecutive locations in the heap that store the values of its fields. The address of the pair is the address of the first of these locations.

We will define a big-step semantics for WHILEDM.

1. **(Expressions)** The big-step semantics for WHILEDM expressions is given by the relation  $\Downarrow_e \subseteq (Expr \times Store \times Heap) \times (Val \times Store \times Heap)$ . For an expression  $E$ , a value  $v$ , variable stores  $s, s'$  and heaps  $h, h'$ ,

$$\langle E, s, h \rangle \Downarrow_e \langle v, s', h' \rangle$$

means that  $E$  evaluates to  $v$  in the state  $(s, h)$ , resulting in the updated state  $(s', h')$ .

The rules defining  $\Downarrow_e$  are as follows:

(E.NUM) In the state  $(s, h)$ , a number  $n$  evaluates to itself. The state remains unchanged.

$$\text{E.NUM} \frac{}{\langle n, s, h \rangle \Downarrow_e \langle n, s, h \rangle}$$

(E.VAR) If, in the state  $(s, h)$ , the variable  $x$  is in the domain of the store  $s$ , then the expression  $x$  evaluates to the value of  $x$  in the store. The state remains unchanged.

$$\text{E.VAR} \frac{x \in \text{dom}(s)}{\langle x, s, h \rangle \Downarrow_e \langle s(x), s, h \rangle}$$

(E.ADD) If, in the state  $(s, h)$ , an expression  $E_1$  evaluates to the number  $n_1$ , updating the state to  $(s', h')$  and if, in the state  $(s', h')$ , an expression  $E_2$  evaluates to the number  $n_2$ , updating the state to  $(s'', h'')$ , then, in the state  $(s, h)$ , the expression  $E_1 + E_2$  evaluates to the sum of  $n_1$  and  $n_2$ , updating the state to  $(s'', h'')$ .

$$\text{E.ADD} \frac{\langle E_1, s, h \rangle \Downarrow_e \langle n_1, s', h' \rangle \quad \langle E_2, s', h' \rangle \Downarrow_e \langle n_2, s'', h'' \rangle}{\langle E_1 + E_2, s, h \rangle \Downarrow_e \langle n_1 \pm n_2, s'', h'' \rangle}$$

(E.NEW) If, in the state  $(s, h)$ , both the address  $a$  and the address  $a + 1$  are not in the domain of the heap  $h$ , then, in the state  $(s, h)$ , the expression **newpair** evaluates to the literal address  $\ulcorner a \urcorner$ , updating the state to  $(s, h[a \mapsto 0][a + 1 \mapsto 0])$ , that is, extending the heap with the pair  $(0, 0)$  at address  $a$ .

$$\text{E.NEW} \frac{a \notin \text{dom}(h) \quad (a + 1) \notin \text{dom}(h)}{\langle \text{newpair}, s, h \rangle \Downarrow_e \langle \ulcorner a \urcorner, s, h[a \mapsto 0][a + 1 \mapsto 0] \rangle}$$

(E.FST) If, in the state  $(s, h)$ , an expression  $E$  evaluates to the literal address  $\lceil a \rceil$ , updating the state to  $(s', h')$ , and if the address  $a$  is in the domain of the heap  $h'$ , then, in the state  $(s, h)$ , the expression  $E.\text{fst}$  evaluates to the value that is at address  $a$  in the heap  $h'$ , updating the state to  $(s', h')$ .

$$\text{E.FST} \frac{\langle E, s, h \rangle \Downarrow_e \langle \lceil a \rceil, s', h' \rangle \quad a \in \text{dom}(h')}{\langle E.\text{fst}, s, h \rangle \Downarrow_e \langle h'(a), s', h' \rangle}$$

(E.SND) If, in the state  $(s, h)$ , an expression  $E$  evaluates to the literal address  $\lceil a \rceil$ , updating the state to  $(s', h')$ , and if the address  $a + 1$  is in the domain of the heap  $h'$ , then, in the state  $(s, h)$ , the expression  $E.\text{snd}$  evaluates to the value that is at address  $a + 1$  in the heap  $h'$ , updating the state to  $(s', h')$ .

$$\text{E.SND} \frac{\langle E, s, h \rangle \Downarrow_e \langle \lceil a \rceil, s', h' \rangle \quad a + 1 \in \text{dom}(h')}{\langle E.\text{snd}, s, h \rangle \Downarrow_e \langle h'(a + 1), s', h' \rangle}$$

Given these big-step rules for expressions, answer the following questions.

(a) Give a derivation of

$$\langle (\text{newpair.fst}) + (\text{newpair.snd}), \emptyset, \emptyset \rangle \Downarrow_e \langle 0, \emptyset, (1 \mapsto 0, 2 \mapsto 0, 5 \mapsto 0, 6 \mapsto 0) \rangle$$

- (b) Give an expression and initial state that can be evaluated to more than one final configuration, and give at least two such final configurations.
- (c) Is the semantics of expressions deterministic? Give a proof that it is, or a counterexample showing that it isn't.
- (d) Find an expression that can be evaluated in one state but cannot be evaluated in another state. Give that expression, the state in which it can be evaluated, and the state in which it cannot be evaluated.
- (e) Give an expression that cannot be evaluated in any state. Briefly justify your answer.
- (f) Is the semantics of expressions total? Give a proof that it is, or a counterexample showing that it isn't.

2. (**Boolean Expressions**) Let  $BVal = \{\text{true}, \text{false}\}$  be the set of Boolean values. The big-step semantics for WHILEDM Boolean expressions is given by the relation

$$\Downarrow_b \subseteq (Bool \times Store \times Heap) \times (BVal \times Store \times Heap).$$

For a boolean expression  $B$ , a boolean value  $b$ , variable stores  $s, s'$  and heaps  $h, h'$ ,

$$\langle B, s, h \rangle \Downarrow_b \langle b, s', h' \rangle$$

means that  $B$  evaluates to  $b$  in the state  $(s, h)$ , resulting in the updated state  $(s', h')$ .

The rules for  $=$  and  $<$  are as follows:

(B.EQ.TRUE) If, in the state  $(s, h)$ , an expression  $E_1$  evaluates to the value  $v$ , updating the state to  $(s', h')$  and if, in the state  $(s', h')$ , an expression  $E_2$  evaluates to the same value  $v$ , updating the state to  $(s'', h'')$ , then, in the state  $(s, h)$ , the expression  $E_1 = E_2$  evaluates to **true**, updating the state to  $(s'', h'')$ .

$$\text{B.EQ.TRUE} \frac{\langle E_1, s, h \rangle \Downarrow_e \langle v, s', h' \rangle \quad \langle E_2, s', h' \rangle \Downarrow_e \langle v, s'', h'' \rangle}{\langle E_1 = E_2, s, h \rangle \Downarrow_b \langle \text{true}, s'', h'' \rangle}$$

(B.EQ.FALSE) If, in the state  $(s, h)$ , an expression  $E_1$  evaluates to the value  $v_1$ , updating the state to  $(s', h')$  and if, in the state  $(s', h')$ , an expression  $E_2$  evaluates to the value  $v_2$ , updating the state to  $(s'', h'')$ , and if  $v_1 \neq v_2$ , then, in the state  $(s, h)$ , the expression  $E_1 = E_2$  evaluates to **false**, updating the state to  $(s'', h'')$ .

$$\text{B.EQ.FALSE} \frac{\langle E_1, s, h \rangle \Downarrow_e \langle v_1, s', h' \rangle \quad \langle E_2, s', h' \rangle \Downarrow_e \langle v_2, s'', h'' \rangle \quad v_1 \neq v_2}{\langle E_1 = E_2, s, h \rangle \Downarrow_b \langle \text{false}, s'', h'' \rangle}$$

(B.LT.TRUE) If, in the state  $(s, h)$ , an expression  $E_1$  evaluates to the number  $n_1$ , updating the state to  $(s', h')$  and if, in the state  $(s', h')$ , an expression  $E_2$  evaluates to the number  $n_2$ , updating the state to  $(s'', h'')$ , and if  $n_1 < n_2$  in the sense of natural numbers, then, in the state  $(s, h)$ , the expression  $E_1 < E_2$  evaluates to **true**, updating the state to  $(s'', h'')$ .

$$\text{B.LT.TRUE} \frac{\langle E_1, s, h \rangle \Downarrow_e \langle n_1, s', h' \rangle \quad \langle E_2, s', h' \rangle \Downarrow_e \langle n_2, s'', h'' \rangle \quad n_1 < n_2}{\langle E_1 < E_2, s, h \rangle \Downarrow_b \langle \text{true}, s'', h'' \rangle}$$

(B.LT.FALSE) If, in the state  $(s, h)$ , an expression  $E_1$  evaluates to the number  $n_1$ , updating the state to  $(s', h')$  and if, in the state  $(s', h')$ , an expression  $E_2$  evaluates to the number  $n_2$ , updating the state to  $(s'', h'')$ , and if  $n_1 \geq n_2$  in the sense of natural numbers, then, in the state  $(s, h)$ , the expression  $E_1 < E_2$  evaluates to **false**, updating the state to  $(s'', h'')$ .

$$\text{B.LT.FALSE} \frac{\langle E_1, s, h \rangle \Downarrow_e \langle n_1, s', h' \rangle \quad \langle E_2, s', h' \rangle \Downarrow_e \langle n_2, s'', h'' \rangle \quad n_1 \geq n_2}{\langle E_1 < E_2, s, h \rangle \Downarrow_b \langle \text{false}, s'', h'' \rangle}$$

Given these big-step rules for booleans, answer the following questions.

- (a) Complete the big-step semantics of booleans by adding appropriate rules for **true**, **false**, **&** and **¬**. Make sure that your rules:
  - *do not have side conditions* — they should only have premises of the form  $- \Downarrow_b -$ ;
  - *enforce strict evaluation* — both arguments of **&** should always be evaluated, from left to right.
- (b) Find an expression  $E$  such that, for some states  $(s, h), (s', h')$ ,

$$\langle E = E, s, h \rangle \Downarrow_b \langle \text{false}, s', h' \rangle$$

Justify your answer with a derivation.

3. (**Commands**) The big-step semantics for WHILEDM commands is given by the relation

$$\Downarrow_c \subseteq (\text{Comm} \times \text{Store} \times \text{Heap}) \times (\text{Store} \times \text{Heap}).$$

For command  $C$ , variable stores  $s, s'$  and heaps  $h, h'$ ,

$$\langle C, s, h \rangle \Downarrow_c \langle s', h' \rangle$$

means that executing  $C$  in the state  $(s, h)$  results in the state  $(s', h')$ .

The semantic rules for commands are as follows:

(C.SKIP) In the state  $(s, h)$ , executing the command **skip** results in the state  $(s, h)$ .

$$\text{C.SKIP} \frac{}{\langle \text{skip}, s, h \rangle \Downarrow_c \langle s, h \rangle}$$

(C.ASSIGN) If, in the state  $(s, h)$ , an expression  $E$  evaluates to a value  $v$ , updating the state to  $(s', h')$ , then, in the state  $(s, h)$ , executing the command  $x := E$  results in the state  $(s'[x \mapsto v], h')$ , i.e. the value of the variable  $x$  in the store  $s'$  is updated to  $v$ .

$$\text{C.ASSIGN} \frac{\langle E, s, h \rangle \Downarrow_e \langle v, s', h' \rangle}{\langle x := E, s, h \rangle \Downarrow_c \langle s'[x \mapsto v], h' \rangle}$$

(C.SEQ) If, in the state  $(s, h)$ , executing the command  $C_1$  results in the state  $(s', h')$  and if, in the state  $(s', h')$ , executing the command  $C_2$  results in the state  $(s'', h'')$ , then, in the state  $(s, h)$ , executing the command  $C_1; C_2$  results in the state  $(s'', h'')$ .

$$\text{C.SEQ} \frac{\langle C_1, s, h \rangle \Downarrow_c \langle s', h' \rangle \quad \langle C_2, s', h' \rangle \Downarrow_c \langle s'', h'' \rangle}{\langle C_1; C_2, s, h \rangle \Downarrow_c \langle s'', h'' \rangle}$$

(C.IF.TRUE) If, in the state  $(s, h)$ , the boolean  $B$  evaluates to **true**, updating the state to  $(s', h')$  and if, in the state  $(s', h')$ , executing the command  $C_1$  results in the state  $(s'', h'')$ , then, in the state  $(s, h)$ , executing the command **if**  $B$  **then**  $C_1$  **else**  $C_2$  results in the state  $(s'', h'')$ .

$$\text{C.IF.TRUE} \frac{\langle B, s, h \rangle \Downarrow_b \langle \text{true}, s', h' \rangle \quad \langle C_1, s', h' \rangle \Downarrow_c \langle s'', h'' \rangle}{\langle \text{if } B \text{ then } C_1 \text{ else } C_2, s, h \rangle \Downarrow_c \langle s'', h'' \rangle}$$

(C.IF.FALSE) If, in the state  $(s, h)$ , the boolean  $B$  evaluates to **false**, updating the state to  $(s', h')$  and if, in the state  $(s', h')$ , executing the command  $C_2$  results in the state  $(s'', h'')$ , then, in the state  $(s, h)$ , executing the command **if**  $B$  **then**  $C_1$  **else**  $C_2$  results in the state  $(s'', h'')$ .

$$\text{C.IF.FALSE} \frac{\langle B, s, h \rangle \Downarrow_b \langle \text{false}, s', h' \rangle \quad \langle C_2, s', h' \rangle \Downarrow_c \langle s'', h'' \rangle}{\langle \text{if } B \text{ then } C_1 \text{ else } C_2, s, h \rangle \Downarrow_c \langle s'', h'' \rangle}$$

(C.WHILE.FALSE) If, in the state  $(s, h)$ , the boolean  $B$  evaluates to **false**, updating the state to  $(s', h')$ , then, in the state  $(s, h)$ , executing the command **while**  $B$  **do**  $C$  results in the state  $(s', h')$ .

$$\text{C.WHILE.FALSE} \frac{\langle B, s, h \rangle \Downarrow_b \langle \text{false}, s', h' \rangle}{\langle \text{while } B \text{ do } C, s, h \rangle \Downarrow_c \langle s', h' \rangle}$$

(C.WHILE.TRUE) If, in the state  $(s, h)$ , the boolean  $B$  evaluates to **true**, updating the state to  $(s', h')$  and if, in the state  $(s', h')$ , executing the command  $C$  results in the state  $(s'', h'')$ , and if, in the state  $(s'', h'')$ , executing the command **while**  $B$  **do**  $C$  results in the state  $(s''', h''')$ , then, in the state  $(s, h)$ , executing the command **while**  $B$  **do**  $C$  results in the state  $(s''', h''')$ .

$$\text{C.WHILE.TRUE} \frac{\langle B, s, h \rangle \Downarrow_b \langle \text{true}, s', h' \rangle \quad \langle C, s', h' \rangle \Downarrow_c \langle s'', h'' \rangle \quad \langle \text{while } B \text{ do } C, s'', h'' \rangle \Downarrow_c \langle s''', h''' \rangle}{\langle \text{while } B \text{ do } C, s, h \rangle \Downarrow_c \langle s''', h''' \rangle}$$

(C.STOR.FST) If, in the state  $(s, h)$ , the expression  $E_1$  evaluates to a literal address  $\lceil a \rceil$ , updating the state to  $(s', h')$ , and if, in the state  $(s', h')$ , the expression  $E_2$  evaluates to a value  $v$ , updating the state to  $(s'', h'')$ , and if the address  $a$  is in the domain of the heap  $h''$ , then, in the state  $(s, h)$ , executing the command  $\text{fst}[E_1] \leftarrow E_2$  results in the state  $(s'', h''[a \mapsto v])$ , i.e. the value at address  $a$  in the heap  $h''$  is updated to  $v$ .

$$\text{C.STOR.FST} \frac{\langle E_1, s, h \rangle \Downarrow_e \langle \lceil a \rceil, s', h' \rangle \quad \langle E_2, s', h' \rangle \Downarrow_e \langle v, s'', h'' \rangle \quad a \in \text{dom}(h'')}{\langle \text{fst}[E_1] \leftarrow E_2, s, h \rangle \Downarrow_c \langle s'', h''[a \mapsto v] \rangle}$$

(C.STOR.SND) If, in the state  $(s, h)$ , an expression  $E_1$  evaluates to a literal address  $\lceil a \rceil$ , updating the state to  $(s', h')$ , and if, in the state  $(s', h')$ , an expression  $E_2$  evaluates to a value  $v$ , updating the state to  $(s'', h'')$ , and if the address  $a + 1$  is in the domain of the heap  $h''$ , then, in the state  $(s, h)$ , executing the command  $\text{snd}[E_1] \leftarrow E_2$  results in the state  $(s'', h''[a + 1 \mapsto v])$ , i.e. the value at  $a + 1$  in the heap  $h''$  is updated to  $v$ .

$$\text{C.STOR.SND} \frac{\langle E_1, s, h \rangle \Downarrow_e \langle \lceil a \rceil, s', h' \rangle \quad \langle E_2, s', h' \rangle \Downarrow_e \langle v, s'', h'' \rangle \quad a + 1 \in \text{dom}(h'')}{\langle \text{snd}[E_1] \leftarrow E_2, s, h \rangle \Downarrow_c \langle s'', h''[a + 1 \mapsto v] \rangle}$$

Given these big-step rules for commands, answer the following questions.

(a) Consider the configuration  $\langle C, s, h \rangle$  given by

$$\begin{aligned} C &\equiv x := c.\text{fst}; \text{fst}[c] \leftarrow y; c := c.\text{snd} \\ s &\equiv (c \mapsto \lceil 5 \rceil, x \mapsto 12, y \mapsto 12) \\ h &\equiv (1 \mapsto 7, 2 \mapsto \lceil 5 \rceil, 3 \mapsto \lceil 1 \rceil, 4 \mapsto 0, 5 \mapsto 3, 6 \mapsto \lceil 3 \rceil) \end{aligned}$$

Find the state  $(s', h')$ , such that  $\langle C, s, h \rangle \Downarrow_c \langle s', h' \rangle$ , and give the full corresponding derivation. You may assume that the sequencing operator  $;$  is right-associative, i.e. that  $C_1; C_2; C_3$  is interpreted as  $C_1; (C_2; C_3)$ .

(b) Consider the configuration  $\langle C, s, h \rangle$  given by

$$\begin{aligned} C &\equiv c := hd; \\ &\quad \text{while}(\neg(c = 0))\{ \\ &\quad \quad y := x; \\ &\quad \quad x := c.\text{fst}; \\ &\quad \quad \text{fst}[c] \leftarrow y; \\ &\quad \quad c := c.\text{snd} \\ &\quad \} \\ s &\equiv (hd \mapsto \lceil 1 \rceil, x \mapsto 7) \\ h &\equiv (1 \mapsto 12, 2 \mapsto \lceil 5 \rceil, 3 \mapsto \lceil 1 \rceil, 4 \mapsto 0, 5 \mapsto 3, 6 \mapsto \lceil 3 \rceil) \end{aligned}$$

Find the state  $(s', h')$ , such that  $\langle C, s, h \rangle \Downarrow_c \langle s', h' \rangle$ .

4. **(Some Simple Types)** Consider a simple type system for WHILEDM. The types are defined by the following grammar:

$$\tau \in Type ::= \text{nat} \mid (\tau, \tau)$$

A value  $v$  has type  $\text{nat}$  if it is a number. A value  $v$  has type  $(\tau_1, \tau_2)$  if it is a literal address whose first field is mapped to a value of type  $\tau_1$  and whose second field is mapped to a value of type  $\tau_2$ . To formalise this, we define the relation  $h \Vdash v : \tau$ , which should be read as “value  $v$  has type  $\tau$  in heap  $h$ ” with the following rules:

$$\frac{n \in \mathbb{N}}{h \Vdash n : \text{nat}} \quad \frac{h \Vdash h(a) : \tau_1 \quad h \Vdash h(a+1) : \tau_2}{h \Vdash \ulcorner a \urcorner : (\tau_1, \tau_2)}$$

In order to type all WHILEDM expressions, we must be somehow able to type variables. This is accomplished with a *typing context*  $\Gamma \in Var \rightarrow Type$ , which is a partial function from variables to their types. We say that a state  $(s, h)$  is compatible with a typing context  $\Gamma$  if, for every variable that  $\Gamma$  associates with a type, the value of that variable (interpreted in  $s$  and  $h$ ) actually has the type that  $\Gamma$  said it would have. We write this as  $\Gamma; s; h \vdash \text{well-typed}$ , which is formally defined as:

$$\Gamma; s; h \vdash \text{well-typed} \iff \forall x \in \text{dom}(\Gamma). h \Vdash s(x) : \Gamma(x)$$

Implicitly,  $\Gamma; s; h \vdash \text{well-typed}$  requires that every variable in  $\text{dom}(\Gamma)$  is also in  $\text{dom}(s)$ .

Now, we can define the typing judgement for expressions,  $\Gamma \vdash E : \tau$ , which should be read as “expression  $E$  has type  $\tau$  in context  $\Gamma$ ”. The rules for this typing judgement are as follows:

$$\frac{\Gamma(x) = \tau}{\Gamma \vdash x : \tau} \quad \frac{n \in \mathbb{N}}{\Gamma \vdash n : \text{nat}} \quad \frac{\Gamma \vdash E_1 : \text{nat} \quad \Gamma \vdash E_2 : \text{nat}}{\Gamma \vdash E_1 + E_2 : \text{nat}} \\ \frac{}{\Gamma \vdash \text{newpair} : (\text{nat}, \text{nat})} \quad \frac{\Gamma \vdash E : (\tau_1, \tau_2)}{\Gamma \vdash E.\text{fst} : \tau_1} \quad \frac{\Gamma \vdash E : (\tau_1, \tau_2)}{\Gamma \vdash E.\text{snd} : \tau_2}$$

Given these typing rules, answer the following questions.

- (a) Consider the heap

$$h \equiv (1 \mapsto \ulcorner 3 \urcorner, 2 \mapsto \ulcorner 1 \urcorner, 3 \mapsto \ulcorner 5 \urcorner, 4 \mapsto 2, 5 \mapsto 6, 6 \mapsto \ulcorner 7 \urcorner, 7 \mapsto 3, 8 \mapsto 0)$$

- i. For what type  $\tau$ , if any, does  $h \Vdash \ulcorner 3 \urcorner : \tau$  hold?
- ii. For what type  $\tau$ , if any, does  $h \Vdash \ulcorner 9 \urcorner : \tau$  hold?
- iii. For what type  $\tau$ , if any, does  $h \Vdash \ulcorner 1 \urcorner : \tau$  hold?

- (b) Consider the state

$$s \equiv (x \mapsto 5, y \mapsto \ulcorner 3 \urcorner, z \mapsto \ulcorner 7 \urcorner) \\ h \equiv (1 \mapsto \ulcorner 3 \urcorner, 2 \mapsto \ulcorner 1 \urcorner, 3 \mapsto \ulcorner 5 \urcorner, 4 \mapsto 2, 5 \mapsto 6, 6 \mapsto \ulcorner 7 \urcorner, 7 \mapsto 3, 8 \mapsto 0)$$

For each of the following typing contexts  $\Gamma$ , does  $\Gamma; s; h \vdash \text{well-typed}$ ?

- i.  $\Gamma \equiv (x \mapsto \text{nat}, z \mapsto (\text{nat}, \text{nat}))$
- ii.  $\Gamma \equiv (x \mapsto (\text{nat}, (\text{nat}, \text{nat})), y \mapsto ((\text{nat}, (\text{nat}, \text{nat})), \text{nat}), z \mapsto (\text{nat}, \text{nat}))$
- iii.  $\Gamma \equiv (y \mapsto ((\text{nat}, (\text{nat}, \text{nat})), \text{nat}), z \mapsto (\text{nat}, \text{nat}))$

- (c) The guiding principle of type systems is that “well-typed programs can’t go wrong”. In particular, we want that every expression  $E$  of type  $\tau$  can always evaluate to an answer of type  $\tau$ . Prove, by induction on the structure of  $E$ , that, for all typing contexts  $\Gamma$ , expressions  $E$ , types  $\tau$ , and states  $(s, h)$ , if

$$\Gamma; s; h \vdash \text{well-typed} \quad \text{and} \quad \Gamma \vdash E : \tau$$

then there exist a value  $v$  and a state  $(s', h')$ , such that

$$\langle E, s, h \rangle \Downarrow_e \langle v, s', h' \rangle, \quad \Gamma; s'; h' \vdash \text{well-typed}, \quad \text{and} \quad h' \Vdash v : \tau.$$

**Attention!** This proof must not be longer than **three sides of A4** and all cases must be given explicitly. All content beyond the first three sides will be disregarded. You may use the following lemma without proof:

**Lemma 1.** *If  $\Gamma; s; h \vdash \text{well-typed}$  and  $a, a+1 \notin \text{dom}(h)$ , then  $\Gamma; s; h[a \mapsto 0][a+1 \mapsto 0] \vdash \text{well-typed}$ .*

The following question is *not assessed*. Please do not include it in your submission.

5. (**Unassessed: Garbage Collection**) WHILEDM does not have any commands for deallocating memory. In order to efficiently implement such a language, it is generally necessary to have a garbage collector, which periodically deallocates memory cells that can no longer be reached by the program. As well as deallocating memory, the garbage collector is allowed to move already-allocated memory, provided that it updates all references to the memory.

To model the effects of a garbage collector, we will consider a (partial) equivalence on states. We will identify states as equivalent if there is a one-to-one mapping between the reachable portions of the heap. The garbage collector may only change the state to an equivalent state.

Given a relation  $R \subseteq \text{Addr} \times \text{Addr}$  and values  $v, v'$ , we say that  $v$  and  $v'$  are *equivalent with respect to  $R$*  and write  $v \sim_R v'$  if either:

- $v = v' = n$  for some number  $n$ , or
- $v = \ulcorner a \urcorner$  and  $v' = \ulcorner a' \urcorner$  for some  $(a, a') \in R$ .

Given a relation  $R \subseteq \text{Addr} \times \text{Addr}$  and states  $(s, h)$  and  $(s', h')$ , we say the states are *equivalent with respect to  $R$*  and write  $(s, h) \sim_R (s', h')$  if the following conditions hold:

- $R$  is an invertible mapping, that is:
  - for all  $a_1, a_2, a_3 \in \text{Addr}$ , if  $(a_1, a_2) \in R$  and  $(a_1, a_3) \in R$  then  $a_2 = a_3$ , and
  - for all  $a_1, a_2, a_3 \in \text{Addr}$ , if  $(a_2, a_1) \in R$  and  $(a_3, a_1) \in R$  then  $a_2 = a_3$ ;
- $\text{dom}(s) = \text{dom}(s')$
- for every variable  $x \in \text{dom}(s)$ ,  $s(x) \sim_R s'(x)$ ;
- for all  $(a, a') \in R$ , it is the case that  $a \in \text{dom}(h)$ ,  $a' \in \text{dom}(h')$  and  $h(a) \sim_R h'(a')$ ;
- for all  $(a, a') \in R$ , it is the case that  $a+1 \in \text{dom}(h)$ ,  $a'+1 \in \text{dom}(h')$  and  $h(a+1) \sim_R h'(a'+1)$ .

We say that states  $(s, h)$  and  $(s', h')$  are equivalent, and write  $(s, h) \sim (s', h')$ , if, for some  $R$ ,  $(s, h) \sim_R (s', h')$ .



(a) Let

$$s_1 = (x \mapsto \ulcorner 33 \urcorner, y \mapsto \ulcorner 15 \urcorner, z \mapsto 20) \quad s_2 = (x \mapsto \ulcorner 5 \urcorner, y \mapsto \ulcorner 29 \urcorner, z \mapsto 20)$$

$$h_1 = \left( \begin{array}{ll} 1 \mapsto \ulcorner 15 \urcorner, & 2 \mapsto \ulcorner 21 \urcorner, \\ 7 \mapsto 4, & 8 \mapsto \ulcorner 21 \urcorner, \\ 15 \mapsto \ulcorner 33 \urcorner, & 16 \mapsto \ulcorner 21 \urcorner, \\ 21 \mapsto \ulcorner 33 \urcorner, & 22 \mapsto \ulcorner 7 \urcorner, \\ 33 \mapsto \ulcorner 15 \urcorner, & 34 \mapsto \ulcorner 21 \urcorner \end{array} \right) \quad h_2 = \left( \begin{array}{ll} 1 \mapsto \ulcorner 5 \urcorner, & 2 \mapsto \ulcorner 7 \urcorner, \\ 5 \mapsto \ulcorner 29 \urcorner, & 6 \mapsto \ulcorner 1 \urcorner, \\ 7 \mapsto 4, & 8 \mapsto \ulcorner 1 \urcorner, \\ 13 \mapsto 7, & 14 \mapsto \ulcorner 17 \urcorner, \\ 17 \mapsto 17, & 18 \mapsto \ulcorner 13 \urcorner, \\ 29 \mapsto \ulcorner 5 \urcorner, & 30 \mapsto \ulcorner 1 \urcorner \end{array} \right)$$

Find a relation  $R$  (a set of pairs of addresses) such that  $(s_1, h_1) \sim_R (s_2, h_2)$ .

(b) Show that, for all states  $(s_1, h_1), (s'_1, h'_1), (s_2, h_2), (s'_2, h'_2)$  and addresses  $a, a'$  if

$$(s_1, h_1) \sim (s'_1, h'_1)$$

$$\langle \text{newpair}, s_1, h_1 \rangle \Downarrow_e \langle \ulcorner a \urcorner, s_2, h_2 \rangle$$

and  $\langle \text{newpair}, s'_1, h'_1 \rangle \Downarrow_e \langle \ulcorner a' \urcorner, s'_2, h'_2 \rangle$

then there is some  $R$  with  $(a, a') \in R$  and  $(s_2, h_2) \sim_R (s'_2, h'_2)$ . [Partial marks will be awarded for just giving  $R$ . More marks will be awarded for explaining in English why  $(s_2, h_2) \sim_R (s'_2, h'_2)$ . Full marks will be awarded for *proving* that  $(s_2, h_2) \sim_R (s'_2, h'_2)$ .]

(c) Prove by induction on the structure of  $E$  that, for all expressions  $E$ , states  $(s_1, h_1), (s'_1, h'_1), (s_2, h_2), (s'_2, h'_2)$  and values  $v, v'$ , if

$$(s_1, h_1) \sim (s'_1, h'_1)$$

$$\langle E, s_1, h_1 \rangle \Downarrow_e \langle v, s_2, h_2 \rangle$$

and  $\langle E, s'_1, h'_1 \rangle \Downarrow_e \langle v', s'_2, h'_2 \rangle$

then there is some  $R$  with  $v \sim_R v'$  and  $(s_2, h_2) \sim_R (s'_2, h'_2)$ . This result establishes that the semantics of expressions is deterministic up to equivalence.