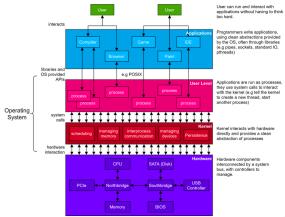
Operating Systems

Overview

Operating System Software that manages hardware and software resources as well as providing clean interfaces/abstractions for running programs to be managed and use these managed resources. OSes manage hardware and software to allow them to interact safely and efficiently so the system can operate.



Managing resources Needs to ensure system resources are usable (not failure, correct restoration of regs), and being used efficiently (switching to other tasks when one is blocked, waiting for IO etc.).

(Cean interface & Abstracts aswy), flower being and the shards are complexity and variability.

Application Programming Interface (API) Interface allowing software to interact.

Portable Operating System Interface (POSIS) Supporting multiple OSEs for an application is difficult because different OS has different interface. POSIS is an interface that could be used to write programs and it is implemented for many OSEs. It is a unix like interface that covers OS interactions like process creation/control, pipes, signals, thread creation, control, synchronization (pithreads).

Sharing resources OSEs share resources they manage (data, software, hardware) between processes/users. Resource allocation must be fair (processes can access system resources they need, no processes excluded from all resource/can monopolia), efficient (low overhead in determining to to manage resources stage and the control of the

isolation/access resources without considering other processes, non can accidentally/maliciously corrupt/damage another).

Concurrency Where tasks can be run in overlapping timeframes, logical/pseudo concurrency where tasks run on single core and are scheduled/descheduled as needed, real/physical concurrency tasks run on multiple core in parallel.

CPU can be interrupted at any time (non-determinism) and can switch activities at arbitrary times. Also, events can occur at unpredictable time/order. Need to provide synchronization primitives, allow processes to run in isolation without being interfered with (own address space).

to provise synamination primitives, allow processes to fail in isolation window tenth great reading the provisers.

File system Os provides abstraction of file system, structure of directories/links/files stored in storage.

Access Controls OS enforces access control to determine which/when processes can run operations on files.

Failure Protection OS needs to detect data corruption, failing disks and create/manage recovery systems.

Organize Devices Storage devices (FDD), SSO Tape) are organized into volumes/partitions/redundant arrays.

Portability OSes manage hardware; however we often want to use same libraries/programs/OSes on many different devices, not feasible to build OS where same binary can run on very different hardware/architectures, however we can make OS that needs little modification when adapting/porting to new architecture.

Most of OS is written in high level language like C that can be recompiled for different hardware, Hardware specific components are separated (e.g., Hardware Abstraction Laper of Windows NT).

Kernel Mode Elevated privileged, full access to hardware. Read/write access to any memory, full access to Lore Upp rivileged instructions/connected devices. OS externel (implements basic OS functionality) runs in kernel mode — needs low level hardware access. Little restrictions can cause problems anywhere in memory.

Vier Mode Unprivileged, use interface provided by kernel (syscall). Can crash/restart, isolated from other processes, can't access privileged instructions, managed by kernel. Processes run in user-mode, considered to be safer but has reduced control over hardware as a result.

Kernel Core of OS, always in memory, implements most of core functionality (managing hardware, resource allocation, protection), runs in kernel/privileged mode with complete access to all hardware.

Acrine Lou O Los, aways in mierulary, implements most or concluding intallinging landware, resource anicculor, proceduring, in its interreprinting entropied induce with complete access to all hardware.

4- of splitting 05 into user/kernel mode +Protection (Prevents system crashes/malfunction with no crash by only running code that needs low level access in kernel mode), +Reliability (Process running in user-mode can crash/restart without crashing system), +Security (Isolates applications as processes running in user-space to restrict damage caused by mallicious program), -Performance (Overhead associated with every transition through kernel-user mode boundary), -

space to restrict drange caused by malicious program). Performance (Overhead associated with every transition through kernel-user mode boundary).

Abstraction (Programmers must use interface) may malicious program.

Abstraction (Programmers must use interface) may be a secretable with own address space. User mode processes use systalls, all parts of kernel execute in one address space, most popular kernel skyle. Performance (All kernel components are in one address space, fist communication), 4cary to write new kernel components, on abstractions). Complexity (Lack of separation, very complex, cannot consider components in isolation). Actor write less the write new kernel components, on abstractions). Complexity (Lack of separation, very complex, cannot consider components in isolation). Actor write less the complex, so difficult to prove correctness/kersl, #@bustness (Complex failing may lead to whole system crash, large kernel = more ways of crashing).

Microkernels Minimal kernel providing very basis functionally, with separate servers for the access, device (1), and over services running as processes in usermode. Kernel provides interprocess communication (used to allow user process/server to interact), servers are processes and can therefore cash/restart/quisted/swapf or differents everevs without having system down -tomplexity (kernel remains mala/imple. Tractable by single developer/hubert), +Correctness (Small/less complex so easy to text/debug, other O'S components e.g., file system/device drivers, can be tested in isolation, easy to extend). +Correctness (Small/less complex so easy to text/debug, other O'S components e.g., file system/device drivers, can be tested in isolation, easy to extend). +Correctness (Small/less complex see easy t

execution/resource utilization (Linux OS).

Server OS Specifically designed to provide services and manage resources for clients or other devices connected to network (Windows Server).

Mainfarmar OS Designed to support large-scale, high-performance computing on mainfarma computers, typically used by organizations with high processing/data storage requirements (IBM JOS).

Embedded OS Specialized software that manages/controls operations of specific device/system, typically with limited resources (FreederTOS).

Mabile Devices OS Software platform that manages/controls operations of mobile devices, providing features and functionalities tailored for handheld usage

Real-Time OS (RTOS) Specialized software that manages/controls operations of system, ensuring timely and deterministic response to events and tasks (QNX).

Linux

Military Bell Labs pioneers Multics but shifts to Unix, leading to its evolutions, the Unix Wars, the creation of GNU and MINIX, and the development of the Linux kernel with Tux as its mascot.

Kernel with Tux as its mascot.

Features Monotike kernel, interrupt handlers are primary means of interaction with devices, IO scheduler used to norder disk onerations. Supports eather in Jernal

rith Tux as its mascot. 8 Monolithic kernel, interrupt handlers are primary means of interaction with devices, IO scheduler used to order disk operations, supports static in-kernel ents and dynamically loadable modules, designed for portability, follow Unix philosophy, exposes many services and devices as files.

conjudents and opnomically disable includes, uselgher of portaining, tolow this principany, exposes many services and uservices as mes. Units Philosophy amountaired as "do one thing and do it well". Dynamically Loadoble Module Modules that can be loaded/unloaded from kernel space as part of kernel, without restarting/rebuilding kernel (ofter drivers and reduce downtime when updating systems).

Windows

History Microsoft's journey includes partnering with IBM, evolving from Windows NT to Windows 11, expanding to mobile devices, introducing Windows
Subsystem for Linux (WSL) for improved compatibility.

Features Hybrid kernel design, dynamic code libraries (DLLs) implement OS services modularly, executive layer contains most services, kernel layer contains thread
scheduling/hynchronization, traps/interrupt handlers, CPU management, HAL separates direct memory access (DMA) operations/BIOS config/CPU architecture
specifics, device drivers are loaded into memory and dynamically linked.

valuntunionized access to system, permit authorized sharing of resources, data confidentiality, data integrity, system availability. Mechanism Security policy specifies what security is provided (what is protected, who has access, what access is permitted), security mechanisms (how ent security policy, same mechanisms can support different policies). Justify Large no of computer crime by insiders, social engineering, people working around security measures for convenience, people with wrong security

Recole security large no of computer crime by insiders, social engineering, people working around security measures for convenience, people with wrong security expectations.

Hardware security with physical access to computer/peripherals one can read contents of memory/disks, listen to network traffic including unencrypted passwords, alter contents of memory/disks, forgree messages on networks, steat machine/set to office the steel can contain exploitable security flaws.

Software security software bugs may allow attackers to compromise system (gain root privileges, crash application, steal data, compromise data integrity, deny access to system), attacks may exploit buffer overflows, integer overflows, format string vulnerabilities).

**Access Contral Authentication – verification of identity of users (principals), authorization – allow principals to perform action only when authorized.

**Access Contral Authentication – verification of identity principal based on personal characteristics (e.g., fregreeprints, violeprinty, retina patterns, can suffer from high equipment cost, false positives/negatives), possessions (e.g., keps, RFID cards, sensors, can suffer from impersonation, high equipment cost, shae positives/negatives), possessions (e.g., keps, RFID cards, sensors, can suffer from impersonation, high equipment cost, shae positives/negatives), possessions (e.g., keps, RFID cards, sensors, can suffer from impersonation, high equipment cost, shae positives/negatives), possessions (e.g., keps, RFID cards, sensors, can suffer from impersonation, high equipment cost, shae positives/negatives), possessions (e.g., keps, RFID cards, sensors, can suffer from impersonation, high equipment cost, shae positives/nega

Protection Domains Set of access rights defined as set of objects, operations permitted on them, principal securing in domain In Das access rights specified by D. Access Control Morth's Specifies sulnorization policy (rows represent principals see, juers, juer groups, ..., columns perspersent traget objects, e.g., flee, devices, processes, Implementation Expensive to implement matrix as global 20 array, two options: access-control lists (AcLs)/ capabilities. Each column on access matrix stored as access control list (ACL), an ACL stores with each object, the principals that can access lit, the operations each principal perform on it. Process Execution If A executes a program, the program runs with AS privileges and can access any file that A has access to notly root can access password file. SETUID programs SUID (set user id) bit — file switches fetcher UID to file owner when executed, increases privileges when using systems of the SETUID programs. Process Dis Each process has 3 lists real UID (id of user who started process), effective UID (effective ID of process which is used in access control hecks), saved UID (saved ID to which the effective ID can be changed to), when a process starts effective UID are all UID, it settled file, effective UID to 10 of file rowner, processes with elevated privileges may temporarily drop privileges changed their UID to an unprivileged value (EUID can be saved as saved UID), Non root processes can change EUID to result UID.

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access to objects)

Bell-La Padula Model Objects and principals have assigned security level (e.g., unclassified, confidential, top secret), two rules (read down, write up): Simple security property: Process running at security level k can read only objects at its level or lower, * property: Process running at security level k can write only objects at its level or higher. No info can leak from higher level to lower one.

Biba Model - Guarantees data integrity (read up, write down) – simple integrity principle: process running at security level k can write only objects at its level or lower (no write up). Integrity * property: process running at security level k can ead only objects at its level/higher (no read down).

Design Principles For Security Give each process least privilege possible (default no access), protection mechanism should be simple/uniform, scheme should be psychologically acceptable, system design should be public (security through obscurity is usually bad idea).

Process Abstraction
Process As funning program. Processes contain state such as: process' own memory (address space) used for heap, stack, static data (such as instructions for program), registers (general purpose for operating on data, and special for PC, stack/frame pointer...), OS provided resources (e.g., process' currently open files). Run in user-mode, and use kernel provided interface for requesting resources, interprocess communication, and management.

Demons processes that run in the background.
Positives: Isolation (Processes isolated from each other, can only affect each other through requests to kernel), Safety (Isolated and can fail in isolation without bringing down of the contains).

entire system). Time Slicing CPU time is split into slices (quanta). At each interval, kernel's process scheduler determines which process gets next slice.

Context Switch CPU switches from executing one process, to another (context being memory map, registers, other process state). Overhead Process state must be saved/restored. Could mean simply extending registers/loading pages to fifty moswap. Caches some need to be invalidated and other will initially miss.

Process Control Block (PCB) Data structure storing info on a process and its state (process id, pointer to its memory map/page table, saved state, allocated resources).

Processes usually created on system initialization or by request of other processes and one of 4 ways (normal completion, abnormal exit (error), aborted, never).

Process bierarchy. Needed to control which processes can terminate/control other processes.

Process is usually created on system initialization or by request of order process and evina. If we have a content of the process here is a content of the process in the p

special pipes stored in file system (can be used like file) that outlive processes that created them.

/*Create new pipe. Params: fd (pipe places fds here. Fd[0] read, fd[1] write). Returns: 0 (success), -1 (failure/error)*/ int pipe (int fd[2]);

Inread Sram of instructions being executed. State defined by CPU (registers, program counter), each thread has own stack (for local variables, frames from function calls), uses address space that may be shared with many other threads (all use same heap). Process has at least one thread, can add more threads for using concurrency in program. "Shared data (threads in process share same address space, so can easily read/write from same memory to communicate), "Acheap Swinting (switching between threads only need to change per-thread state (registers, stack pointer) and many caches remain unchanged (e.g., TLB)), "Cheap Management (Very basic, little state so creating/destroying is much chapper than creating/destroying processes). Concurrency bug (Memory shared between threads, hence need to consider manage/synchronize memory access). "Fork issues (copies calling thread into new process only, if another thread holds a lock, this lock can never be released in new process). Blocking (if one thread block) process the lock. "Process are blocked sometimes of the process are blocked sometimes of the process are blocked sometimes." In the process are blocked sometimes of the process are blocked sometimes. The process are blocked sometimes are supported by the process are blocked sometimes. The process are blocked sometimes of the process are blocked sometimes. The process are blocked sometimes of the process are blocked sometimes." The process are blocked sometimes are processed to be processed to the process are blocked sometimes. The process are blocked sometimes are processed to the processed blocked t

PASSE Threads/Phreads Part of POSIX standards defining an interface for managing (creating, destroying, joining, ...) threads. For phreads functions sinclude sphreads. For phreads the positives influide soyly/types. h>

"Create new thread. Params: thread (pointer to location to store new thread), attr (attributes for each stack, guard size, joineable (joineable), and for positive state of the process that the p

**Pre-emption Where kernel interrupts/suspends a task before it completes/yields. Done through interrupts, when interrupt received CPU switches to executing routine specified as interrupt handler, on timer interrupt handler saves process/thread state, runs scheduler and potentially performs context switch to another scheduled process. Goals Fairness (Comparable Porcess should get comparable CPU time), all run (avoid indefinitely postponing process), max utilization (do not leave CPU idle), min overhead (overhead from context switches, running scheduler), correctness (ensure scheduler correctly implemented). Fair Share Scheduling Each user has ready queue used by scheduler. The scheduler nound robins through users, scheduling processes from their ready queues, in order to fairly distribute CPU time.

**Bacts systems Optimize for throughput/tumaround time, interactive systems response time is critical, real-time systems run jobs to meet deadlines.

**None Pre-Emptive Scheduler Processes run until voluntarily yield control to OS, must trust software, bad for interaction, good for batch systems.

None Pre-Emptive Scheduler Processes run unto voluntarily yield control to Ds, must trust software, bad for interaction, good for for batch systems.

Pre-Emptive Scheduler Requires time interrupts, on time interrupts kernel takes control and switches to another process. Good for interactive processes.

CPU Bound Process Processes that spend majority of time using CPU. Time to run limited by IePU performance. ID Bound Process Spend majority of time waiting for IO, briefly use CPU to inside the CPU consist. In the CPU consist interactive processes sententially serving the CPU performance. ID Bound Process Spend majority of time waiting for IO, briefly use CPU to inside the CPU to inside the CPU consist. In the CPU consist in the CPU consist in the CPU consists of the CPU time, and the CPU consists of the CPU time, and the CPU consist Time (low reproses time (low reposes time for small not of jobs and small time siles). A furnamenout fime (Inigh when processes same/similar size, any turnaround time (Irigh when processes same/similar size, any turnaround tim

increases overhead).

Shortest Lob First (SIF) Schedule ready thread with shortest time remaining (assumes we know how long process will take, non-preemptive), *Turnaround time (optimally low quickest; (SIF) Schedule ready thread with shortest time remaining (assumes we know how long process will take, non-preemptive), *Turnaround time (optimally low quickest; (SIF) Scheduler), *Sontest (SIF) Sch

Time Estimation Runtimes are rarely known in advance can use heuristics based on previous history (not always available), can use user provided estimates (could be inaccurate/malicious).

General Purpose Scheduling Favour short 10 bound jobs, OS determines nature of jobs and adapts to changes. Good resource utilization, short response times, jobs often 10 and CPU bound at different times.

Priority, Scheduling Sche

Synchronization

Advantages of concurrent programming Parallelism (Independent operations can be done in parallel reducing time taken), blocking (avoid blocking operations preventing progress we can separate out different IO and non-IO operations into separate threads/processes to allow progress even when some IO blocks).

Race Condition Type of bug when result of some program is dependent on some non-deterministic ordering of operations. Usually, this refers to order of access to some data by different threads in process (all ahrea have assembled and the requires mutual exclusion.

Critical Section A section of Code accessing some shared resource/data that requires mutual exclusion.

Mutual Exclusion Only one thread can access some shared resource/data to be being nicritical section) at once. Requirements in the context of a critical section: only one thread can be in critical section at once, no thread from outside critical section can prevent other threads from entering it, no thread requiring access to critical section in dischingly one of the context of a critical section is delayed indefinitely, no assumptions are made about when threads will be scheduled/relative execution speed of threads.

Busy Wairing Where a program continually checks on some condition to wait rather than blocking/waiting to be avoiden. Wastes CPU time (constantly rechecking) but avoids any overhead from context whitching to and from, should be used when expected whit is smill provided interrupts and provided interrupts are enabled after the next instruction. I shall prevent the properties of ICIC lear interrupt flag disables interrupts are enabled after the next instruction. I shall prevent the properties of the pr

need unance turns. A continue of the continue

exclusion.

Alomic operation Single assembly instruction cannot be interrupted.

Semaphares Consists of counter representing no of threads that can enter and list of blocked threads waiting to enter that synchronize access to some critical section.

Mutual Exclusion. Semaphore initialized with 1 to enforce mutual exclusion. Ordering Semaphored initialized with value 0 to order operations.

Locke enforce mutual exclusion. A most only one thread can hold lock at any given time, threads can attempt to acquire lock, it if its currently already held by another thread, the acquiring thread is blocked, only thread holding lock can release it, and on release one waiting thread is woken up and acquires the lock, a lock is much like semaphore initialized with value 1, except only holding thread can unlock.

Spin locks Spin locks are locks that busy wait (spin in while loop), does not require kernel involvement – does not need to use syscalls to block/unblock threads, need to ensure check on acquiring locks is atomic, can run into priority inversion problem. If expected wait is low busy waiting may be preferable than blocking threads.

Priority inversion When using priority-based scheduling algorithm, it is possible for low-priority threads to block higher priority threads from running. Low priority thread holds some resource, high priority thread attempts to acquire the resource and is blocked/busy waits. Due to low priority compared with other threads, low priority thread is not run and cannot release resource (could use priority donation).

some resource, high priority thread attempts to acquire the resource and is blocked/busy waits. Due to low priority compared with other threads, low priority thread is not run and cannot release resource (could use priority donations).

**Road/Write Locks Race conditions can only occur when multiple threads access some shared data, and at least one thread is writing. Allows multiple readers to hold a lock, or a single writer. Reduced lock contention as when the read lock is bed, any readers can acquire without blocking. Some read/write lock implementations allow a read lock to be upgraded to a write lock, or a write lock, or a write lock or a write lock to a downgraded to read without requiring the lock to be released and reacquired.

**Producer-Consumer Producer constraints: Can only deposit if there is space in buffer, can only deposit in buffer if mutual exclusion is ensured. Buffer constraints: buffer has limited capacity (0 > N spaces).

**Monitor A synchronization mechanism which enables threads to wait on condition, as well as signal to other waiting threads that indication and any access internal data without going through entry procedures, internal procedures can only be called from within the monitor lock and implicit monitor lock ensures mutual exclusion within the monitor, usually implemented as a language construct.

Condition Variables A condition variable is a flag that represents some high-level condition. There are three main operations on condition variables. Preliable with a condition variable is a flag that represents some high-level condition. There are three main operations on condition variables. Preliable vicinity is a condition variable is a flag that represents some high-level condition. There are three main operations on condition variables. Preliable vicinity is a switched and vicinity is a switched with monitor, is a way are guaranteed the signal and valving for condition. The valving of condition variable is a law are guaranteed the signal and valving for condition. The valvine

Deadlocks Set of processes/threads are deadlocked if each is waiting for event that only another can cause. Resource deadlock is most common. Can occur with single threads/processes (trying to reacquire re-entrant lock). Coffman conditions: Mutual acclusion (each resource is either analysisgened to 1 thread, blod and wait (thread can request new resources while holding other resources) no pre-emption (thread cannot have resources taken away), circular wait (closed chain of threads waiting on each other).

Memory Management

Memory is key component of computer. In Yon Neumann architecture, all data/code stored in same memory systememories used for data/instructions).
Memory management needs to provide memory allocation/memory protection.

ide memory allocation/memory protection. mory addresses generated (don't care about whether it is for instruction, indexing something, ...), No knowledge of what m

address will be used for. Memory Hearth Registers (small/fast), main memory (big/slower), disk (bigger/slower), cache (between registers/main memory has levels 1, 2, 3). Logical Address Generated by CPU, virtual representation of memory location used by process. Address space seen by process; Physical Address Address seen by memory unit, refers to physical system memory. Logical/Physical addresses Same in compile/local didness-bring local faces, different in execution-time address-bring address-bring local faces and the second section of the section of th

Memory Management Unit (MMU) Hardware device for mapping logical to physical addresses. User process deals with logical addresses only. Must be fast therefore implemented in hardware implemented in hardware configuous Memory Allocation Main memory usually split into two partitions: resident OS (kernel) usually held in low memory with interrupt vector, user processes (user) held in high memory.

Low memory essential for system operations and high memory used for application execution.

Configuous indicontion with relocation registers base register contains value of smallest physical address, limit register contains range of logical addresses (each logical address must be less than him register). MMU maps logical address dynamically (e.g., if process has base register = 300040, limit register = 120900, choosing 0 generates address 3000400 = 300004, choosing 120901 is error—beyond address space).

Memory Protection Check memory access is valid to protect user processes from each other and from changing OS code/data.

Mole Available memory block, when here process arrives need to allocate memory from large enough hole.

OS maintains information about allocated partitions and free partitions (hole).

Dynamic Storage Allocation First-All Rolicate first hole that is big enough, Best-fit Allocate big enough smallest hole (must search list for smallest); Worst-fit Allocate largest hole (search entire list for largest).

External fragmentation Total emmory exists to satisfy request but not contiguous; Internal fragmentation Allocated memory larger than requested memory, size difference internal to partition is not used.

External fragmentation Total emmory exists to satisfy request but not contiguous internal fragmentation Allocated memory larger than requested memory, size difference internal to partition is not used.

External fragmentation roleaded by compocition shuffle memory to get free blocks together, 10 bottlenecks.

Swopping Number of processes immeted by amount of available memory to get, swap price of size an

Memory Protection when the protection of the pro

e *kept in memory, ruge-tome base negater proxy* points to page table, *ruge-tome tength negater (PTLN)* indicates size. Illemitted it every div wo memory accesses. *Ne Memory*. Use special fast-lookup hardware **cach**e as associative memory. Supports parallel search. For address translation (p, d) if p in assoc

number otherwise page table.

In Look-Aside Buffer (TLBs) Some store Address-Space IDs (ASIDs) in entries to uniquely identify process to provide address-space protection for that process.

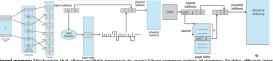
anishon Look-Asse Suppr. (TLBs) Some store Audress-Space Ibs (ASUS) in entries to uniquely identity process to provide address-Space protection for that predet to be flushed after context switch.

Feature Access Time (EAT) Associative lookup = ϵ , assume memory cycle time = 1 jusec, hit ratio (fraction of times page found in associative registers, ratio relationer of associative registers), cst T (ϵ =10 α =(α >[12]-10] = 2 + ϵ - α . Simple of associative registers (α), Cst T (α =10 α =

Page Table Size increases as machine size increases, use hashed page table/inverted page table to not store entry per page but per frame.

Hashed Page Table Hash virtual page number into page table. Page table contains chain of elements hashing to same location, search for match of virtual page in chain, extract corresponding physical frame if match found.

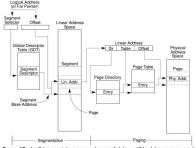
verted Page Table Each entry represents a frame in order, look through to match pid and p, the number of spaces moved down is i which is combined with d to get sisted address.



inared memory Mechanism that allows multiple processes to access/share common region of memory. Enables efficient interprocess communication by eliment of memory is established, no need for kernel involvement.

Mamper Allocates Apard memory commons and advantable and access the shared memory is established, no need for kernel involvement.

get Allocates shared memory segment; shmat Attaches shared memory segment to address space of process; shmct/ Changes properties associated with shared nory segment; shmdt Detaches shared memory segment from process.



Segmentation has independent address space from 0 to some maximum, can grow/shrink independently, supports different kinds of protection, unlike pages, programmers are aware of segments. Memory allocation harder due to variable size (external fragmentation) but good for shared libraries.

Working Set Model - Working set of pages: W(t, w) set of pages referenced by process during process-time interval t-w to t. Add time of last use to Clock Replacement algorithm. At to the Aud time of heart last user to Cuck Replacement agontum.

each page fault, examine page pointed to, if R=1, then set R=0 and move to next page, if R=0, calculate age: if age<working set age w, continue (page in WS), if age>working set age w. if page age, w. on the page in WS), if age>working set age w. if page clean, replace; otherwise, trigger write-back, continue Processes transition between working sets: OS temporarily maintains in memory pages outside of current working set, goal of memory management to reduce misallocation. Could also observe page fault frequency if many faults, allocate more page frames.

Segmentation

Paging

page traines.

Page faults

page page traines.

Paging

page

Page Replacement Find location of desired page on disk, free frame ? use : select victim frame. Read desired page into (newly) freed frame, update page

tables, restart process.

Page Replacement Algorithms Want lowest page-fault rate, page faults decrease with more frames.

First-in-first-Out [FIFO] Algorithm Replace oldest page (may replace heavily used page).

Belady's Anomaly's Ser fames/FIFO objectment, more frames = more page faults but goes down again.

Optimal Algorithm Replace page that won't be used for longest period of time (can't do in practice, used for measuring performance of algorithms).

Less Recently Used (RIU) Algorithms Each entry has counter, page referenced = copy clock into counter, to replace choose lowest counter. Proper IBU is expensive, use approximations instead. Reference bits initially set to 0, when referenced set to 1, replace page with r=0 if exists. Periodically reset reference bits (does not provide rowner IBU order). use approximations instead. Reference bits initially set to 0, when referenced uses to 2, reprincipable
proper LRU order).

Second chance (or clock) page replacement Combines round robin replacement with reference bit r, if page to be replaced in RR order has r=1, set r=0 and in

Second chance (or clock) page replacement. Combines round robin replacement with reference bit r, if page to be replaced in RR order has rs1, set rs0 and increment. BR frame index (peep page in memory), repeat subject to same rules, rof replace page and increment RR index.

Counting Algorithms LFU algorithm replace page with smallest count, may replace page just brough in, never forgets heavily used page (use aging/reset counters), MFU algorithm replace page with largest count.

Locality of Reference (Programs tend to request same pages in space and time) System must maintain program's favoured subset of pages in main memory otherwise thrashing (excessive paging activity causing low processor utilization) may occur.

Locality of Reference (Programs tend to request same pages in space and time) System must maintain program's favoured subset of pages in main memory otherwise thrashing excessive paging activity causing low processor utilization) may occur.

Local Strategy Earl process gets the dail Location of physical memory, need to pick up changes in working set size; Global Strategy Dynamically share memory between runnable processes, initially allocate memory proportional to process size, consider page fault frequency (PFF) to tune allocation (measure page faults per sec and increase/decrease allocation).

Continuation of Linux

files/dirs in FS, stores info relevant to single file/dir e.g., time stamps, permissions, owner), ext2 inode pointers (first 12 directly locate 12 data EA2 londe (represents files/dirs in Fs, stores info relevant to single file/dir e.g., time stamps, permissions, owner), ext2 mode pointers (first 12 directly) cotes 12 data blocks, 13° is inforted pointers) block of pointers to data blocks, 14° is inforted pointers), bright yill direct pointer locates block of double indirect pointers), provides fast access to small files, while supporting very large files, book groups (clusters of configuous blocks, fatternpts to store related data is amen block group, educes seek time for accessing groups of related data), superblock (critical data subout entire Fs), indeed bable (contains entry for each indee in block group), indee allocation bitmap (incides used within block group), block allocation bitmaps (blocks used within group), group descriptor (block numbers for location of indee allocation bitmaps), block allocation bitmaps (incides used within group), group descriptor (block numbers for location of indee allocation bitmaps, block allocation bitmaps (incide successing files on UMX/Innux Users are principals (each user has unique user if (uid), supernear root has UID of and can access any resource), files are objects, groups (each laser and being to 1 groups, each file can only belong to one group), access rights are read (R), write (W), execute (E), For dir, it means read - can list contents of dir, write - can create/delete owned files, execute =

Continuation of Synchronization



Ostrich Algorithm When resource contention is low and deadlocks are very unlikely to occur, we may decide to ignore possibility of deadlock.

Detection and Recovery Opnamically check for deadlock, after system becomes deadlocked, which resources and processes/threads are in cycle and recover. Build RAG, use PSTs to look for cycles, find members of cycle and recover from deadlock by removing one (reveloxing resource). Pre-emption (Talks resource from owner and give to another temporarily), rollback (periodically take snapshot of system's state and rollback to older checkpoint at deadlock), killing processes (select random process and

orderings.

Livelock – Processes/threads are not blocked, but system as a whole does not make progress.

Device Management

Objectives fair access to shared devices (allocation of dedicated devices), exploit parallelism of 10 devices for multiprogramming, provide uniform simple view of 10 (hide complexity of device handling, give uniform naming/error handling). Device independence from device type and device instance; Device varietions: Unit of data transfers: character/block; Supported operations: read/write/sek; Synchronous/Asynchronous operation; Speed differences; Shareable/Single user, Error conditions.

For access device device file that allows sequential, unbifered access to data at character levic, persenting devices that process data character by character device allows data to be read/written in fixed-size blocks, such as hard drives/SSDs.

Interrupt Handler Processes each interrupt, for block devices on transfer completion, signal device handler; for character devices when character transferred, process next.

Device Driver Handle one device type but may control multiple devices of same type, implements block read/write, access device registers, initiate operations

Chief Driver Handle one device type but may control multiple devices of same type, implements block read/write, access device registers, initiate operations, schedule request, shandle errors.

Device Independent OS Layer provides device independence mapping logical to physical devices (naming and switching), request will dation against device characteristics, allocation of adecidated devices, proceeding of the control of t

Disk Management

Sector Layout Surface divided into 20+ zones, outer zones have more sectors per track, ensures sectors have same physical length, zones hidden using virtual geometry.

Disk addressing Physical hardware address: actual geometry is complicated, hidden from OS. Modern disks use logical sector addressing (or logical block addresses LBA) –

Sectors numbered consecutively O.n., makes disk management easier, helps work around BIOS limitations.

Disk formatting low level format Disk sector layout preamble, data, ECC, opiniore skew, interleaving, High level format boot block, free block list, root directory, empty file

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Disk Formatting low level format bios sector layout: preamble, data, ECC, opinder skew, Interdeaving, High level format boot block, free block list, root directory, empty file system.

Physical Disk Sector size 512 bytes, seek time (adjacent cylinder) cl.ms, seek time (average) 8ms, rotation time (average latency) 4ms, transfer rate upwards of 100MB/s. Disk scheduling Minimize seek/latency times, order pending requests with respect to head position, seek time "2-J/s latency."

Disk speciments esek time (Lay, Latency time (totalonal delay) t_{tume} = 12/t, transfer time t_{tume} = 12/t, t

to lated us knier-urinar (and sway). Disk Cache Use main memory for disk sectors (contains copy of some sectors from disk, OS manages disk in terms of blocks-multiple sectors for efficiency), buffer uses finite space (need replacement policy when buffer full). Least Recently Used (IRU) Replace block that was in cache longest with no references, cache consists of stack of blocks (most recently referenced block on top of stack, when block referenced (or brought into cache), place on top of stack, remove block at bottom of stack when new block brought in), don't move blocks around in main memory (us

stack foretrettect or overlage, more steep, put.

The stack of pointers instead), and the stack of pointers instead of the leads to misleading reference count, use frequency-based engagement, pointers instead of the leads to misleading reference count, use frequency-based engagement, pointers instead of pointers instead of the leads to misleading reference count, use frequency-based engagement, pointers instead of the leads to misleading reference count, use frequency-based engagement, pointers instead of the leads to misleading reference count, use frequency-based engagement, pointers instead of the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count, use frequency-based engagement, pointers in the leads to misleading reference count in the leads to misleading referen

File Systems

Objectives Long term, of data of arbitrary size

of data of arbitrary size.

File User Function Create: Create empty file, allocate space and add to directory, Delete: Deallocate space, invalidate/remove directory entry, Open: Search directory for file name, check access validity and set pointers to file, Glose: Removes pointers to file, Read: Access file, update current position pointers, Write: Access file, update pointers, Repositors/peek: et current position to given value, Funcate: Erase contents but keep all other artiruluse; Remover: Change file name. Read attributes: creation ades, size, archive file, ..., Write attributes: protection, immutable filag. ... Size super functions logical name to physical disk address translation, management of disk space, file locking for exclusive access, performance optimization, protection

archive flag. —, Write attributes: protection, immutable flag. ... \$\$ Support Functions logical name to physical disk address translation, management of disk space, file locking for exclusive access, performance optimization, protection against system failure, security.

File Attributes basic info: file name (symbolic name, unique within directory), file type (text, binary, executable, directory), file organization (sequential, random), file creator (program which created file). Address info: volume (disk drive, partition), start address ((cyl, head, sext), LBA), size used, size allocated. Access control information: owner (person who controls file), authentication (password), permitted actions; fead, write, delete for owner/others), logae info: creation timesamp (date/fime), last modified (could include user id), last read, last archived, expiry date (when file will be automatically deleted), access activity counts (number of reads/writes).

Spoce Allocation Oynamic space management (file size naturally variable, space allocated in blocks), choosing blocks size (too large or waste space for small files – more memory needed for buffer space, too small – wastes space for large files – high overhead in terms of management data, high file transfer time: seek time greater than transfer time.

File Allocation Place file data at contiguous addresses on storage device, +Successive logical records typically physically adjacent, External fragmentation, -Poor e if files grow/shrink, -File grows beyond size originally specified and no contiguous free blocks available (transfer to new area, leads to additional IO

Configuous Fire Autocarbon Frider the Data at configuous approaches on storage device, successive logical records typically physically adjusted, Fireth and regimentation, 4-roof performance if their grow/shrink, Filer grows beyond size originally specified and no contiguous free blocks available (translate to new area, leads to additional 10 operations).

Filer their properties of the pr

Linux

22-bit Virtual Memory Layout Processes map kernel to 3-468 virtual address range (user processes can make option calls without TLB flush): kernel maps lower 896M8 of physical memory to its virtual address space All memory access must be virtual but need efficient access to user memory + DMA in low memory, create temporary mappings for 796M6 of physical memory to the virtual address space All memory access must be virtual but need efficient access to user memory + DMA in low memory, create temporary mappings for 796M6 of physical memory to the virtual address space All memory access must be virtual but need efficient access to user memory + DMA in low memory, create temporary mappings for 796M6 of physical memory to the virtual address space and the physical Address Stension (PAE), also contains gage status bits: dirty, read-only, —Pope Replocement Uses variation of food algorithm to approximate IRU page-replacement strategy, memory manager uses two lined indicated need for the physical Address Stension (PAE), also contains gage status bits: dirty, read-only, —Pope Replocement Uses variation of Low designation and the physical Address Stension (PAE), also contains gage status bits: dirty, read-only, —Pope Replocement Uses a desirated with a pages (least face-only used near tail), replace pages in Inactive list; kewaped (swap daemon) pages in Inactive list streatimed when memory low uses dedicated swap partition/file, must handle loxede/scharge pages; published thereal periodically file without the process of the process of the pages of the process of the proces