

DAY-10 #100DAYSOFRTL

Aim:- Implementation of 4-Bit Carry Select Adder using Full Adders and Multiplexers.

RTL CODE:-

```
1 $\to$ ////DATE:-10/01/2024
2 A ///CARRY SELECT ADDER
4 | output Sum, Carry);
5 | assign Sum=A^B^C;
 6 | assign Carry=A&B|B&C|C&A;
 7 \stackrel{\triangle}{=} endmodule
9 module mux(input A,B,sel,
10 | output reg y);
11 \ominus always @(A,B,sel)
12 🕏 begin
13 \(\bar{9}\) if (sel==0)
14 y=A;
15 else
16 ⇔ y=B;
17 ∩ end
18 endmodule
19 module CarrySelectAdder(input [3:0] A,B,
20 input Cin, output Cout,
    output [4:0] Sum);
22 wire [3:0] w0,w1,c0,c1;
25 FullAdder M2(.A(A[1]),.B(B[1]),.C(c0[0]),.Sum(w0[1]),.Carry(c0[1]));
26 FullAdder M3(.A(A[2]),.B(B[2]),.C(c0[1]),.Sum(w0[2]),.Carry(c0[2]));
27 FullAdder M4(.A(A[3]),.B(B[3]),.C(c0[2]),.Sum(w0[3]),.Carry(c0[3]));
28
30 FullAdder M6(.A(A[1]),.B(B[1]),.C(c1[0]),.Sum(w1[1]),.Carry(c1[1]));
31 FullAdder M7(.A(A[2]),.B(B[2]),.C(c1[1]),.Sum(w1[2]),.Carry(c1[2]));
32 | FullAdder M8(.A(A[3]),.B(B[3]),.C(c1[2]),.Sum(w1[3]),.Carry(c1[3]));
33 ¦
34 : ///To select the carry
      mux m5(.A(c0[3]),.B(c1[3]),.sel(Cin),.y(Cout));
      mux m1(.A(w0[0]),.B(w1[0]),.sel(Cin),.y(Sum[0]));
     mux m2(.A(w0[1]),.B(w1[1]),.sel(Cin),.y(Sum[1]));
     mux m3(.A(w0[2]),.B(w1[2]),.sel(Cin),.y(Sum[2]));
39
     mux m4(.A(w0[3]),.B(w1[3]),.sel(Cin),.y(Sum[3]));
40
41 assign Sum[4]=Cout;
42 🖨 endmodule
```

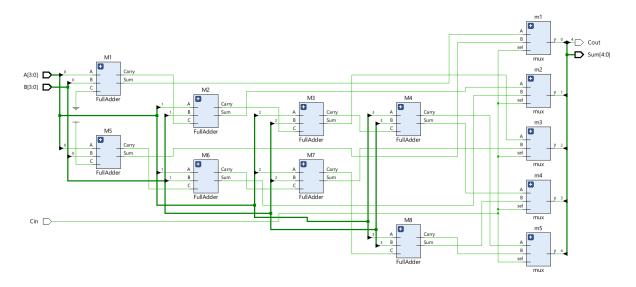
TESTBENCH:-

```
1 module CarrySelectAdder_tb();
2 | reg [3:0] A,B;
3 | reg Cin;
 4 | wire Cout;
 5 | wire [4:0] Sum;
 6 | CarrySelectAdder dut(.A(A),.B(B),.Cin(Cin),.Cout(Cout),.Sum(Sum));
7 - initial begin
   A=4'b0000; B=4'b0000;
9 ! Cin=1;
10 | #10;
11 | $display("A=%d,B=%d,Cin=%b,Cout=%b,Sum=%d",A,B,Cin,Cout,Sum);
12 A=4'b0001; B=4'b0100;
13
    Cin=0;
14
15 Sdisplay ("A=%d,B=%d,Cin=%b,Cout=%b,Sum=%d",A,B,Cin,Cout,Sum);
16 | A=4'b0011; B=4'b0000;
17 ! Cin=1;
18 | #10;
19 | $display("A=%d,B=%d,Cin=%b,Cout=%b,Sum=%d",A,B,Cin,Cout,Sum);
    A=4'b0110; B=4'b1110;
21 ¦ Cin=0;
22 | #10;
23 Sdisplay ("A=%d, B=%d, Cin=%b, Cout=%b, Sum=%d", A, B, Cin, Cout, Sum);
24 | A=4'b1111; B=4'b1111;
25 ¦ Cin=1;
26
    #10;
   $display("A=%d,B=%d,Cin=%b,Cout=%b,Sum=%d",A,B,Cin,Cout,Sum);
28 | A=4'b1000; B=4'b0110;
29 | Cin=0;
31 | $display("A=%d,B=%d,Cin=%b,Cout=%b,Sum=%d",A,B,Cin,Cout,Sum);
33 | Cin=1;
34 #10;
35 | $display("A=%d,B=%d,Cin=%b,Cout=%b,Sum=%d",A,B,Cin,Cout,Sum);
37 | $finish();
38 🖨 end
 39 🖨 endmodule
```

OUTPUT:-

```
A= 0,B= 0,Cin=1,Cout=0,Sum= 1
A= 1,B= 4,Cin=0,Cout=0,Sum= 5
A= 3,B= 0,Cin=1,Cout=0,Sum= 4
A= 6,B=14,Cin=0,Cout=1,Sum=20
A=15,B=15,Cin=1,Cout=1,Sum=31
A= 8,B= 6,Cin=0,Cout=0,Sum=14
A= 6,B=13,Cin=1,Cout=1,Sum=20
```

SCHEMATIC:-



WAVEFORMS:-

Name	Value	0.000 ns	10.000 ns	20.000 ns	30.000 ns	40.000 ns	50.0	00 ns	60.000 ns	70.000 ns
> W A[3:0	1000	0000	0001	0011	0110	1111		1000	0110	
> 💆 B[3:0	0110	0000	0100	0000	1110	1111		0110	1101	
¹ Cin	0									
¹⊌ Cout	0									
> 🚳 Su(01110	00001	00101	00100	10100	11111		01110	10100	

