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| 1. The internal RAM memory o f the 8051 is. 2. 32 Bytes 3. 64 Bytes 4. 128 Bytes 5. 256 Bytes 6. The 8051 has \_\_\_\_\_\_\_ 16-bit counter/timers. 7. 1 8. 2 9. 3 10. 4 11. The 8051 can handle\_\_\_\_\_\_ interrupt sources   Excluding reset.   1. 3 2. 4 3. 5 4. 6 5. The special function registers are maintained in the next 128 location after the general purpose data storage and stack. 6. True 7. False 8. This statement will set the address of the bit to 1 (8051 Micro controller).   SETB 01h  A. True  B. False   1. MOV A,@R1 will 2. Copy R1 to the accumulator 3. Copy the accumulator to R1. 4. Copy the contents of memory whose address is in R1 to the accumulator 5. Copy the accumulator to the contents of memory whose address is in R1. 6. The following program will receive data from port 1, determine whether bit D0 is high, and then send the number FFH to port3.   READ: MOV A,P1  ANL A,#01H  CJNE A,#01H,READ  MOV P3,#0FFH   1. True B. False 2. When Pin No. 31 is connect to VCC and 8051 is reset, the program counter points to the first program instruction in the. 3. Internal code memory 4. External code memory 5. Internal data memory 6. External data memory | | 1. An alternate function of port pin P3.4 in the 8051 is. 2. Timer0 3. Timer1 4. Interrupt 0 5. Interrupt 1 6. The I/0 ports that are used as address and data for external memory are. 7. Ports 1 and 2 8. Ports 1 and 3 9. Ports 0 and 2 10. Ports 0 and 3 11. The total external data memory that can be interfaced to the 8051is. 12. 32K 13. 64k 14. 128K 15. 256k 16. Bit- addressable memory locations are. 17. 10H through 1FH 18. 20H through 2FH 19. 30H through 3FH 20. 40H through 4FH 21. The 8-bit address bus allows access to an address range of. 22. 0000 to FFFFH 23. 000 to FFFH 24. 00 to FFH 25. 0 to FH 26. What is the function of pin described ‘VPP’ at pin no. 31. 27. For external RAM excess. 28. For external ROM excess. 29. For internal ROM excess. 30. Used by programmer kit while programming chip. 31. What is the function of pin described ‘ PROG’ at pin no 30. 32. For external RAM excess. 33. For external ROM excess 34. For internal ROM excess. 35. Used by programmer kit while programming chip. 36. 8051 has. 37. 16-bit address bus 38. 14-bit address bus 39. 10- bit address bus 40. 8- bit address bus 41. 8051 has. 42. 16-bit data bus 43. 14-bit data bus 44. 10- bit data bus 45. 8- bit data bus | |
| 1. One ULN2003A IC can be used to drive. 2. Only 1 stepper motor at a time. 3. 2. Stepper motor at a time 4. 4 stepper motor at a time. 5. Microcontrollers often have. 6. CPU 7. RAM 8. ROM 9. All the above 10. The contents of the accumulator after this operation.   MOV A,#0BH  ANL A,#2CH  Will be   1. 11010111 2. 11011010 3. 00001000 4. 00101000 5. Which of the following instructions will move the contents of register 3 to the accumulator. 6. MOV A,@R3 7. MOV A,03H 8. MOV A,R3 9. Both B and C 10. Instruction MOVC A,@A+DPTR is used for ROM memory. 11. True B. False 12. By default all the Ports are. 13. Input Ports 14. Output Ports 15. Both A. and B. | 1. For 8-bit auto reload in Timer0 count should be loaded to . 2. TH0 Reg. 3. TL0 Reg. 4. Both A. B. 5. TH1 REG. 6. For baud rate selection value is loaded into register 7. Timer 0 8. Timer 1 9. Both A. and B. 10. None of these.   Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_  Marks:\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ | |

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