**Class:** Third Year B.Tech(Computer Science and Engineering)

**Year:** 2025-26 **Semester:** Odd

**Course:** Cutting Edge Technologies Lab

**Course code:** 7CS352

**Practical No. 6**

**Exam Seat No:**

**Title of practical:**

Study and Implementation of Reduction operations and nested loop parallelism using collapse.

1. Matrix Matrix Multiplication
2. Vector Dot Product

**Problem Statement 1:**

**Screenshots:**

**Information and analysis: Tima analysis, Speedup analysis for all programs**

**Problem Statement 2:**

**Screenshots:**

**Information and analysis:**

**Problem Statement 3:**

**Screenshots:**

**Information and analysis:**

**Github Link:**