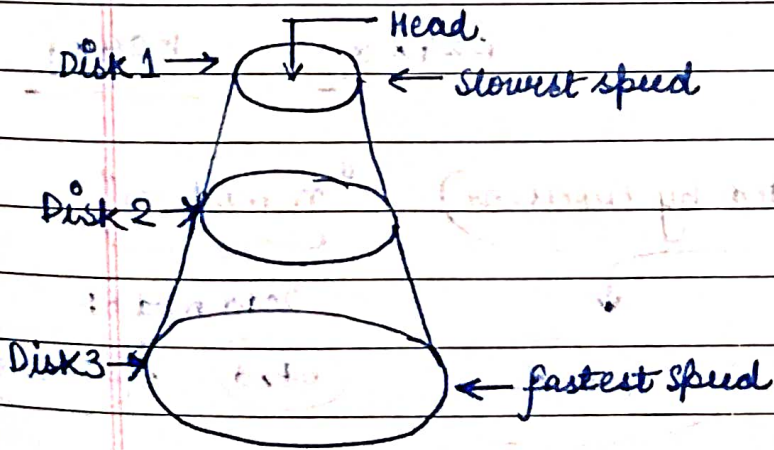
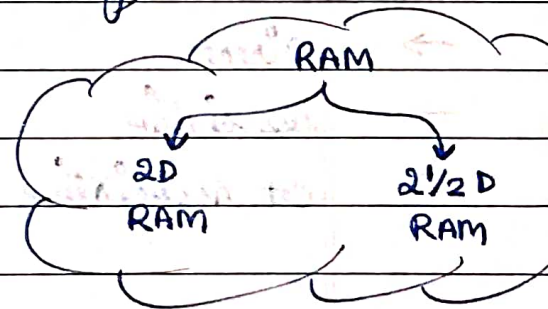
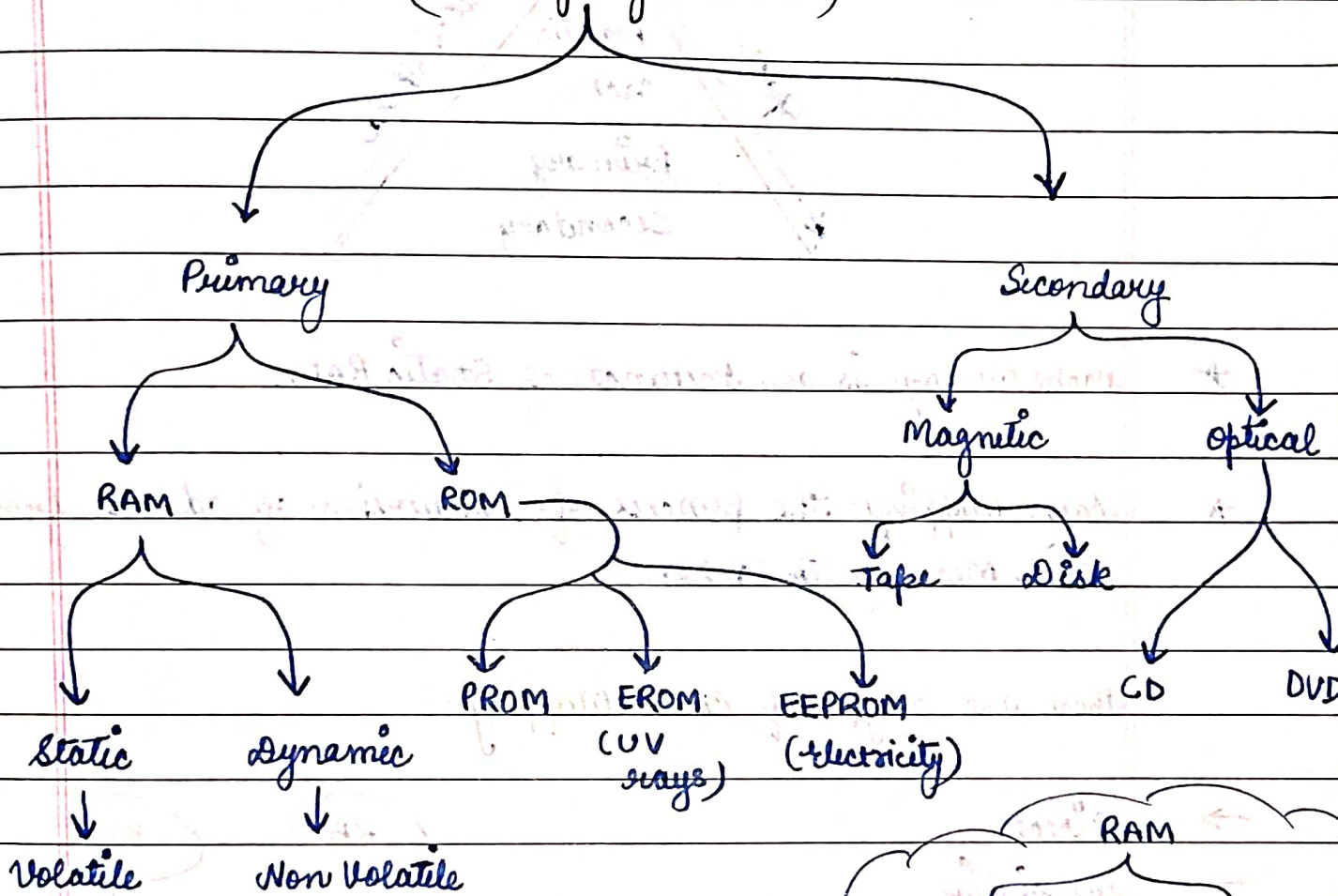
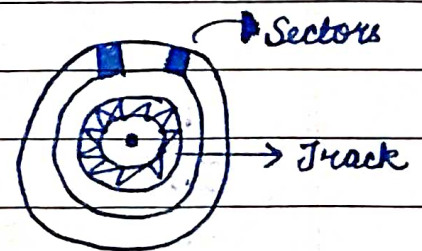


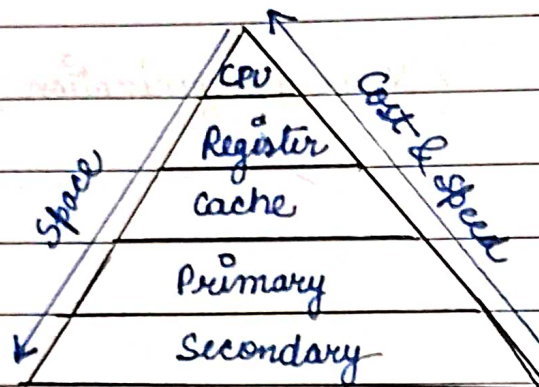
(Unit: 4)

(Memory Organization)



(Cylindrical Structure)





★ Cache memory is an instance of Static RAM.

★ Cache Mapping: The process of conversion of address from main memory to cache.

There are 3 types of Cache Mapping:

→ Direct

→ Associative

→ Set Associative

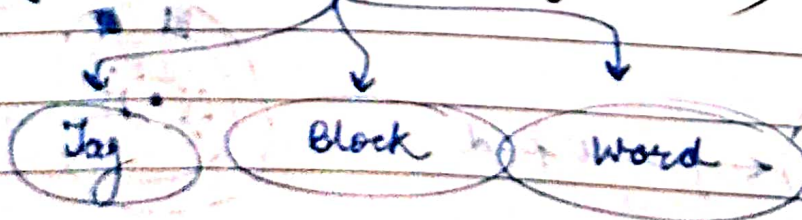
KB

Kilo Byte  
 $1024 \times 8$

Kb

Kilo bits  
 $1024 \times 1$

Physical Address (Generated by Processor)



"n mod m"

$2048 \bmod 64$

$64 \times 8 \rightarrow 2048 \times 8$

$$\# \text{ No. of Tags} = \frac{16 \times 8}{4 \times 8} = 4$$