

Department of Computer Science and Engineering

**FACULTY OF ENGINEERING AND TECHNOLOGY
UNIVERSITY OF LUCKNOW
LUCKNOW**



CS-501

Dr. Zeeshan Ali Siddiqui
Assistant Professor
Deptt. of C.S.E.

PROCESS SYNCHRONIZATION

**(Peterson's Solution
for
The Critical-Section Problem)**

Peterson's Solution_{1/3}

- Peterson's solution is restricted to *two processes* that alternate execution between their *critical sections* and *remainder sections*.
- For convenience, let's represent one *process* as P_i and other one as P_j , here j equals $1 - i$.

Peterson's Solution_{2/3}

- Peterson's solution requires the two processes to *share* two data items:

➤ `int turn;`

- The variable `turn` indicates whose turn it is to *enter* its critical section.
- That is, if `turn == i`, then process P_i is allowed to execute in its critical section.

➤ `boolean flag[2];`

- The flag array is used to indicate if a process is *ready* to enter its critical section.
- For example, if `flag[i]` is true, this value indicates that P_i is ready to enter its critical section.

Peterson's Solution^{3/3}

Process P_i

```
do
{
    flag[i]=true;
    turn = j;
    while(flag[j] && turn == j);

        critical section

    flag[i]=false;

        remainder section

}while(true);
```

Process P_j

```
do
{
    flag[j]=true;
    turn = i;
    while(flag[i] && turn == i);

        critical section

    flag[j]=false;

        remainder section

}while(true);
```

Peterson's Solution: Analysis^{1/3}

- To prove that this solution is correct, we need to show that:
 1. *Mutual exclusion is preserved.*
 2. *The progress requirement is satisfied.*
 3. *The bounded-waiting requirement is met*

Peterson's Solution: Analysis^{2/3}

- To prove property 1, we note that each P_i enters its critical section only if either $\text{flag}[j] == \text{false}$ or $\text{turn} == i$.
- Also note that, if both processes can be executing in their critical sections at the same time, then $\text{flag}[0] == \text{flag}[1] == \text{true}$.
- These two observations imply that P_0 and P_1 could not have successfully executed their while statements at about the same time, since the value of turn can be *either 0 or 1 but cannot be both*.
- Hence, one of the processes: say, P_j —must have successfully executed the while statement, whereas P_i had to execute at least one additional statement (*"turn == j"*).
- However, at that time, $\text{flag}[j] == \text{true}$ and $\text{turn} == j$, and this condition will persist as long as P_j is in its critical section; as a result, mutual exclusion is preserved.

Peterson's Solution: Analysis^{3/3}

- To prove properties 2 and 3, we note that a process *P_i can be prevented from entering the critical section* only if it is stuck in the while loop with the condition *flag[j] == true and turn == j*; this loop is the only one possible.
- If P_j is not ready to enter the critical section, then flag[j] == false, and P_i can enter its *critical section*.
- If P_j has set flag[j] to true and is also executing in its while statement, then either turn == i or turn == j.
 - *If turn == i, then P_i will enter the critical section.*
 - *If turn == j, then P_j will enter the critical section.*
- However, once P_j exits its critical section, it will reset flag[j] to false, allowing P_i to enter its critical section.
- If P_j resets flag[j] to true, it must also set turn to i.
- Thus, since P_i does not change the value of the variable turn while executing the while statement, P_i will enter the critical section (*progress*) after at most one entry by P_j (*bounded waiting*).

References

1. Silberschatz, Galvin and Gagne, “Operating Systems Concepts”, Wiley.
2. William Stallings, “Operating Systems: Internals and Design Principles”, 6th Edition, Pearson Education.
3. D M Dhamdhere, “Operating Systems: A Concept based Approach”, 2nd Edition, TMH.

Thank You.

