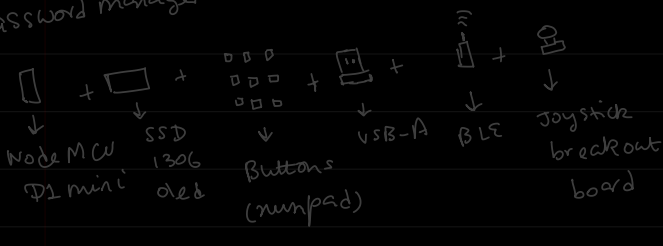


ECE 270 : Embedded Logic Design \Rightarrow Co + Dc

Password manager



Resource for Quiz : hdlbits.01xz.net/wiki/Main_Page
Lab videos : youtube

Programming: 1st half \rightarrow Verilog
2nd half \rightarrow Embedded C

Theory : FPGA and SoC

Arch user repository

Vivado 2019.1 (including SDK)

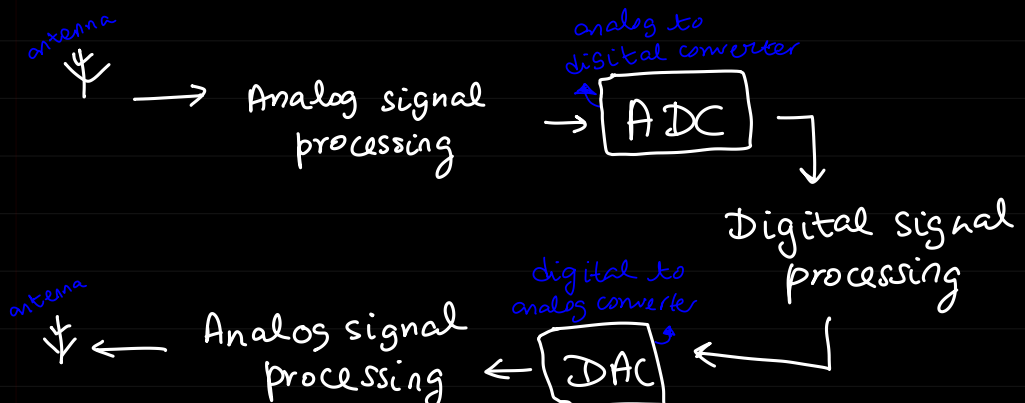
* GRADES:	mid sem	30 %
	end sem	30 %
	Surprise quiz	25 %
	lab hw	15 %

* Which is faster : Analog vs digital?

\Rightarrow Depends on the use case

* No product is purely digital / analog?

\Rightarrow Analog is present in nature however digital can be processed easily and has more use cases.



HDL \rightarrow Hardware Description language
 \hookrightarrow eg \Rightarrow Verilog