tentative date for simulation → 15th April project deadline: before endsern

MOSFET AC analysis (small signal operation)

moster = Amplifier

only in saturation sugion

VOD RAMO VO so that linear amplification is

Vo = VDD - IDRD iDRD = VDD - Vo = VDD - VDS

io = Vop - Vos Rp Ro

 $\frac{\text{case } 1 \rightarrow \text{io} = 0}{4 \text{ Vps} = \text{Voo}}$

CUTOFF (Vi < VT) no chancel induced

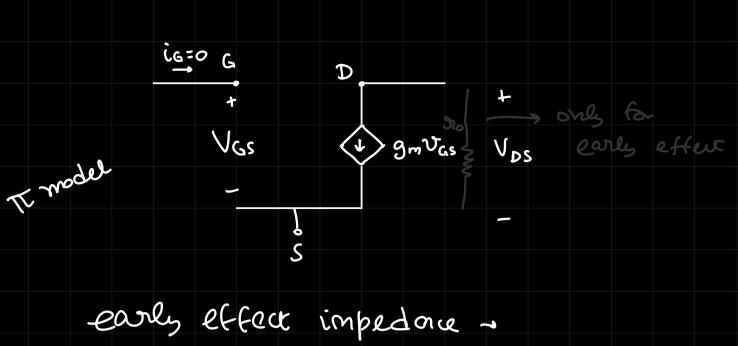
Case 2 > saturation: Vps > Vcs - Vr Vit - Vast - ipt - Vost until Vos = VGs -VT io = Voo - Vos Ro cueff triode non linear because of the -1 Vos quadrate VOS = VGS - VT Vi term in Is in triode (db=180, om of by

linear

non linear

Vgs << Z(VGs-VT)

for saturation ->



$$970 = \frac{1}{2K(V_{GS}-V_{T})^{2}}$$

SMALL SIGNAL OPERATION

1 DC Analysis

$$g_m = 2K(V_{GS} - V_{\Gamma})$$

2 AL onalysis

gain

