

# Physics of semiconductor devices

## Books

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B601

Semiconductor physics and devices by D.A. Neamen

physics of semiconductor devices by S.M. Sze and K.K. Ng

inverter can be used to create all logical operations / gates

increase voltage  $\rightarrow$  amplifier eg : op Amp  
decrease voltage  $\rightarrow$  rectifier

let the threshold current for 'on' state be 10mA.

even then, 9mA will be considered 'on' because there isn't sufficient difference and hence difficult to sense by hardware

$V = IR$   $\rightarrow$  consequence of the Ohm's Law

Ohm's Law  $\rightarrow \bar{J} = \sigma \bar{E}$

which leads to  $V = IR$

Resistance is different for different metals because of mobility

Current : no. of electrons flowing per unit density area per length

$$\sigma \propto \frac{1}{R}$$

$$\sigma = n q \mu_e$$

generally, a system with high  $\sigma \equiv$  metal

Systems with lower  $\sigma \equiv$  semiconductors

electrons are bound with the atom due to electrostatic forces

$$\bar{E} = -\frac{dV}{dx}$$

intel i7 processor has 7bil transistors

processor has  $\rightarrow$  contact pins  
global interconnect } vias  
local interconnect } (metal)  
transistors

The vias enable us to access all billions of transistors using limited  $\sim 100$  contact pins

imp question : which material to be used

## TRANSISTOR

1.2nm	Gate	metal
	SiO <sub>2</sub>	insulator
	silicon substrate	semiconductor

$$10\text{\AA} = 1\text{nm}$$

diameter of an atom:  $\sim 1-2\text{\AA}$

dimension of a transistor:  $\sim 20\text{nm}$

metal gate

insulator

inversion channel

depletion layer

copper and Al

suitable for via

$\rightarrow$  not only cheap

when electrons move between materials, they collide

these collision are inelastic

no conservation of

energy / momentum

most probable

energy here is heat energy

This is the reason filament

bulbs glow

$x$ : real dimension

$k$ : momentum dimension

Passing marks: 25%.	2x Assignment	15%	Relative Grading
	4x Quiz	20%	
	Mid sem	30%	
	End sem	30%	
no N-1 policy		95% + 5% attendance	
	Assignment	①	②
		5/02	8/04
	deadline	15/02	18/04

calculator allowed for quizzes

# Attendance

>60% before & after mid sem	5%
>60% before only	2.5%
after only	2.5%
<60% both	0%