

Lecture: 0

Integrated Electronics

PROF

Dr. Abhijit Mitra

office hour : Friday (11-12)

TF: Suhail Khan and Aafreen

Evaluation

Group

Project : 10

2x Quiz : 10

mid sem : 20

end sem : 30

Lab : 20

Tut and simulation : 10

BOOK

Microelectronic circuits
by A. Sedra and K. Smith
7th edition

GRADING : ?

FOCUS

understand analog elements
and circuits using BJT & CMOS

PROJECT

3 topics will be given
Hardware based

Group of 2

the debugging slots will
be for the project

LAB

LTSpice
PCB design

6 Labs

4 debugging slot

no midsem/endsem

lab viva/practical

EXAM

harder than quiz

bonus : 5% for 75% attendance in lectures

23 lectures

Project starts from week 7

Final Demo : 16th week

informed simulation (not surprise)