

# CoSSJIT: Combining Static Analysis and Speculation in JIT Compilers

**Research Highlights in Programming Languages (RHPL 2025)**

**Aditya Anand\***, Vijay Sundaresan<sup>†</sup>, Daryl Maier<sup>†</sup> and Manas Thakur\*

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# Brief Introduction

- Program Analysis and JIT (Just-In-Time) compilers for Java like languages.
- JITs prioritize fast decisions over precise analysis, resulting in conservative optimization.
- Idea: Staged Static+Dynamic analysis for Managed Runtimes.
  - Offload the costly analysis to static time.
  - Use analysis results in the JIT to enable additional optimizations.

# Staged Analysis

- Optimization: Object Allocation

- By default, **objects** in Java are allocated on the **heap**.

Escape Analysis

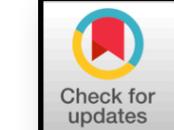
Indirection, Need GC

- Stack Allocation: Allocate method local objects on the stack frame.

Faster access, No GC

- Perform precise (context-, flow-, field-sensitive) **escape analysis statically**.

- Use statically generated escape analysis result to **optimistically** allocate objects on stack at runtime.



## Optimistic Stack Allocation and Dynamic Heapification for Managed Runtimes

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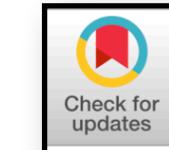
MANAS THAKUR, Indian Institute of Technology Bombay, India

PLDI 24



# Optimistic Stack Allocation

- Can we always rely on the static analysis result ??
- Features like Dynamic ClassLoading (DCL), HotCode Replacement (HCR), Callbacks can make your static analysis result go **unsound**.
- We need to have a fallback mechanism to ensure functional correctness.



## Optimistic Stack Allocation and Dynamic Heapification for Managed Runtimes

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### Example for Dynamic ClassLoading

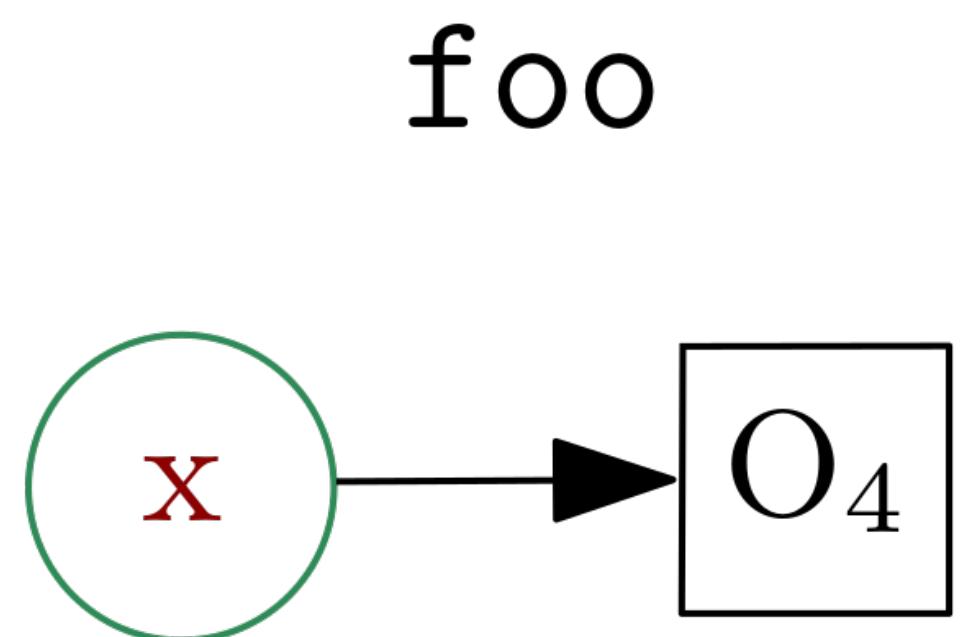
# Dynamic ClassLoading Example

```
1. class A {  
2.     A f;  
3.     void foo(A q, A r) {  
4.         A x = new A(); // O4  
5.         A y = new A(); // O5  
6.         x.f = new A(); // O6  
7.         A p = x.f;  
8.         bar(p, y);  
9.         r.zar(p, q);  
10.    } /* method foo */  
  
11.    void zar(A p, A q) { . . .}  
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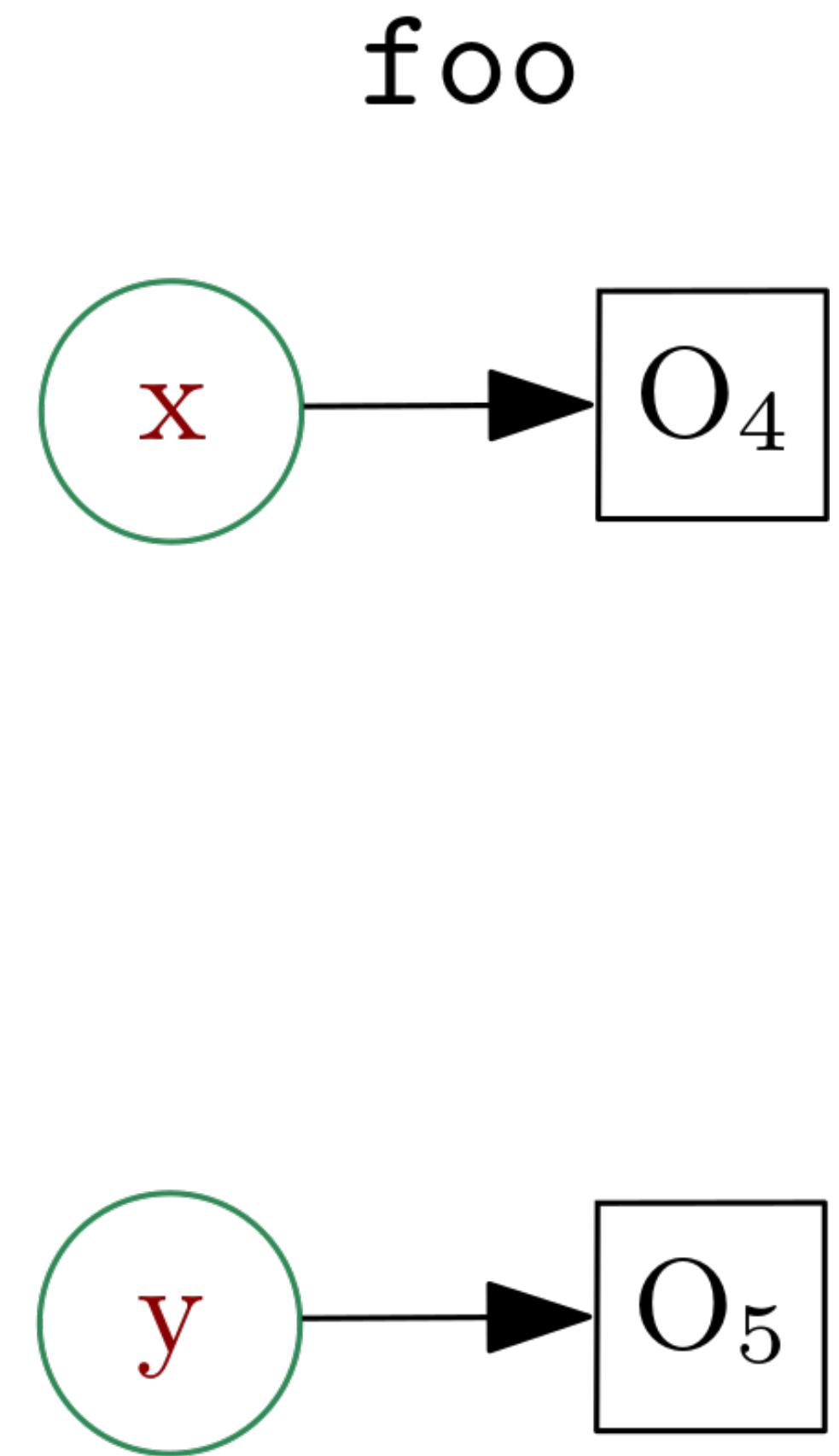
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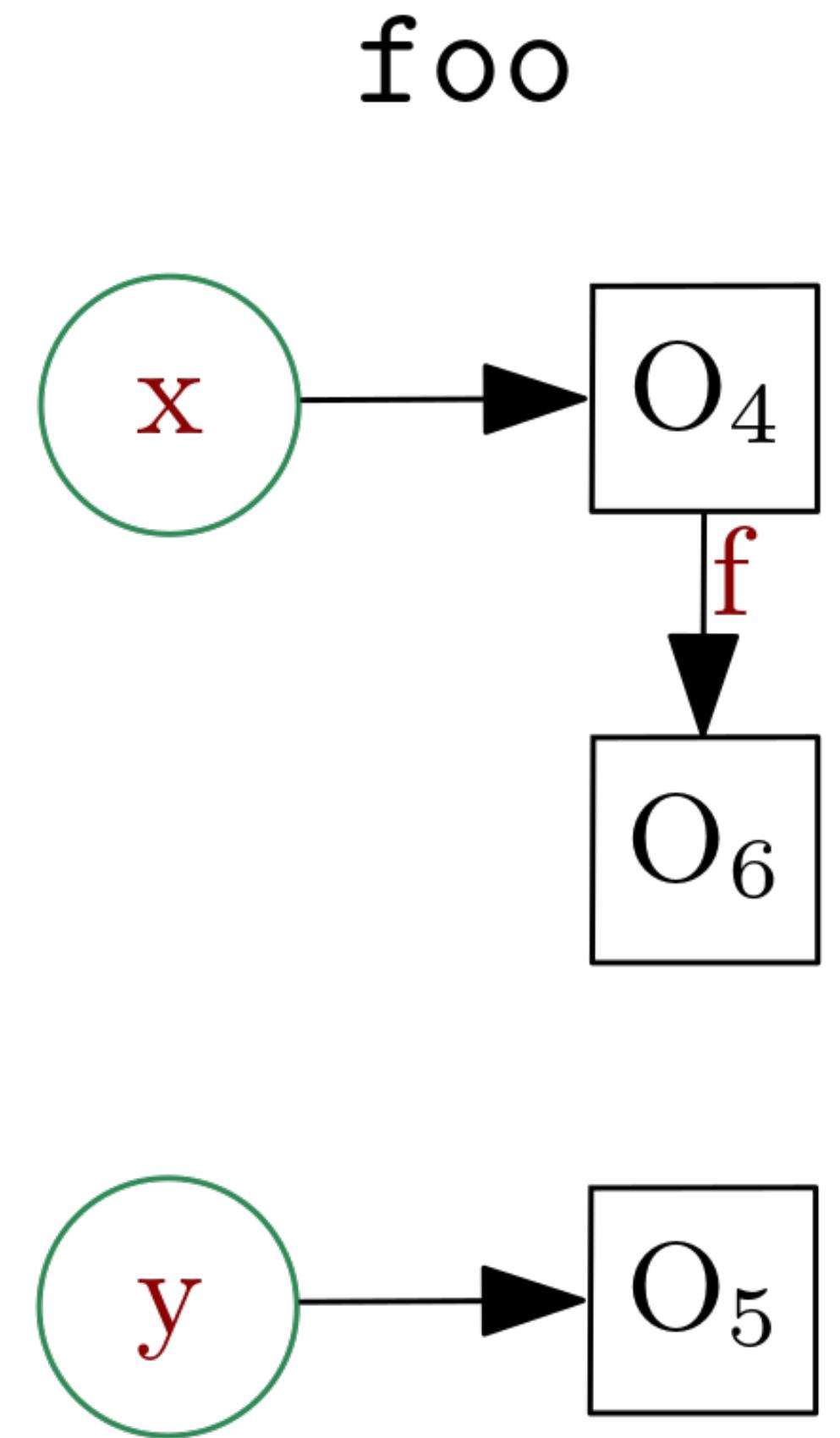
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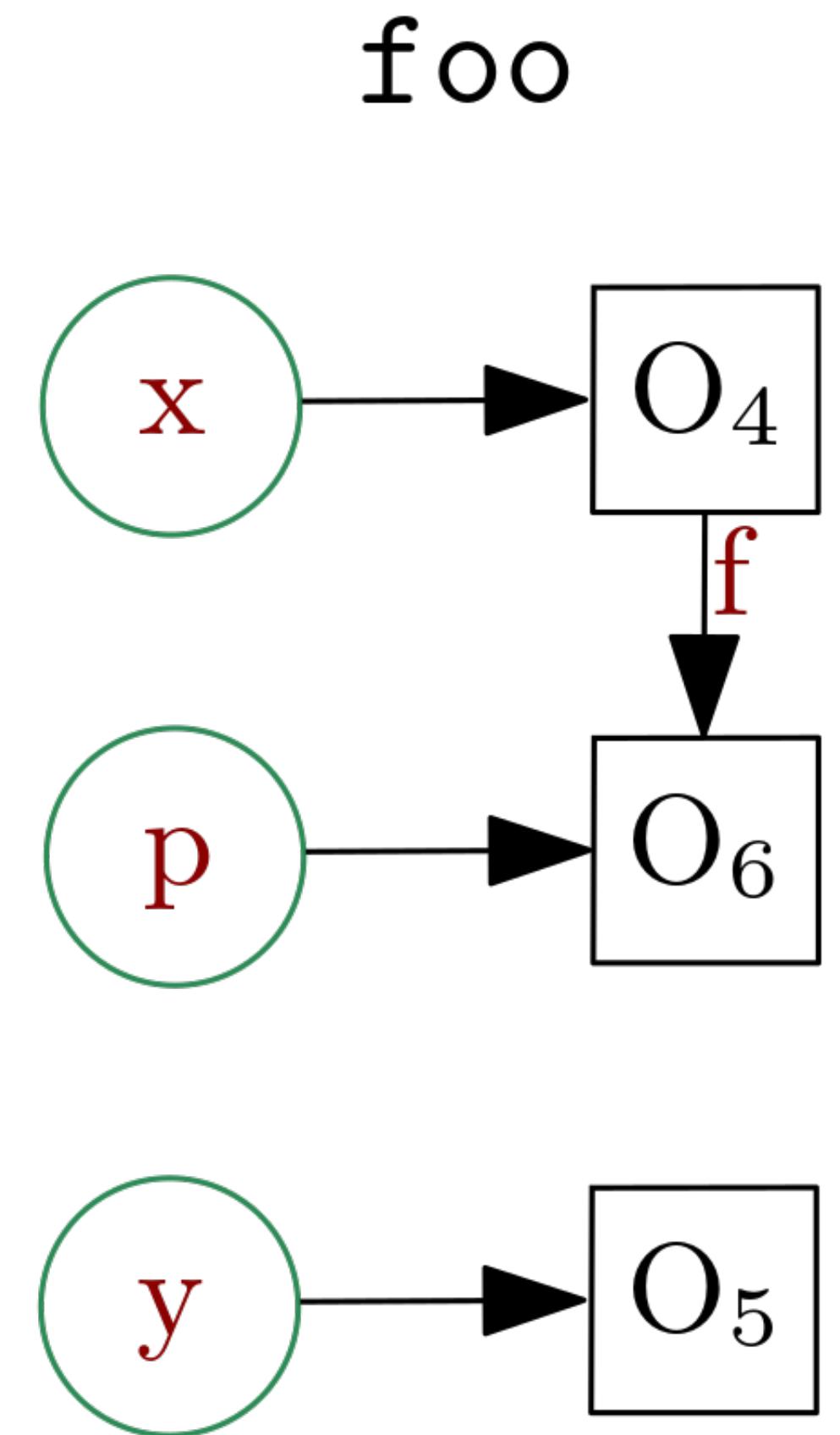
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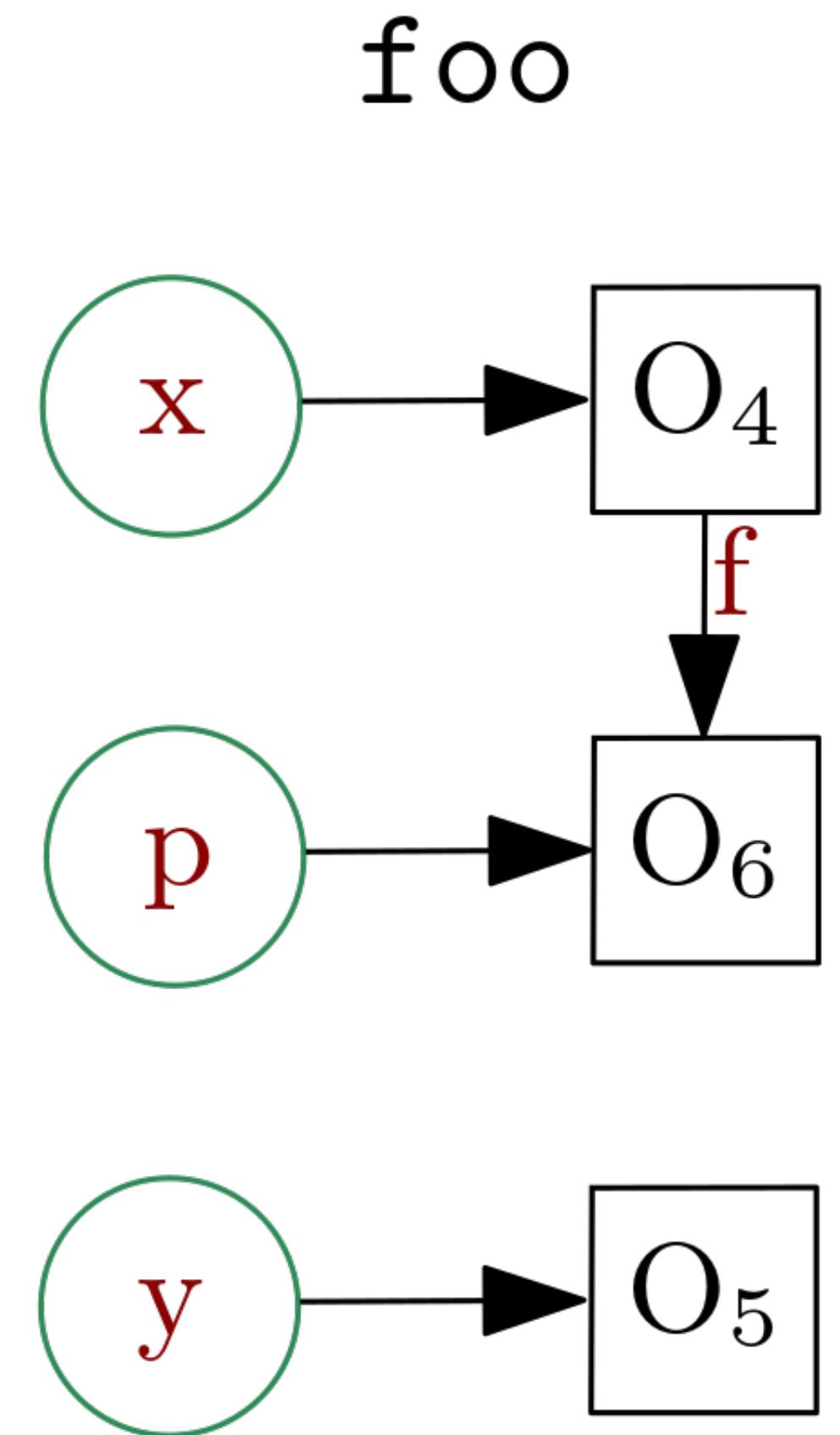
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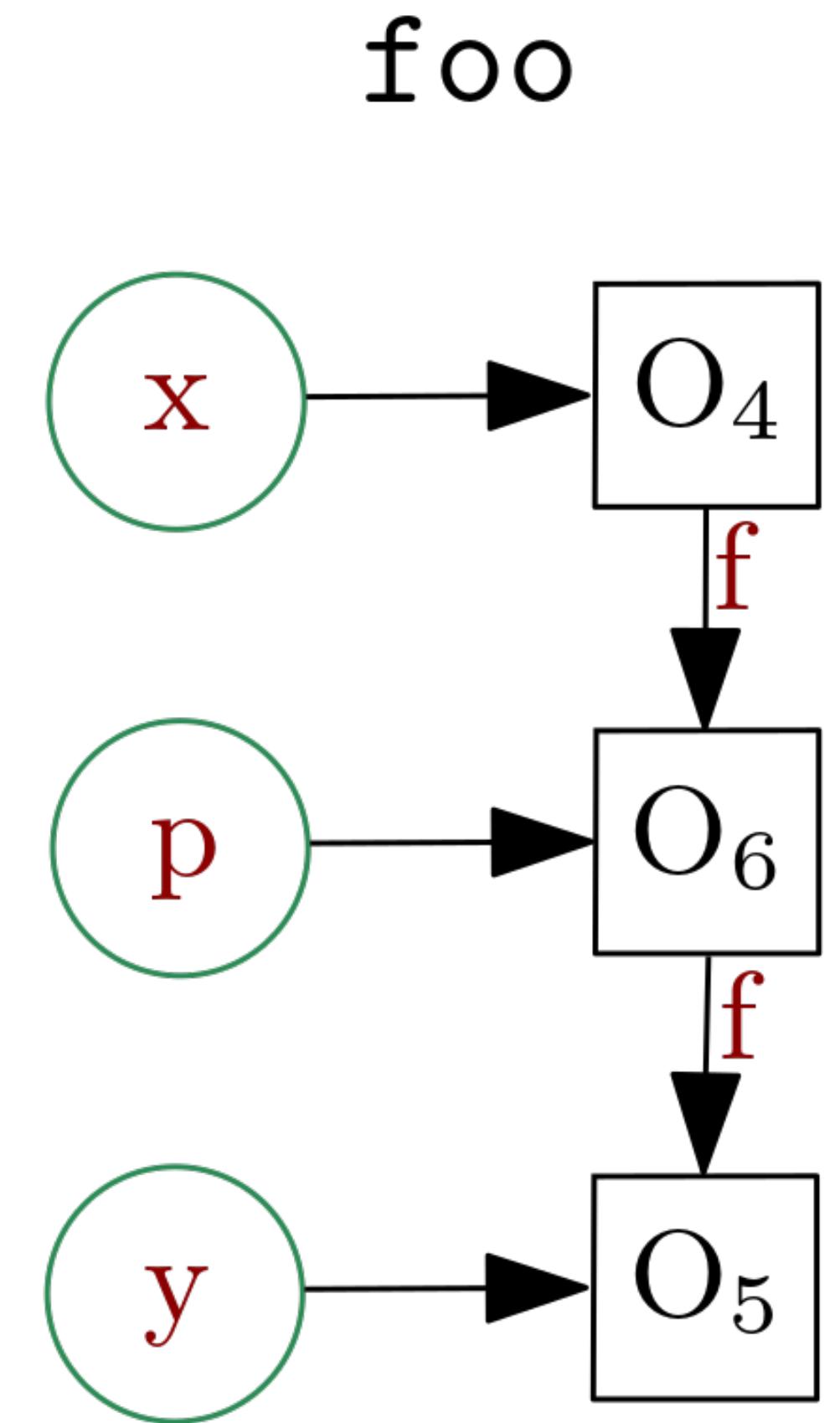
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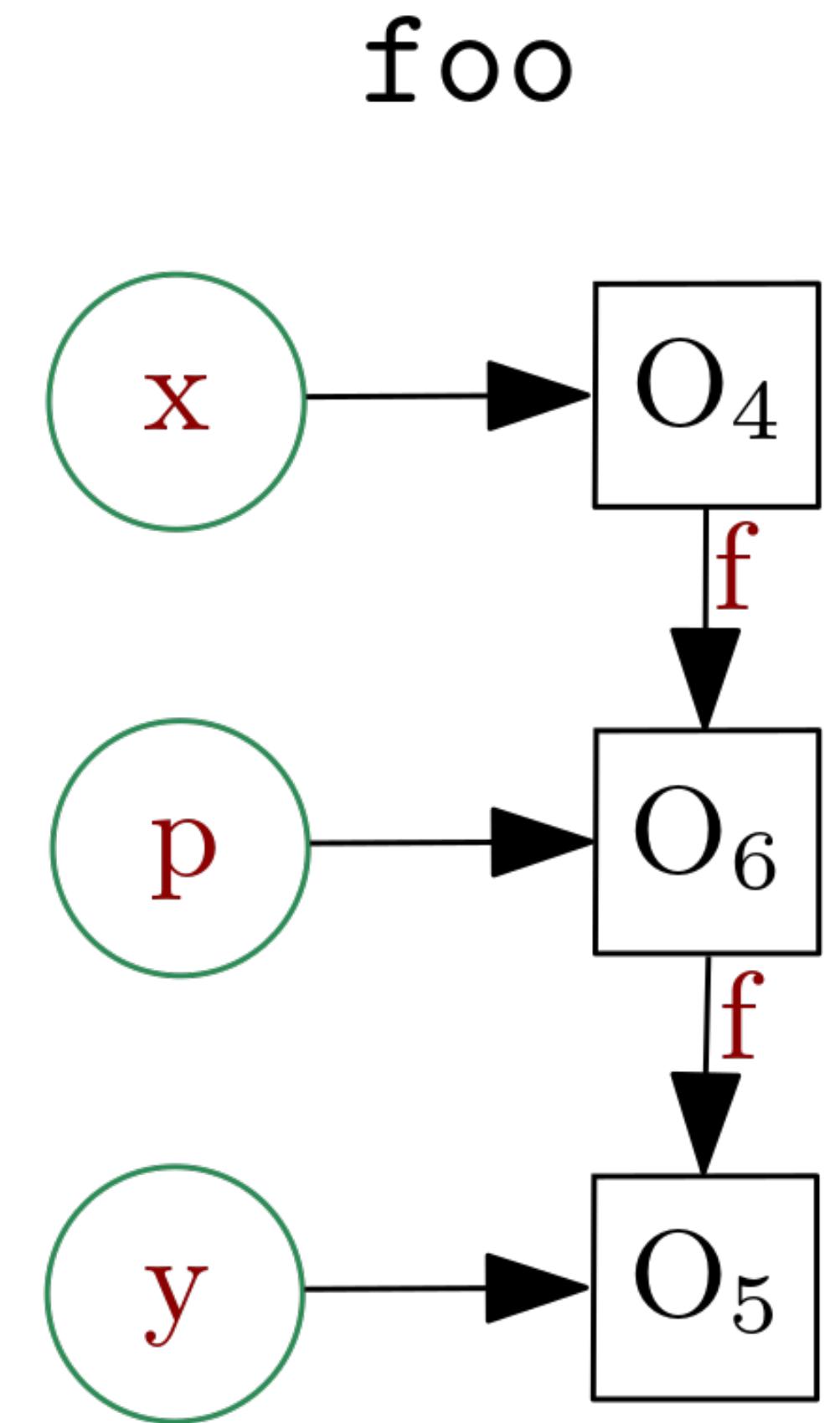
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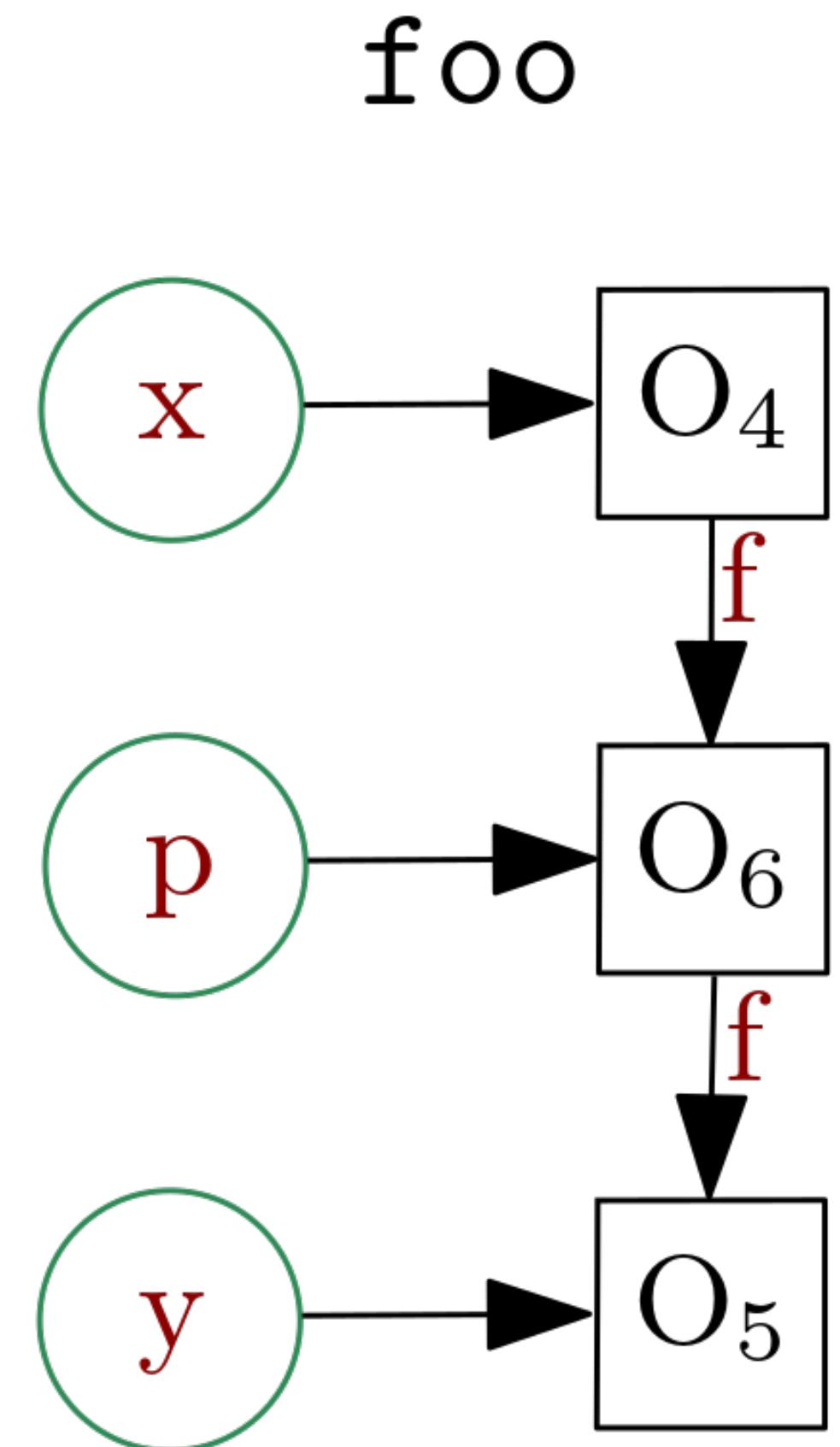


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Stack Allocate  
O<sub>4</sub>, O<sub>5</sub> and O<sub>6</sub>



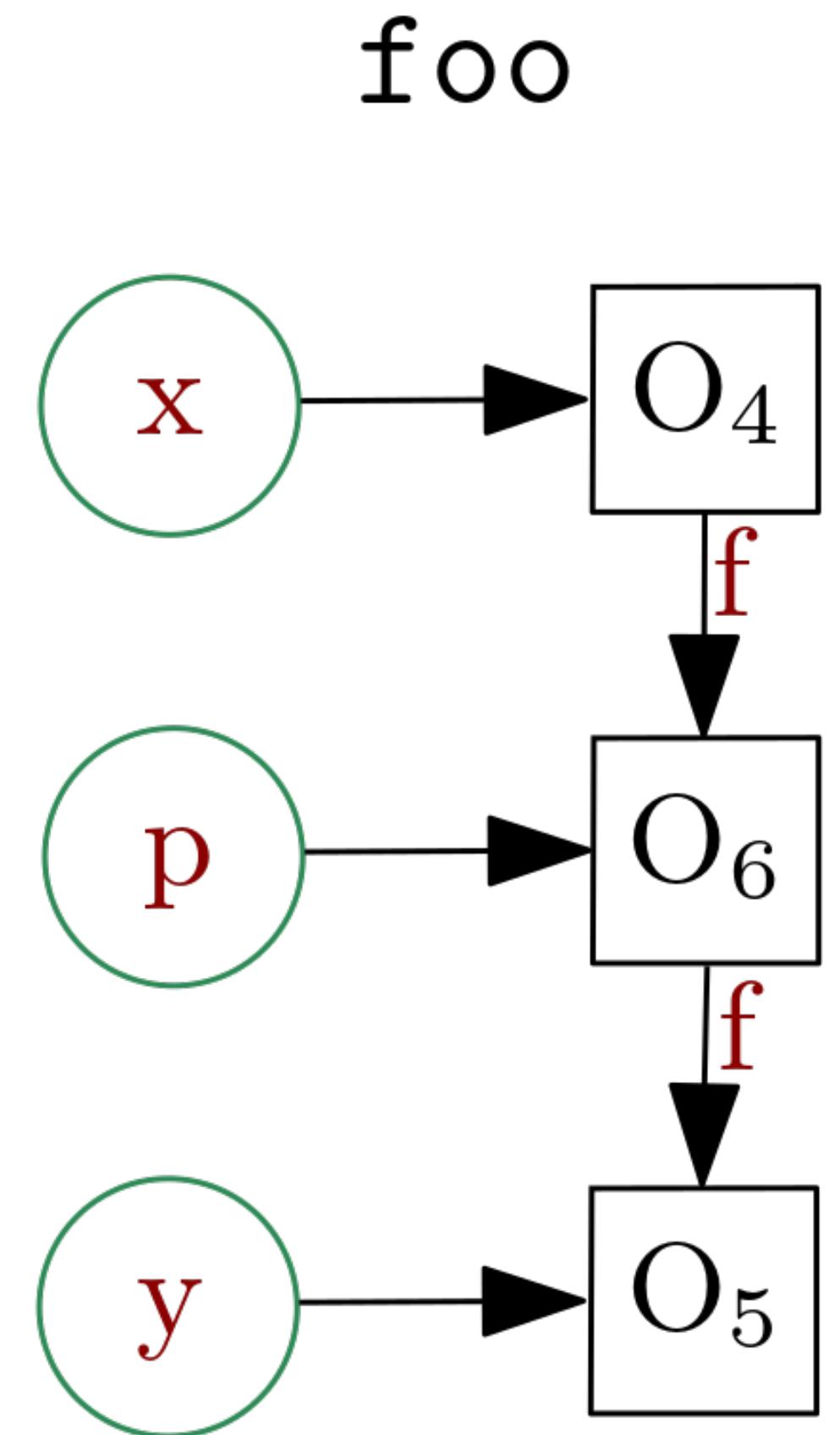
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```
16. class B extends A  
17.     void zar(A p, A q) {  
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19.     } /* method zar */  
20. } /* class B */
```

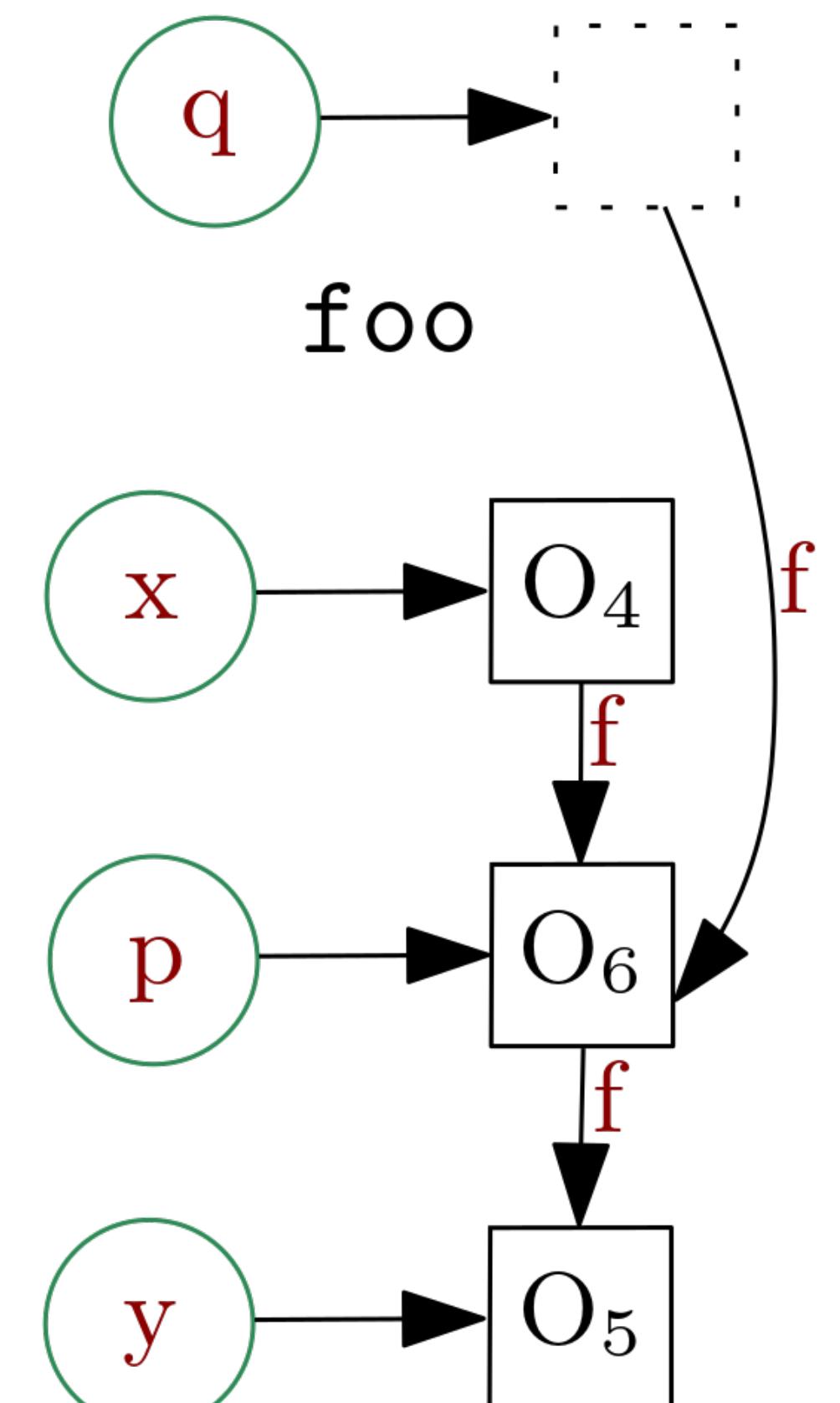
Dynamically loaded



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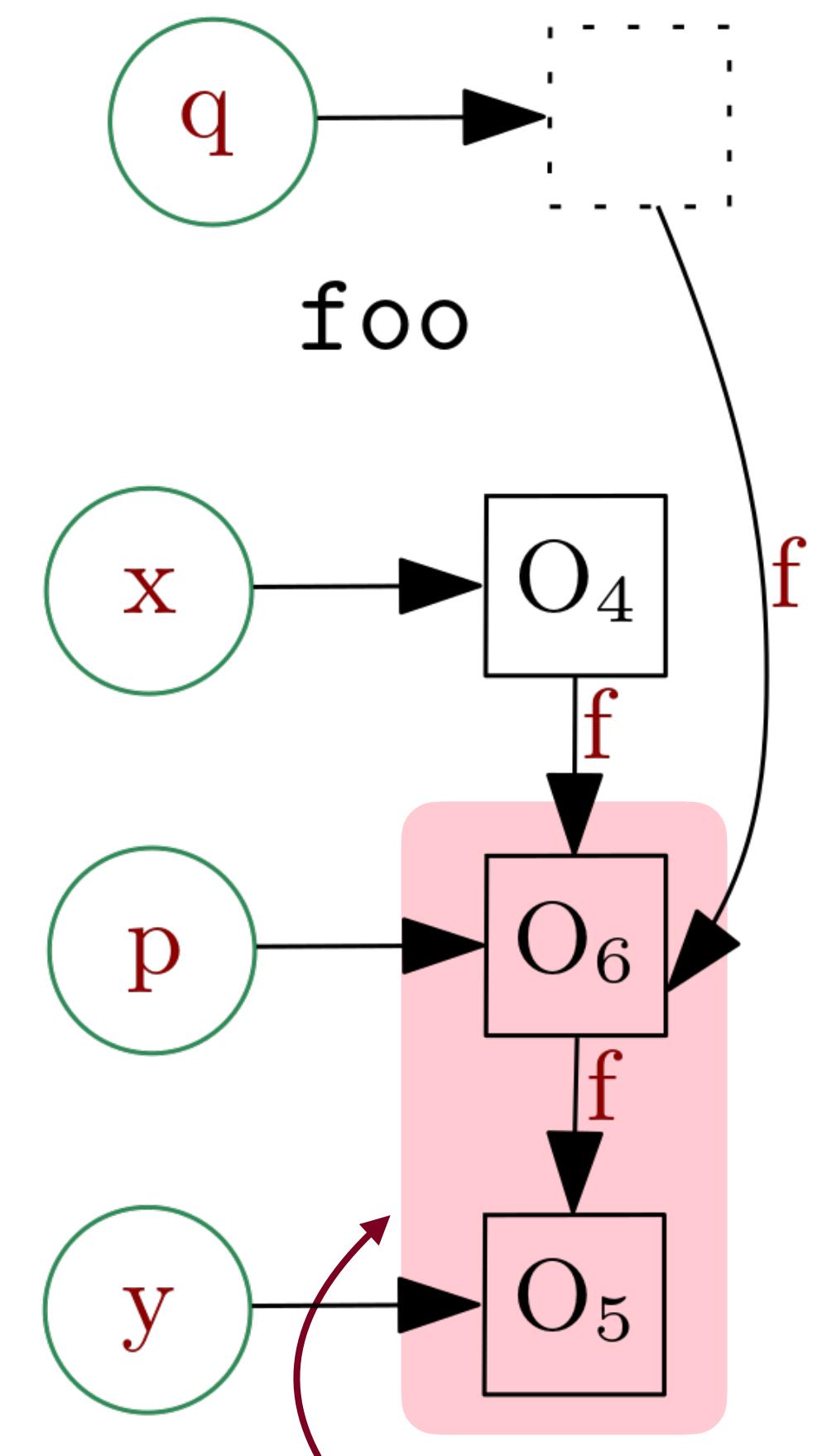
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Incorrect allocation on stack

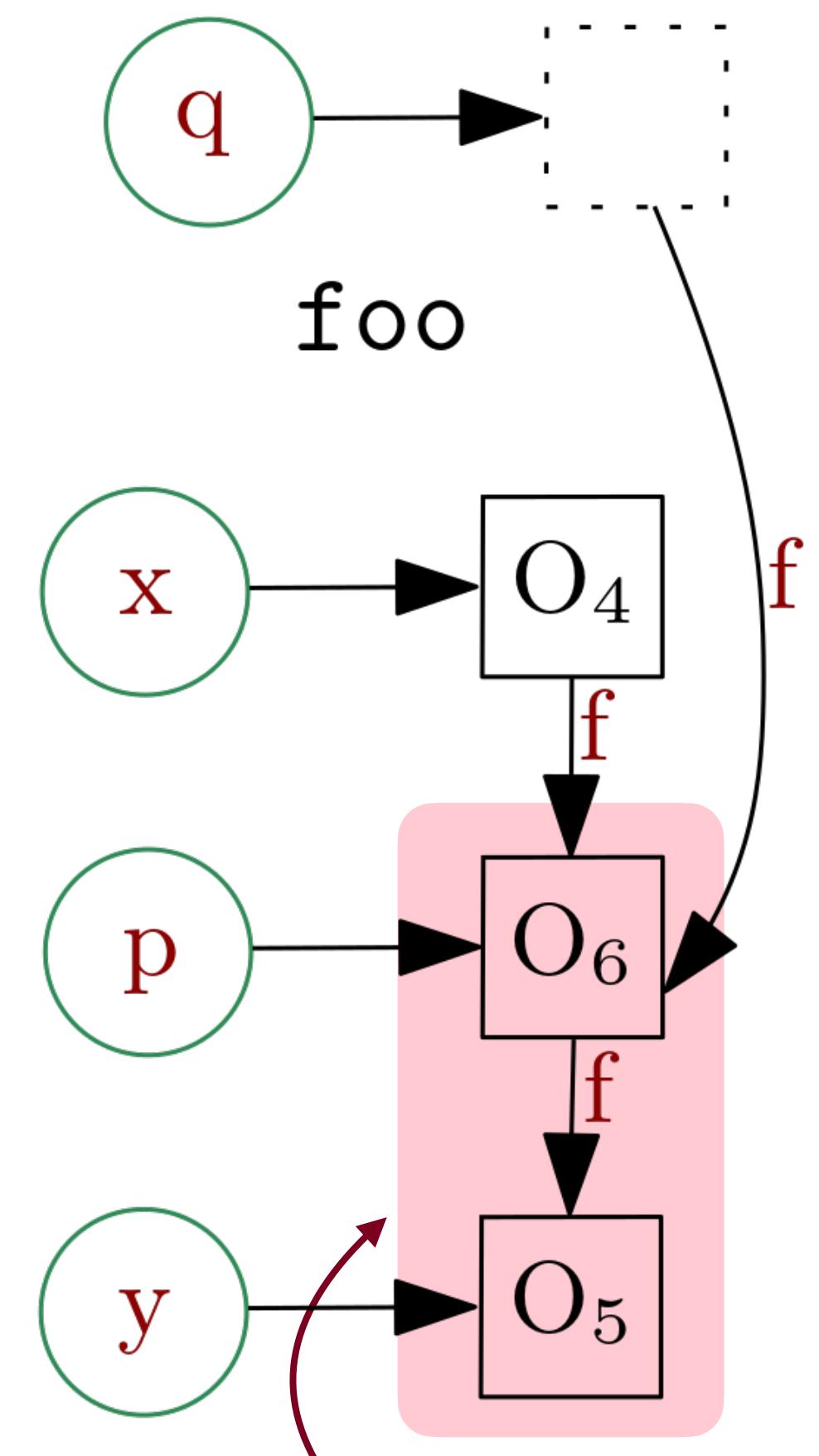
# Dynamic Heapification



# Dynamic ClassLoading Example

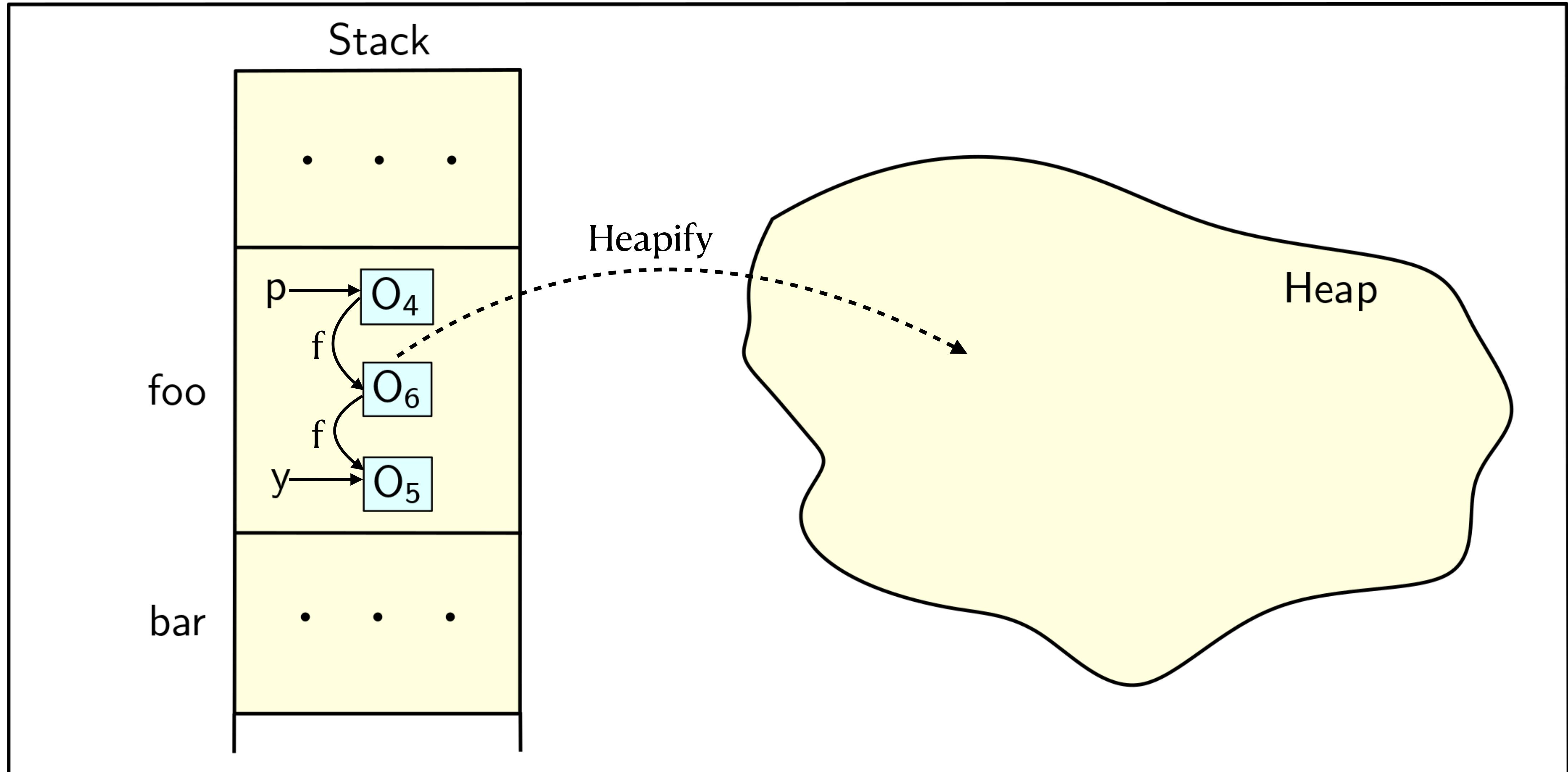
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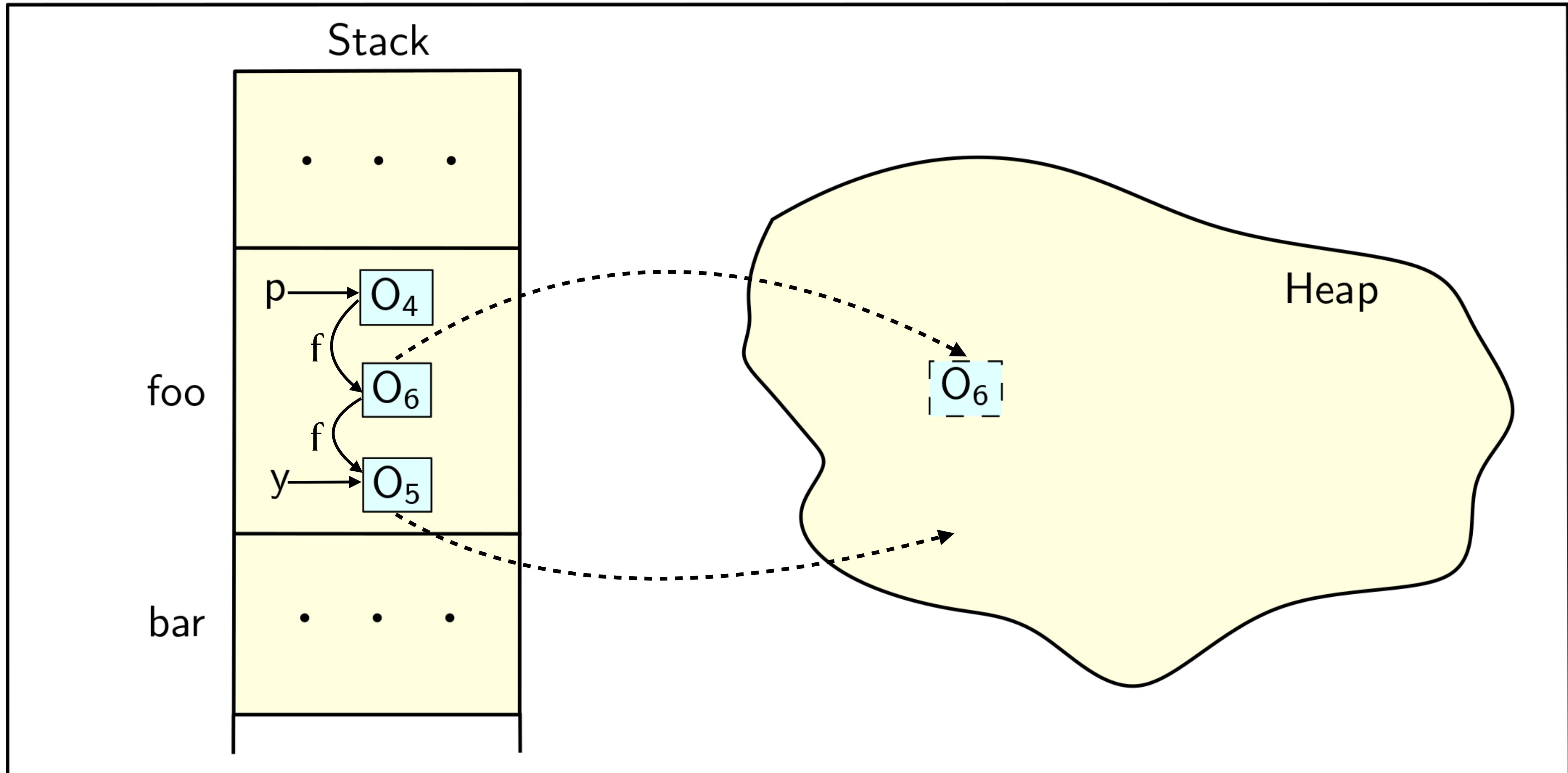


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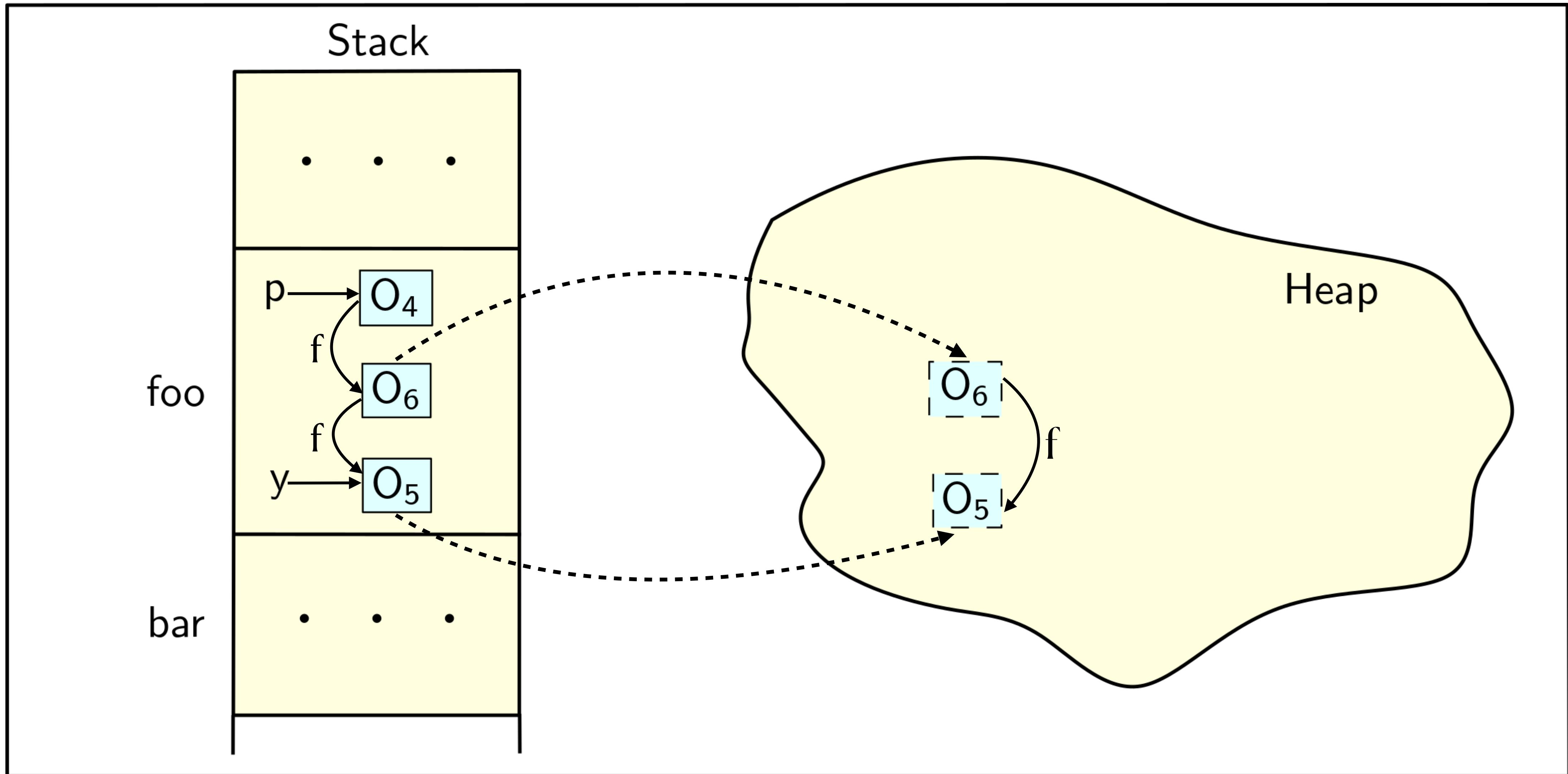
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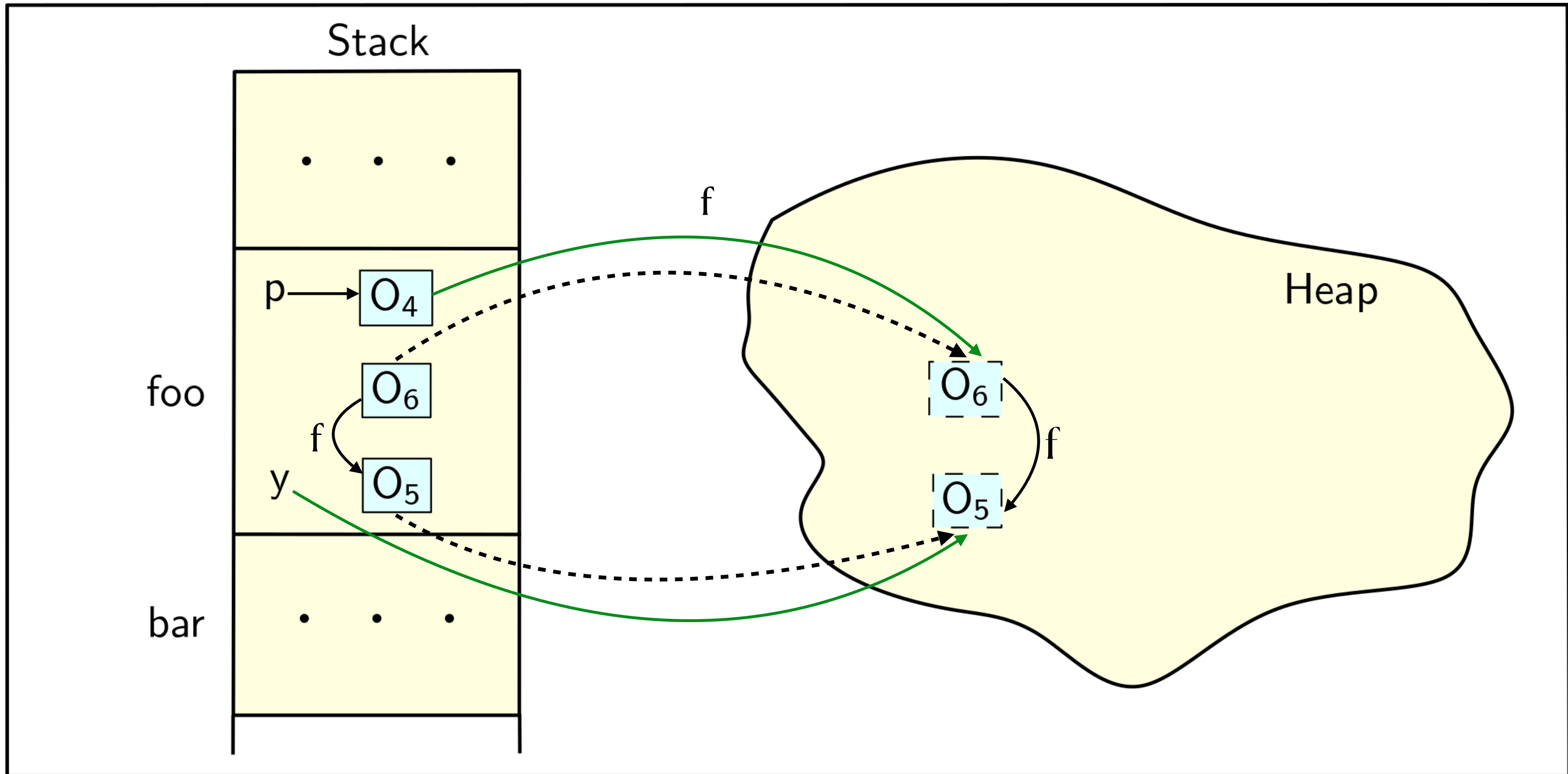
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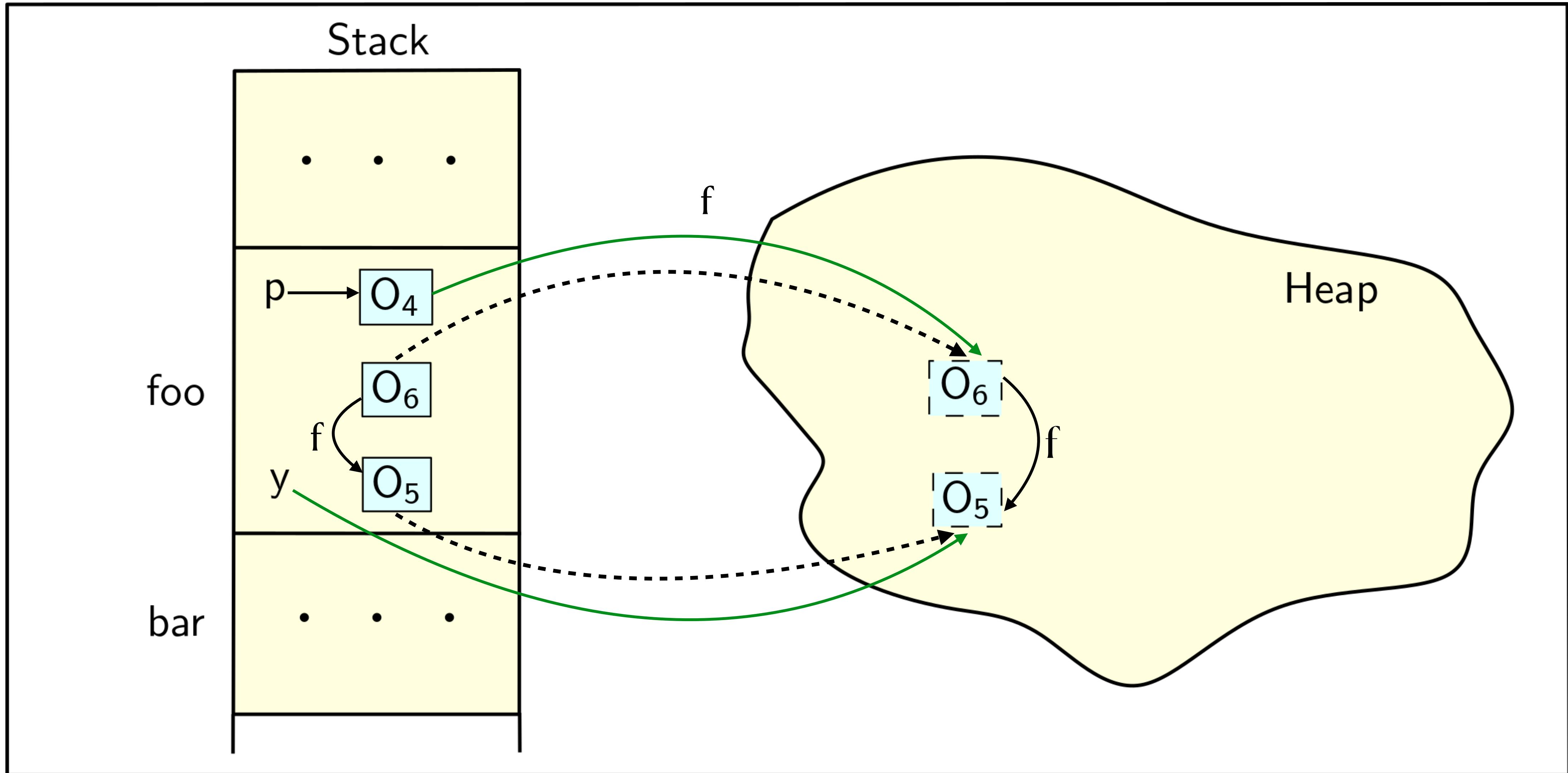
# Heapification



# Heapification



# Heapification



# Heapification

## How to identify the need for heapification?

### Heapification Checks

- Return of references. (Byte code: `return .`)
- References stores. (Byte code: `putfield`, `putstatic`, `aastore .`)
  - Throwing of exception. (Byte code: `athrow .`)
- Calls to native. (Byte code: `putObject`, `putObjectOrdered`, `putObjectVolatile .`)
- JNI APIs used to perform stores in called C/C++ code. (Byte code: `setObjectField .`)

# Summary and Moving Ahead

- Fallback as **heapification** allowed us to maintain **functional correctness** due to the **dynamism** offered by the Language/VM
- Overall, one of the first approaches to **soundly** and **efficiently** use static (offline) analysis results in a JIT compiler!

Is this the best we can do for stack allocation?

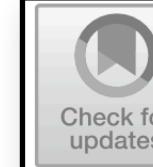
Can we go more aggressive?

# Recent Works on Static + Dynamic Analysis

## PYE: A Framework for Precise-Yet-Efficient Just-In-Time Analyses for Java Programs

TOPLAS 19

MANAS THAKUR and V. KRISHNA NANDIVADA, IIT Madras



## Principles of Staged Static+Dynamic Partial Analysis

SAS 22

Aditya Anand and Manas Thakur

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## ZS3: Marrying Static Analyzers and Constraint Solvers to Parallelize Loops in Managed Runtimes

CASCON 22

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RESEARCH

## Partial program analysis for staged compilation systems

FMSD 24

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All these works use AOT-analysis results to perform optimizations in JIT Compilers.

# Speculative Optimization in JIT Compilers

- Profile Information:

- Basic invocation, loop invariant values.
- Type profiles at type-cast statements and polymorphic callsites.
- Branch information, instance of checks.

Standard Speculative Optimizations

Conservative Fallback

- Dynamic Class Hierarchy:

- Information about loaded subclasses of a given class during execution.

- Inlining Table:

- Information about the list of methods inlined at various callsites.



# Speculative Optimization in JIT Compilers

- Profile Information:

- Basic invocation, loop invariant values.
- Type profiles at type-cast statements and polymorphic callsites.
- Branch information, instance of checks.

Standard Speculative Optimizations

Conservative Fallback

How can we get the best of static analysis and run-time information ??

- Dynamic Class Hierarchy:

- Information about loaded subclasses of a given class during execution.

- Inlining Table:

- Information about the list of methods inlined at various callsites.



# CoSSJIT: Combining Static Analysis and Speculation in JIT Compilers

- Idea:
  - Enrich Static Analysis results with possibility of speculation at run-time.
  - Enable the JIT Compiler to perform speculative optimization based on the static analysis results.

# 1. Polymorphic Callsites

```
1. class A {  
2. static A global;  
3. . . .  
4. void foo(A z) {  
5.     A x = new A(); // O5  
6.     A y = new A(); // O6  
7.     x.f = new A(); // O7  
8.     . . .  
14.     z.bar(x);  
15. } /* method foo */  
16. } /* class A */
```

```
17. class B extends A {  
18.     void bar(A p2) { . . . }  
19. } /* class B */
```

```
20. class C extends A {  
21.     void bar(A p3) {  
22.         global = p3; Escapes  
23.     }  
24. } /* class C */
```

$\Pi : \{B, C\} \rightarrow [O_5, O_7] \text{ [Escaping]}$

# 1. Polymorphic Callsites

```
1. class A {  
    . . .  
4. void foo(A z) {  
    A x = new A(); // 05  
    A y = new A(); // 06  
    x.f = new A(); // 07  
. . .  
14.     z.bar(x);  
15. } /* method foo */  
16. } /* class A */
```

polymorphic\_cond

A.foo() [...] [z<sub>\_14</sub>, {B}, {0<sub>5</sub>, 0<sub>7</sub>}]

```
17. class B extends A {  
18.     void bar(A p2) { . . . }  
19. } /* class B */
```

```
20. class C extends A {  
21.     void bar(A p3) {  
22.         global = p3; Escapes  
23.     }  
24. } /* class C */
```

At runtime:

- Class Hierarchy (CHTable): C is not loaded.
- Most of the times **z** is of type “B”.

# 2. Branching

```
1. class A {  
    . . .  
5. void foo(A p1) {  
6.     A y = new A(); // O6  
    . . .  
9.     if(p1 instance A) {  
    . . .  
11. } else {  
13.     global = y; Escapes  
14. }  
15.     y.f = p1;  
16. } /* method foo */
```

$\Pi : \rightarrow O_6 (\text{Escaping})$

# 2. Branching

```
1. class A {  
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5. void foo(A p1) {  
6.     A y = new A(); // 06  
    . . .  
9.     if(p1 instance A) {  
    . . .  
11. } else {  
13.     global = y;  
14. }  
15.     y.f = p1;  
16. } /* method foo */
```

At runtime:

- The “if” branch is taken most number of times.

branching\_cond

A.foo() [..] [9, {0<sub>6</sub>}]

# 3. Method Inlining

```
1. class A {  
2.     void foo() {  
3.         . . .  
4.         z.bar(x);  
5.         r.foobar(p, q);  
6.     } /* method foo */  
7.     void bar(A p1) { . . . }  
8.     void foobar(A p2) { . . . }  
9. } /* class A */
```

```
10. class B extends A {  
11.     void bar(A p3) {  
12.         // p3's pointee doesn't escape  
13.         p3.f = new A(); // O13  
14.         p3.foobar(p3.f);  
15.     } /* method bar */  
16.     void foobar(A p4) { . . . }  
17. } /* class B */  
18. class C extends A { . . . }
```

**O<sub>13</sub> marked as Escaping.**

# 3. Method Inlining

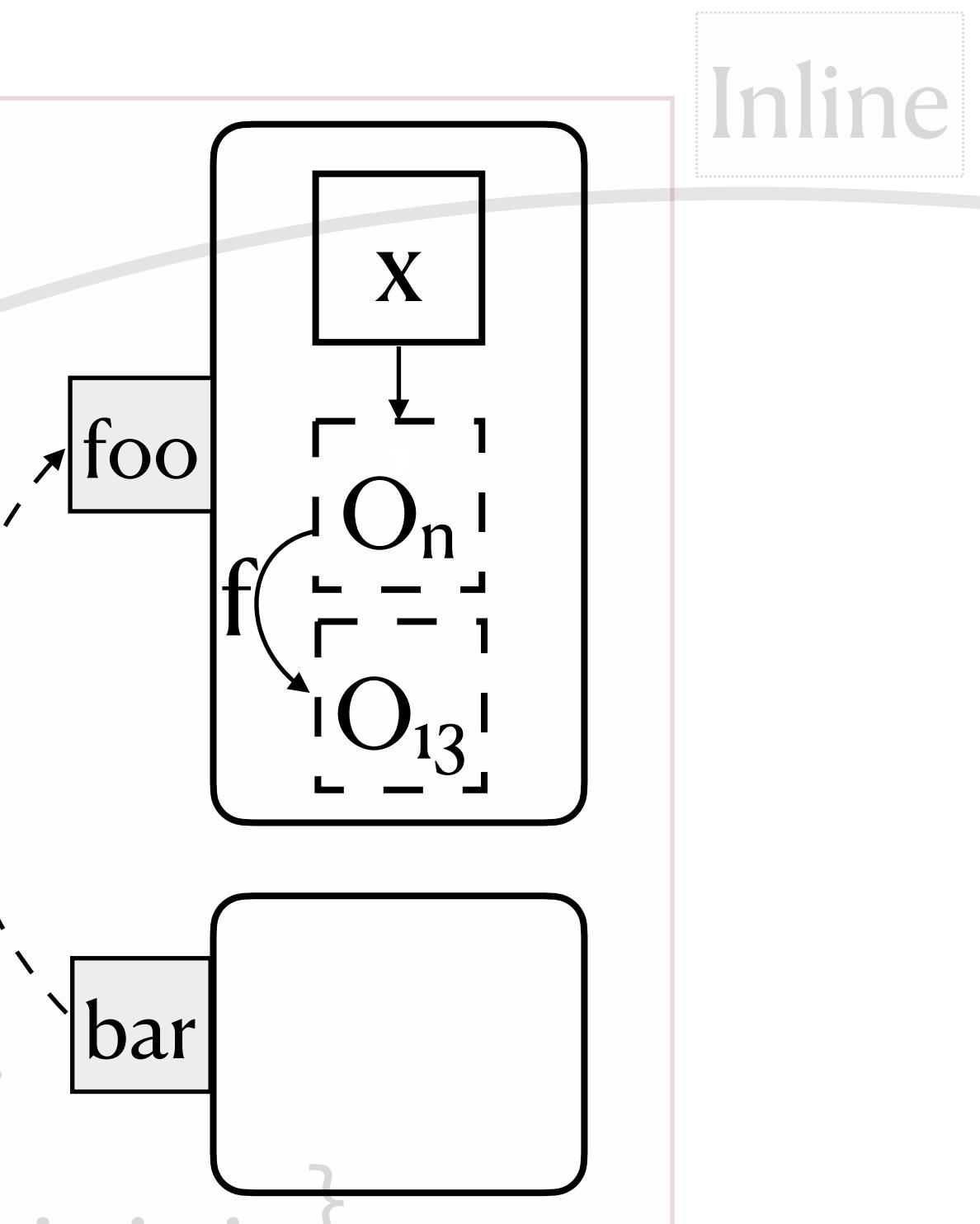
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Inline

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14.         p3.foobar(p3.f);  
15.     } /* method bar */  
16.     void foobar(A p4) { . . . }  
17. } /* class B */  
18. class C extends A { . . . }
```

# 3. Method Inlining

```
1. class A {  
2.     void foo() {  
3.         . . .  
4.         z.bar(x);  
5.         r.foobar(p, q);  
6.     } /* method foo */  
7.     void bar(A p1) { . . . }  
8.     void foobar(A p2) { . . . }  
9. } /* class A */
```



```
10. class B extends A {  
11.     void bar(A p3) {  
12.         // p3's pointee doesn't escape  
13.         p3.f = new A(); // O_{13}  
14.         p3.foobar(p3.f);  
15.     } /* method bar */  
16. } /* class B */  
17. class C extends A { . . . }
```

Callee object on caller's stack frame

inlining\_cond

A.foo() [..] [4, B.bar(p3), {O\_{13}}]

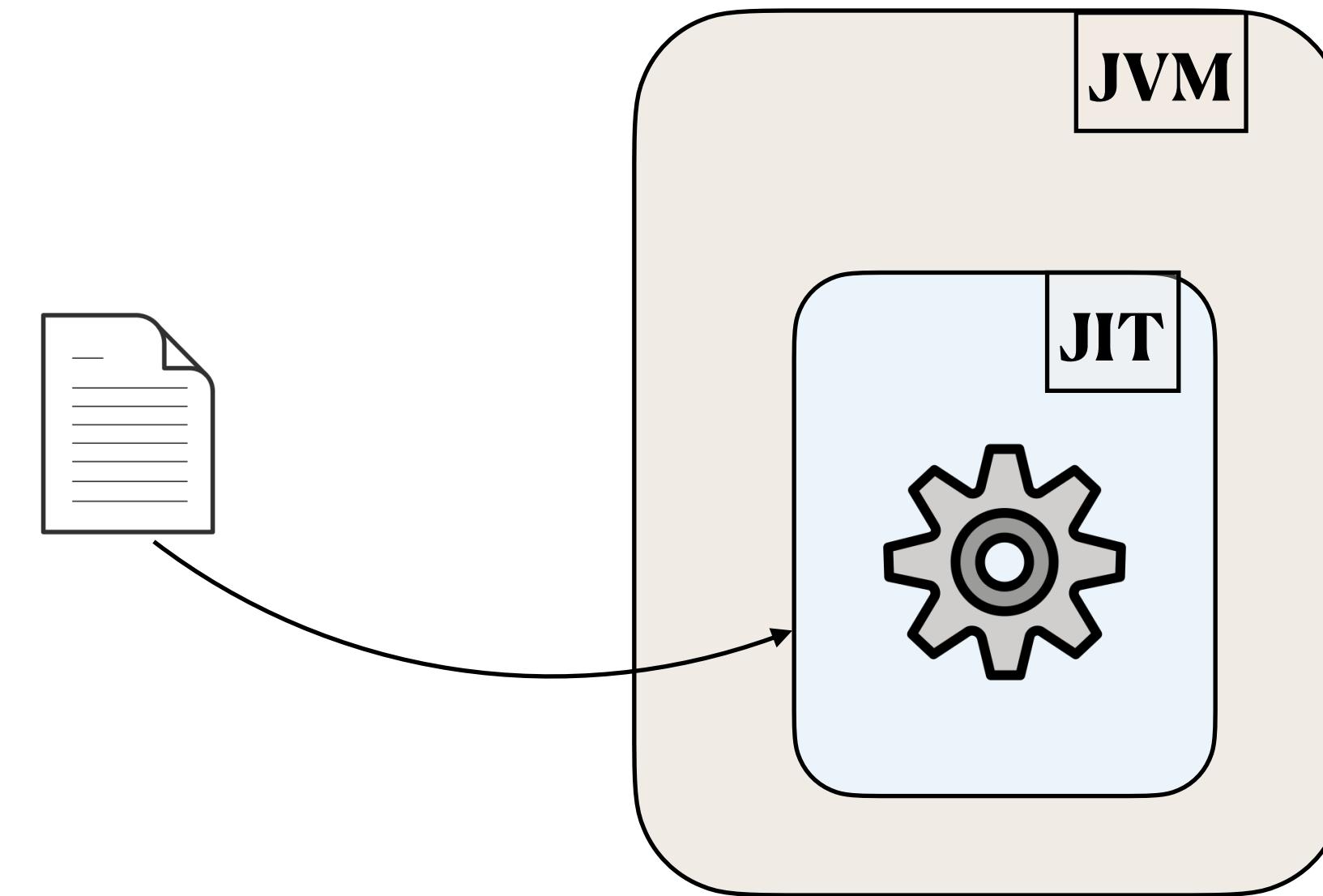
# Summary

Direct

Conditional

A::foo() [Direct\_Allocation] [polymorphic\_cond] [branching\_cond] [inlining\_cond]

Statically generated results

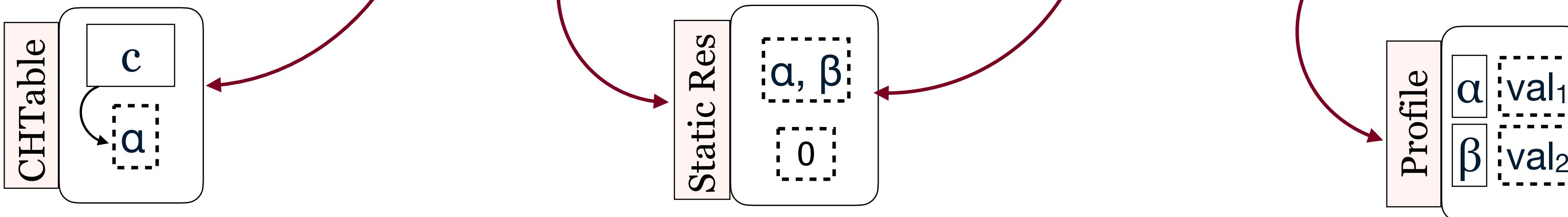


# Algorithm in the JIT Compiler

## 1. Polymorphic Callsite

```
for each o in JIT_identified_objects(m):
```

$$(CH_m[c] \subseteq SA_m[0][c]) \vee (\forall t \in SA_m[0][c] \sum CP_m(t) > ST)$$



## 2. Branching Conditions

```
for each o in JIT_identified_objects(m):
```

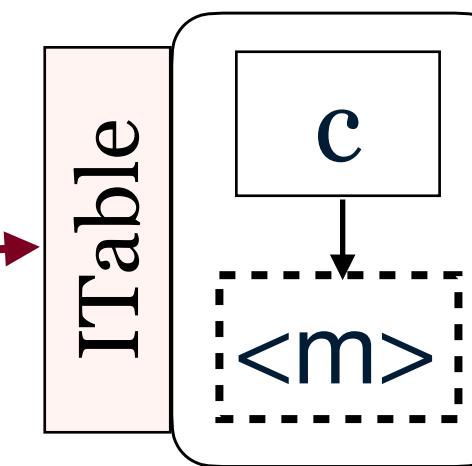
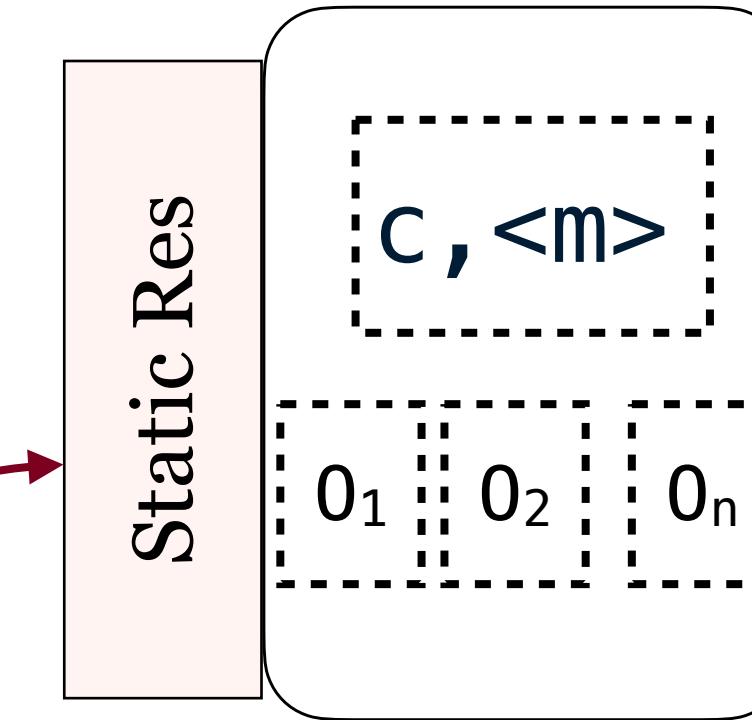
$$(\forall b \in SA_m[0][br] \text{ with } \sum BP_m(b) > ST)$$



# Algorithm in the JIT Compiler

## 3. Inlining Conditions

```
for each callsite  $c \in \text{CallSites}_m$ :  
    Callers $_c = \text{SAM}[c]$   
    if  $\exists n$  such that  $(n \in \text{ITM}[c] \wedge n \in \text{Callers}_c)$ :  
         $0_{\text{static}} = \text{statically\_marked\_objects}(m)$   
        mark all  $o \in 0_{\text{static}}$ 
```



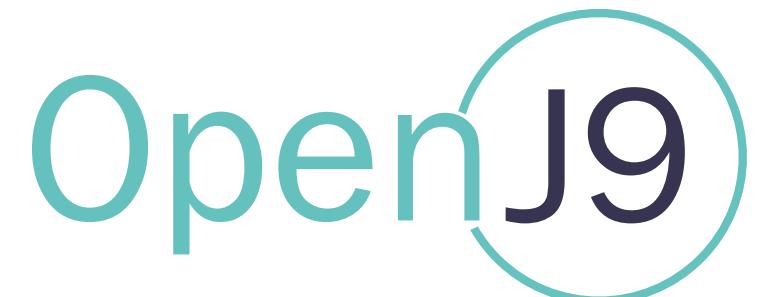
# Evaluation (Setup)

- Implementation:

Static analysis



Runtime components:



- Evaluation schemes:

- **BaseLine**: Stack allocation with the existing JIT scheme.
- **CoSSJIT**: Stack allocation with our direct+conditional scheme.

- Benchmarks:

- **DaCapo benchmark suites**:  
(23.10-chopin and 9.12 MRI)
- **SPECjvm 2008**

- Compute:

- Enhancement in stack allocation.
- Impact on performance and garbage collection.

# Evaluation (Stack Allocation)

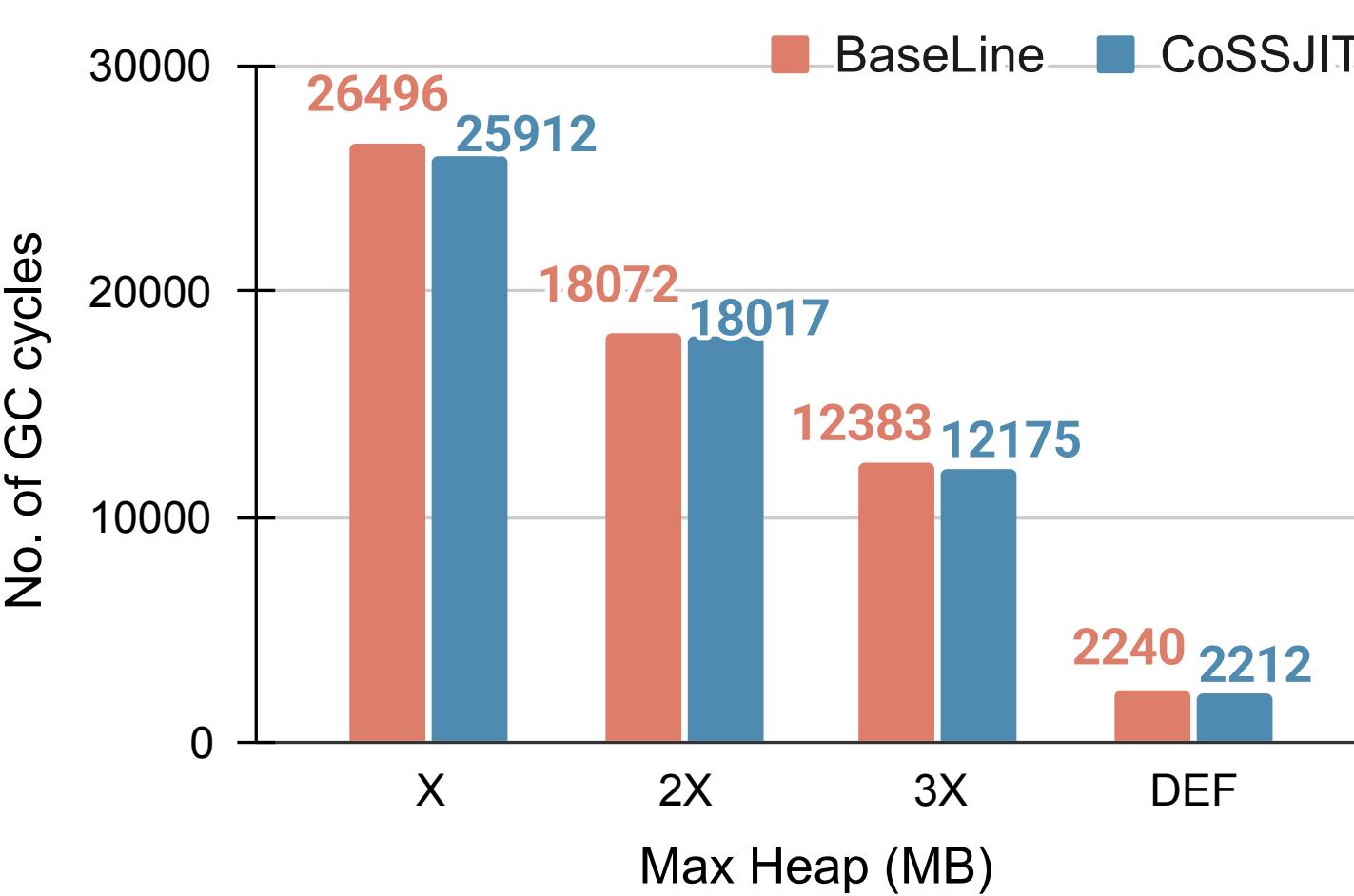
Benchmark	Base Scheme (BaseLine)			Conditional Scheme (CoSSJIT)			
	Static Count	Dynamic Count	Stack Bytes	Static Count	Dynamic Count	Stack Bytes	Heapify Count
avrora	12	104.2M (38.0%)	3335MB	14	106.5M (39.3%)	3391.4MB	0.4M
compress	8	0.01M (3.29%)	1720MB	18	0.093M (15.5%)	2.75MB	0M
graphchi	12	349M (5.20%)	8327MB	109	1041.1M (14.2%)	20020MB	0.0006M
h2	61	33M (1.02%)	579MB	129	525M (16.2%)	12749MB	6M
luindex	30	4.9M (3.16%)	137MB	84	24.2M (15.4%)	746MB	0.06M
pmd	24	1762M (9.80%)	42295MB	92	1835M (10.2%)	43468MB	0.2M
sunflow	100	1077M (20.0%)	27577MB	243	2286M (34.7%)	56042MB	0.19M
signverify	15	0.24M (0.86%)	6.8MB	40	3.25M (6.34%)	102.2MB	0.5M
zxing	82	24.2M (2.61%)	987.6MB	347	1539M (16.4%)	52796.7MB	0.4M

# Evaluation (Stack Allocation)

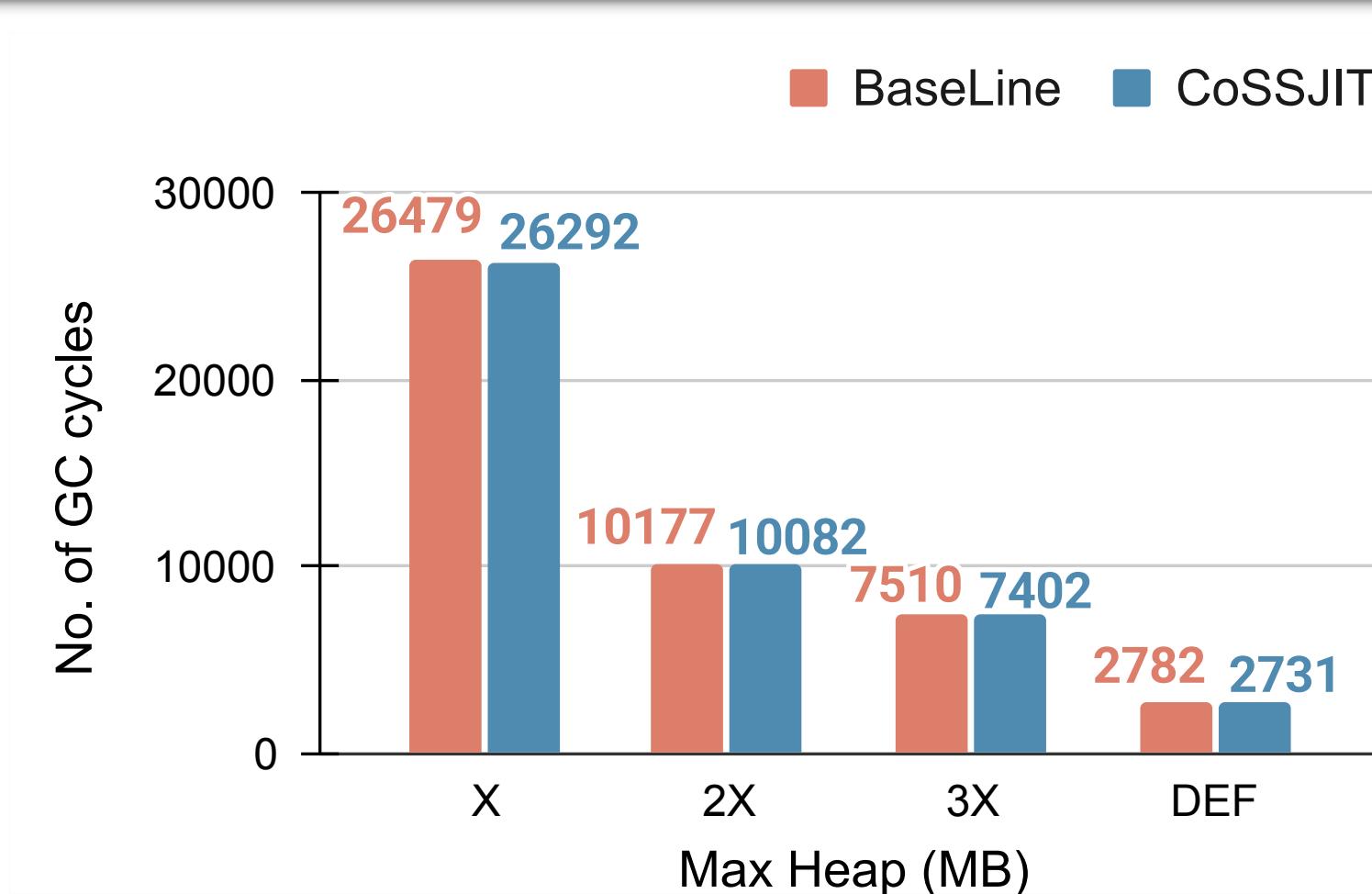
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**Stack Allocation: 1.4x↑ Stack Bytes: 5.7x↑**  
**(Less Heap Allocation)**

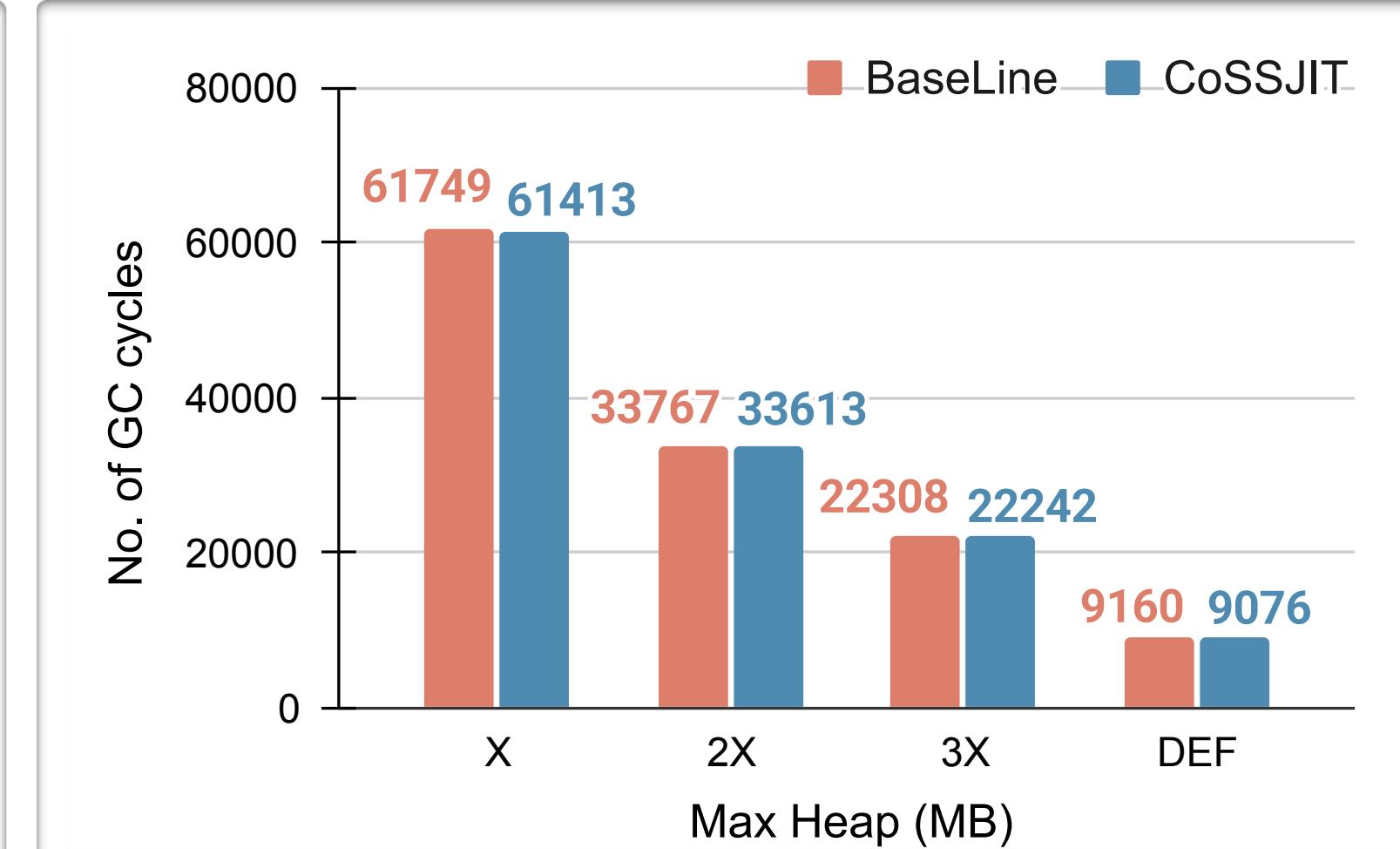
# Garbage Collection



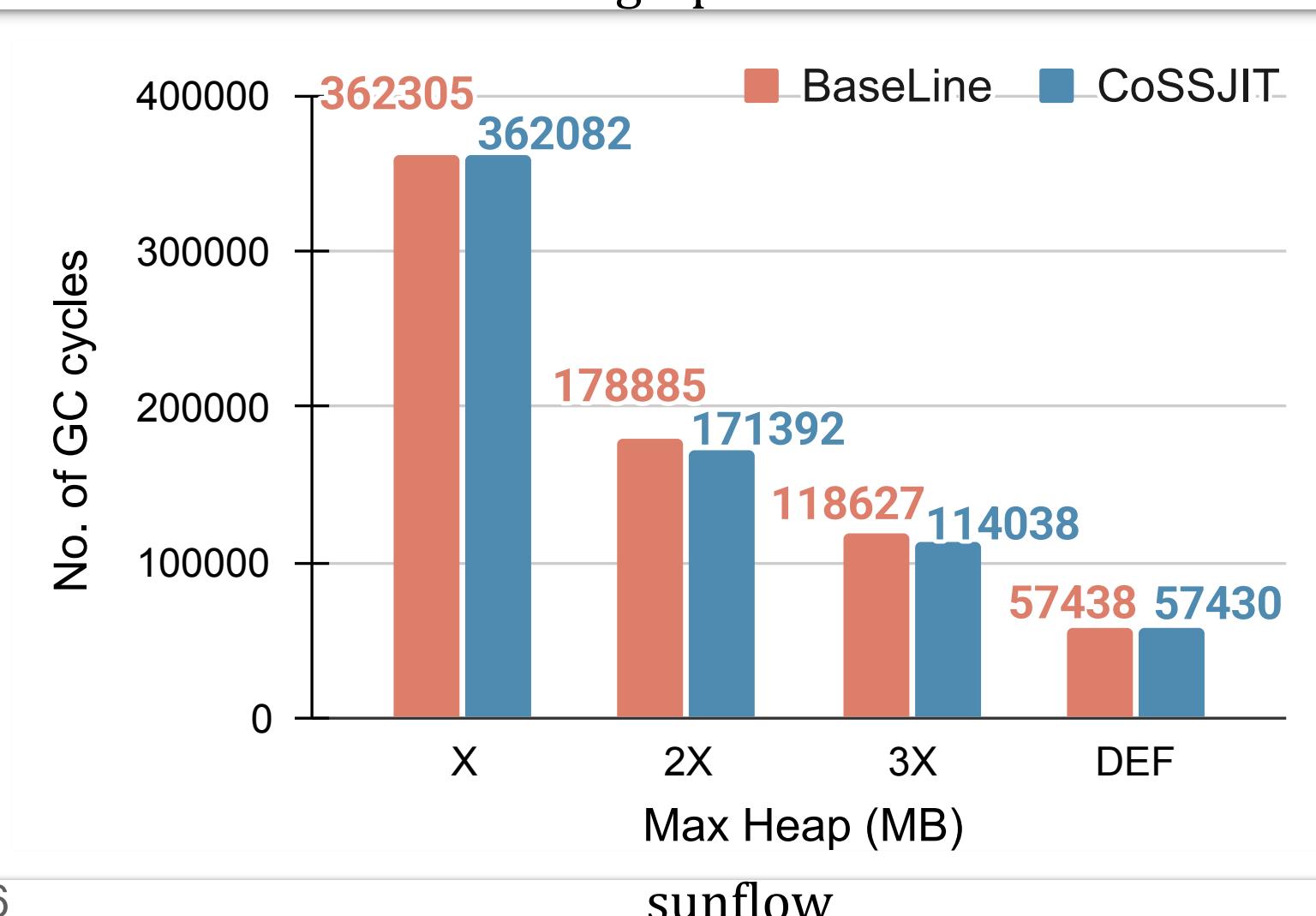
graphchi



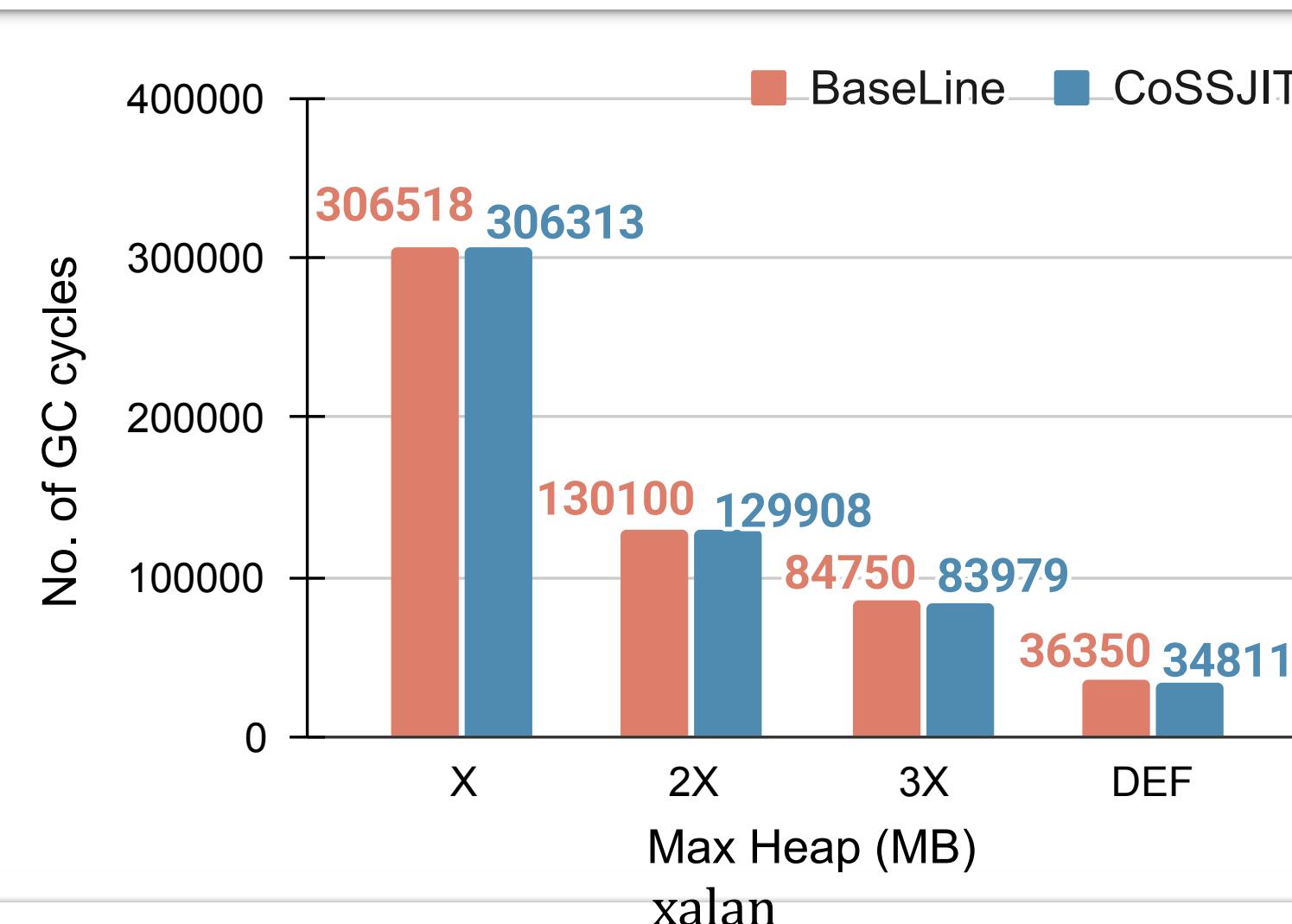
luindex



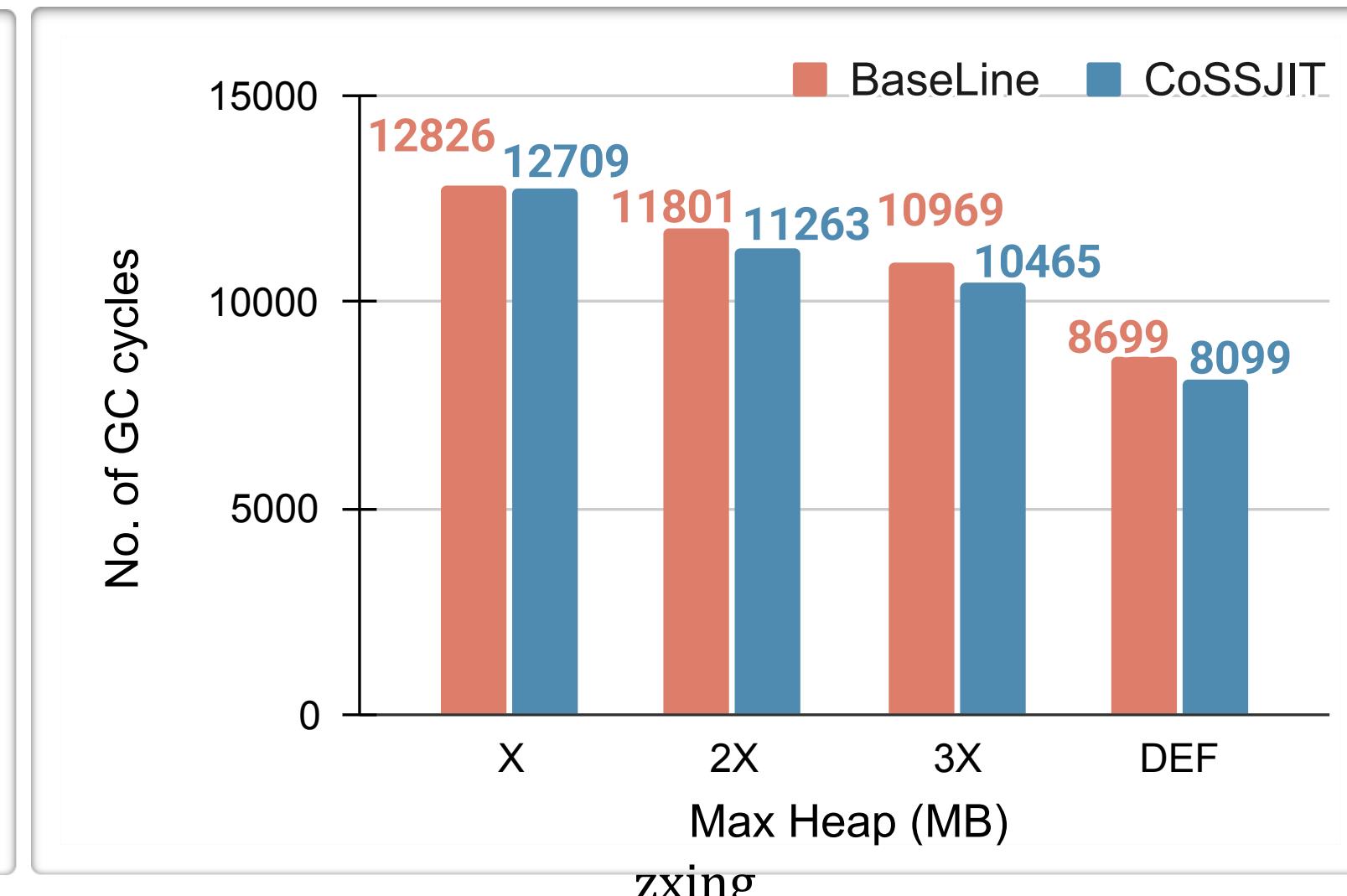
lusearch



sunflow

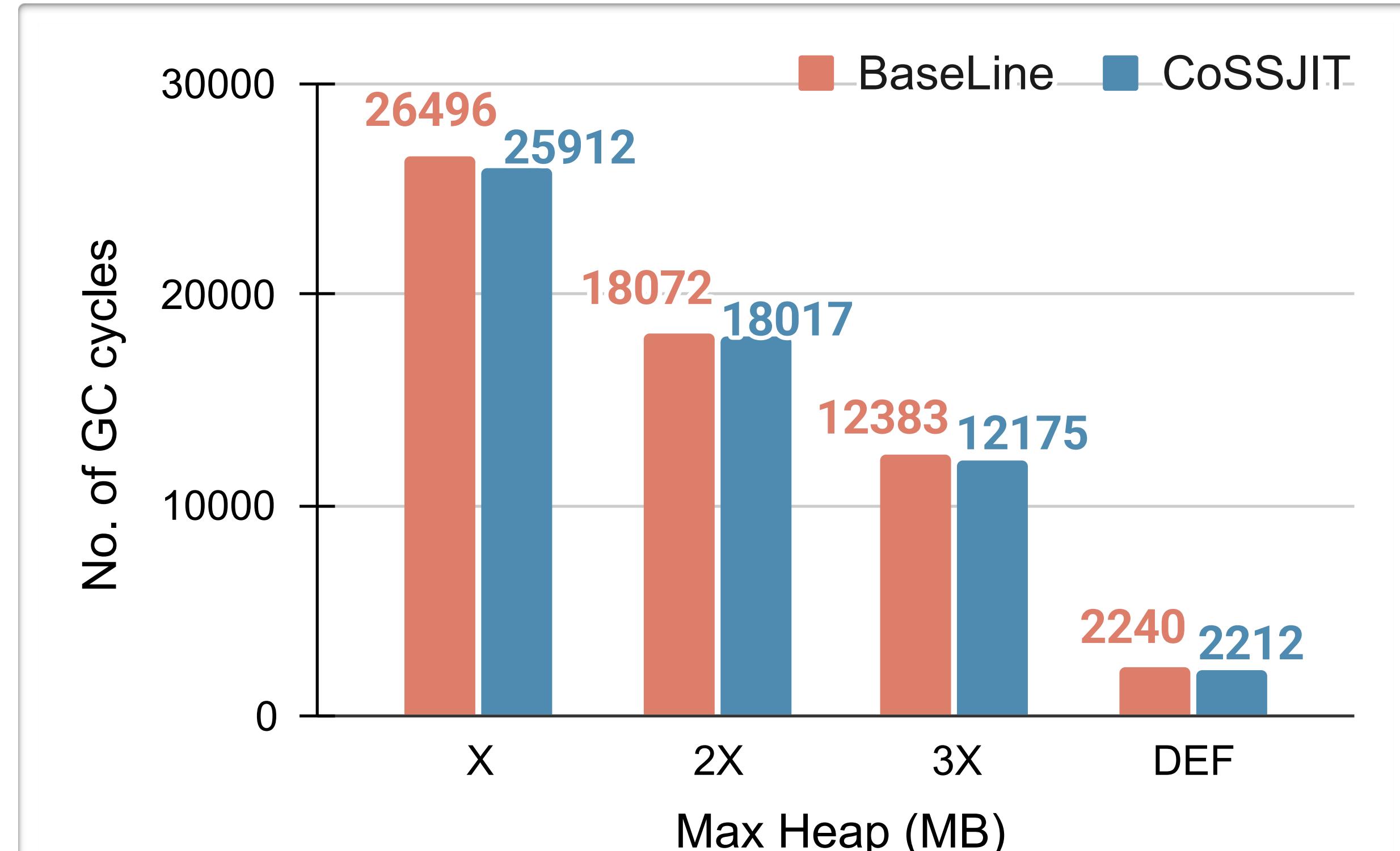


xalan



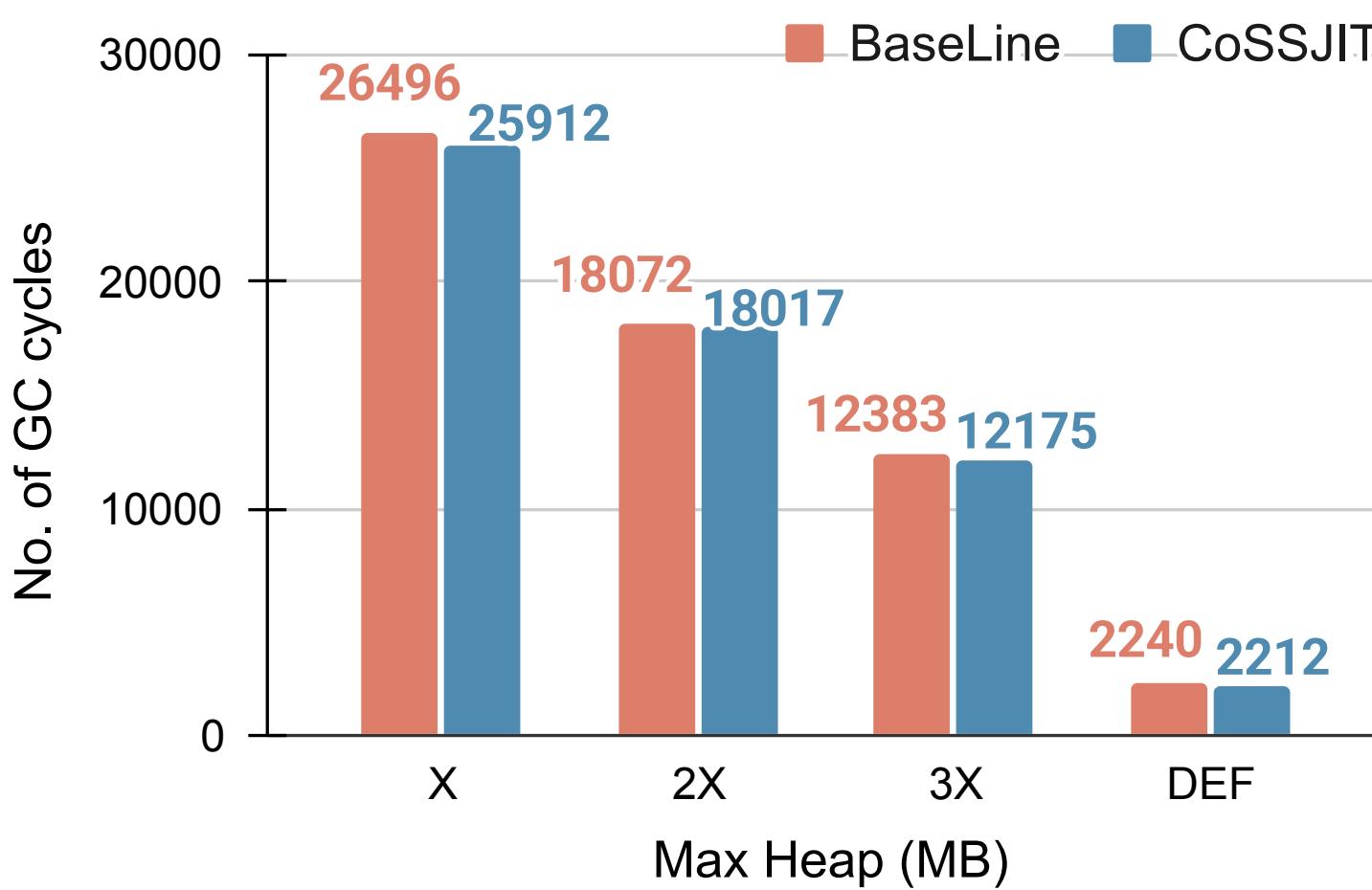
zxing

# Garbage Collection

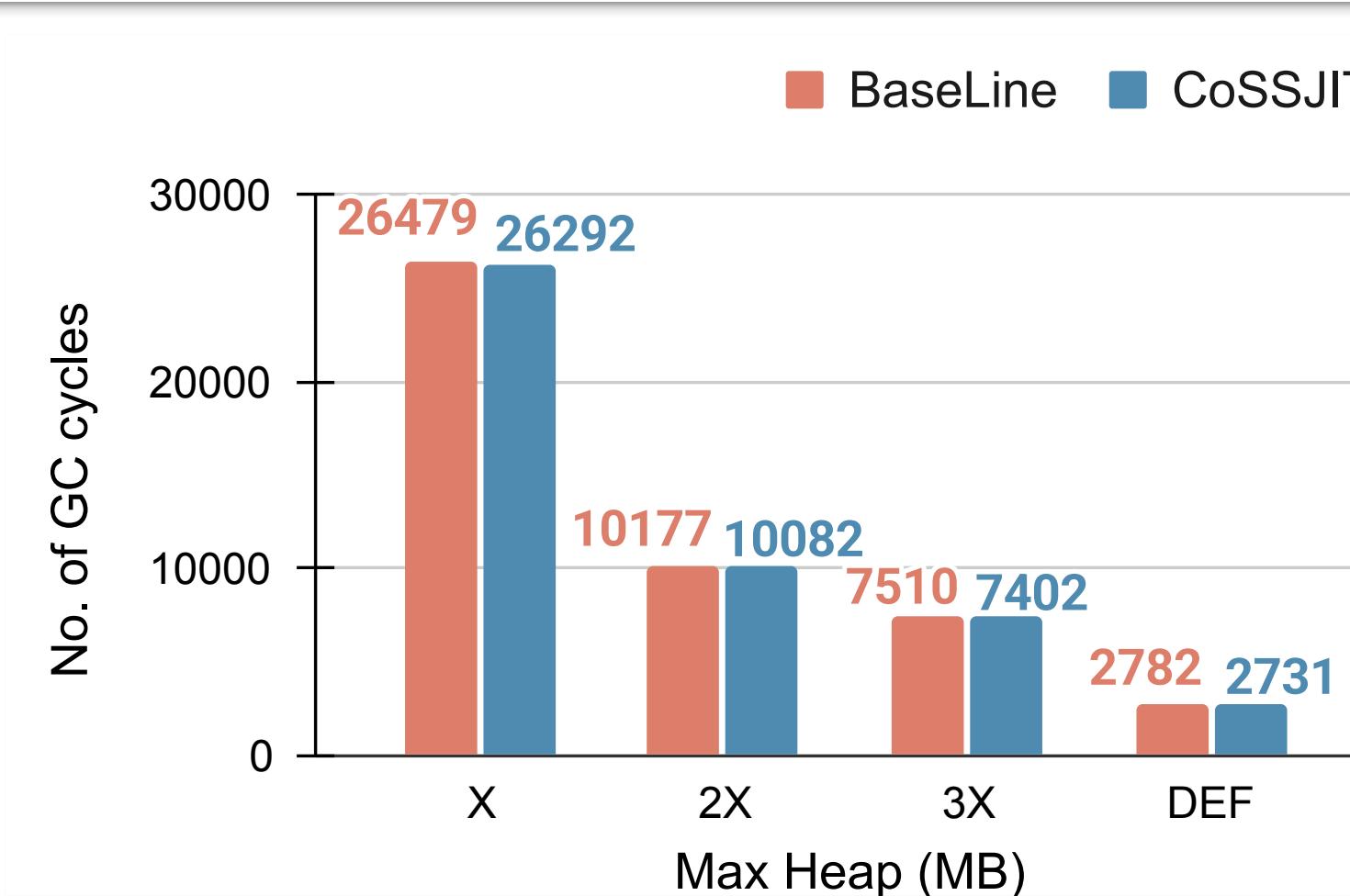


graphchi

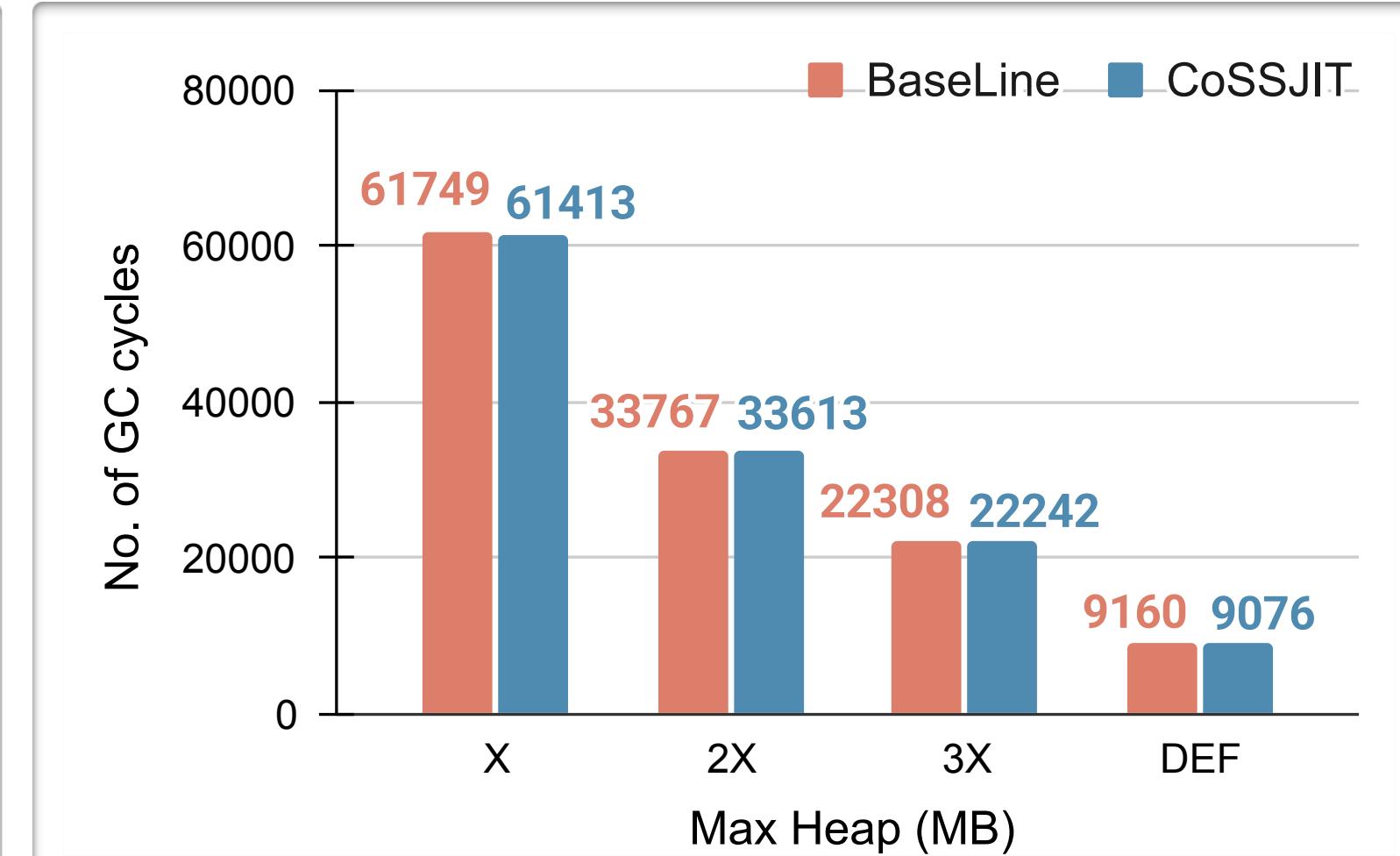
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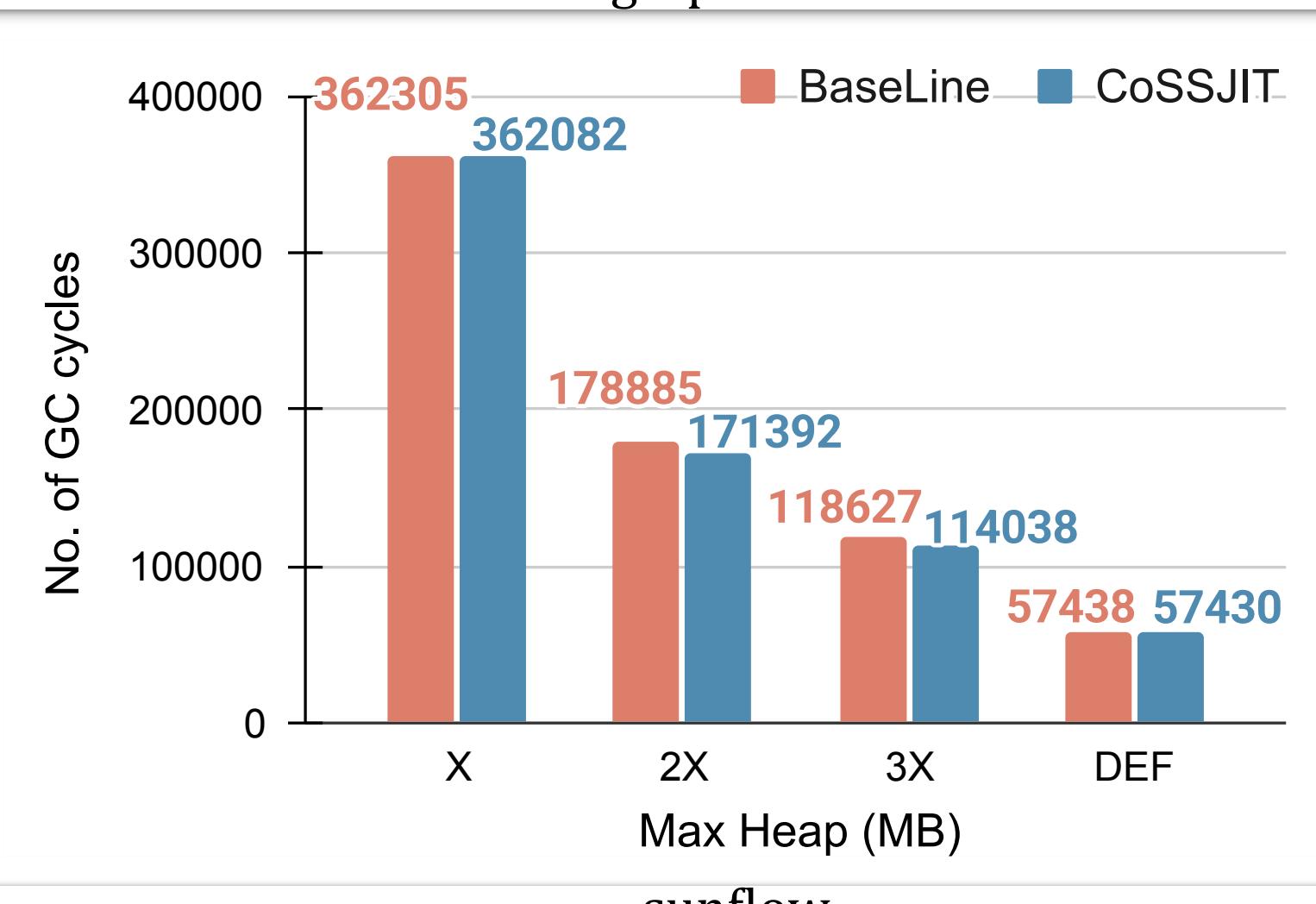
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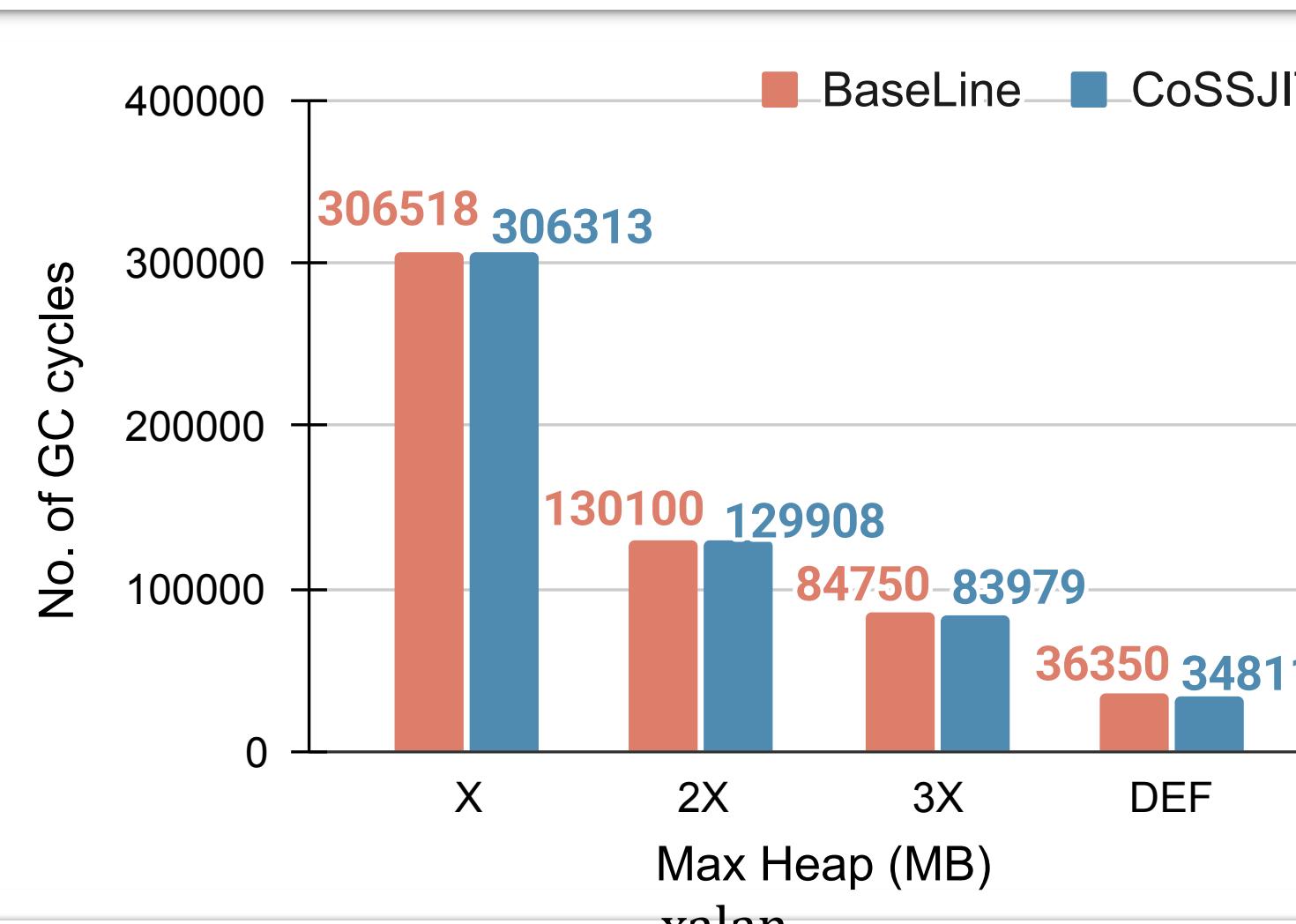
luindex



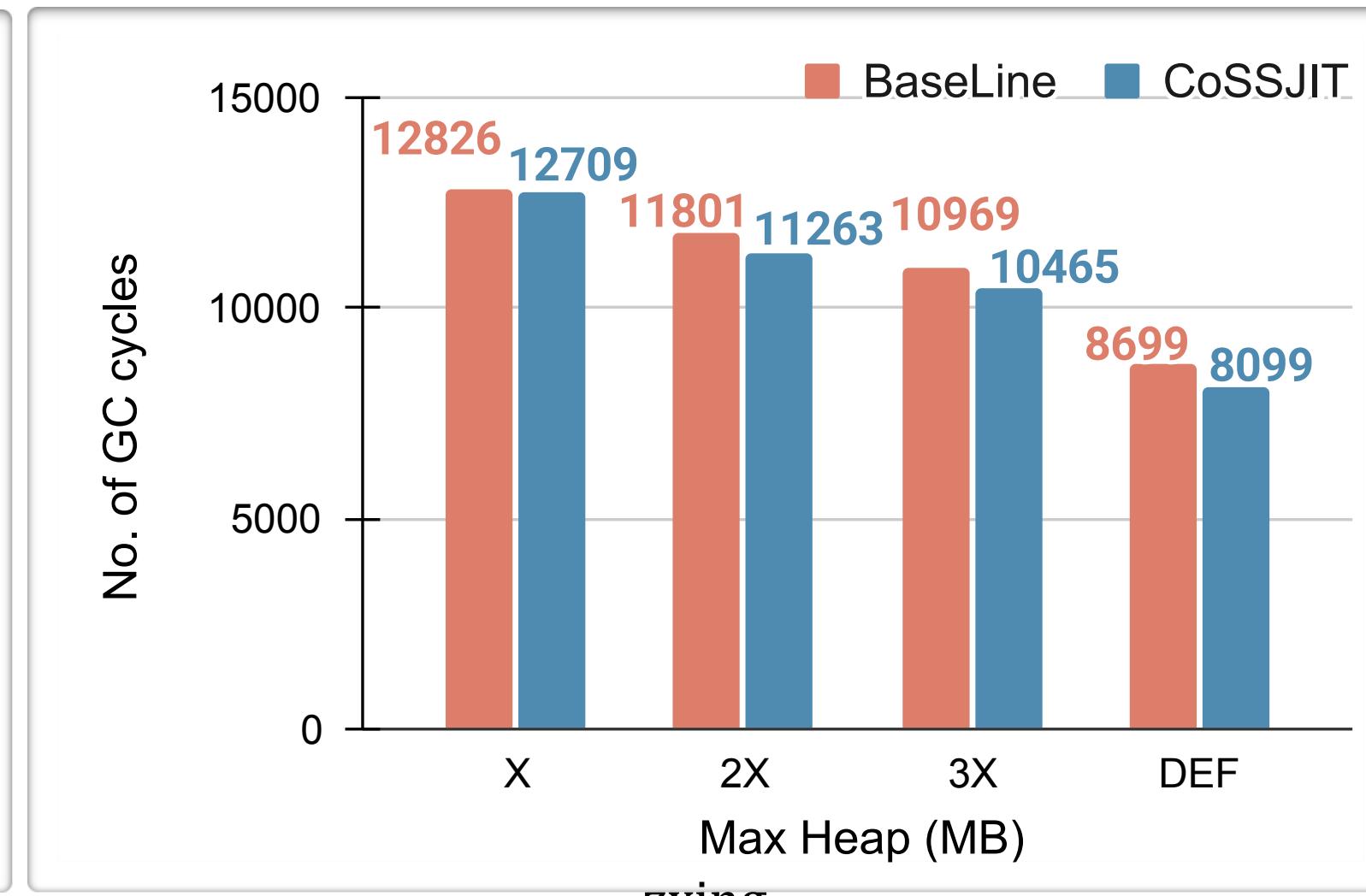
lusearch



sunflow

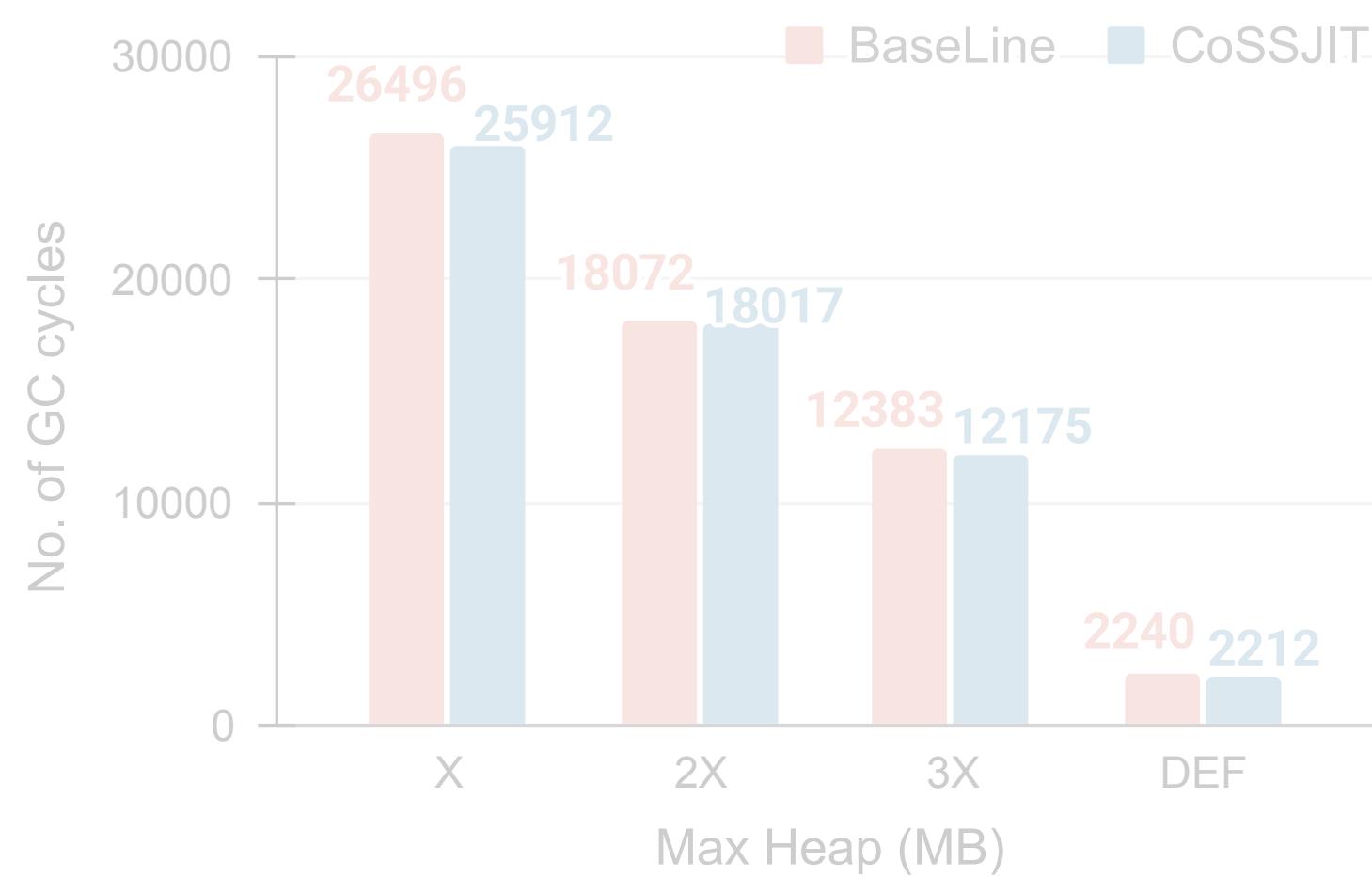


xalan

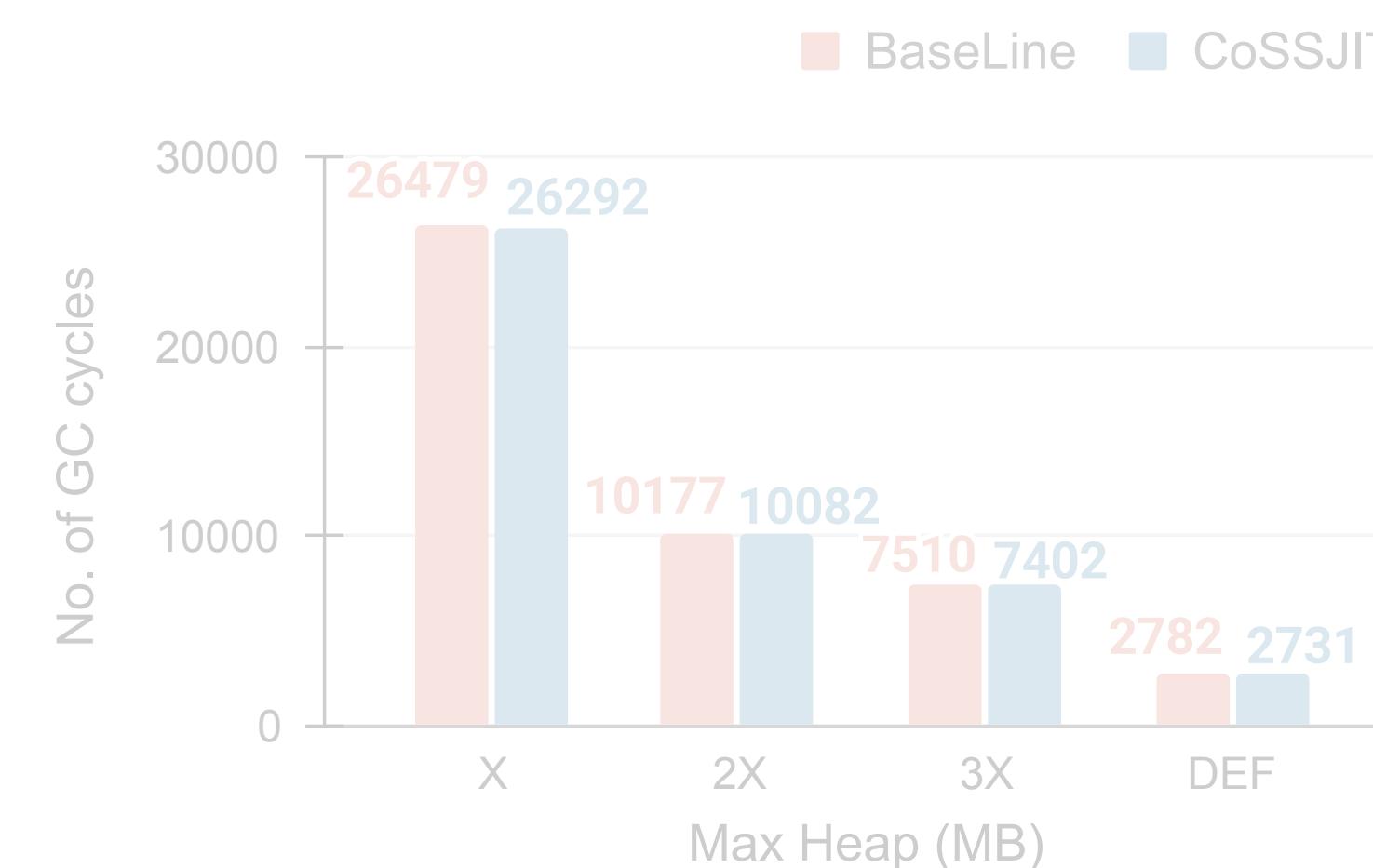


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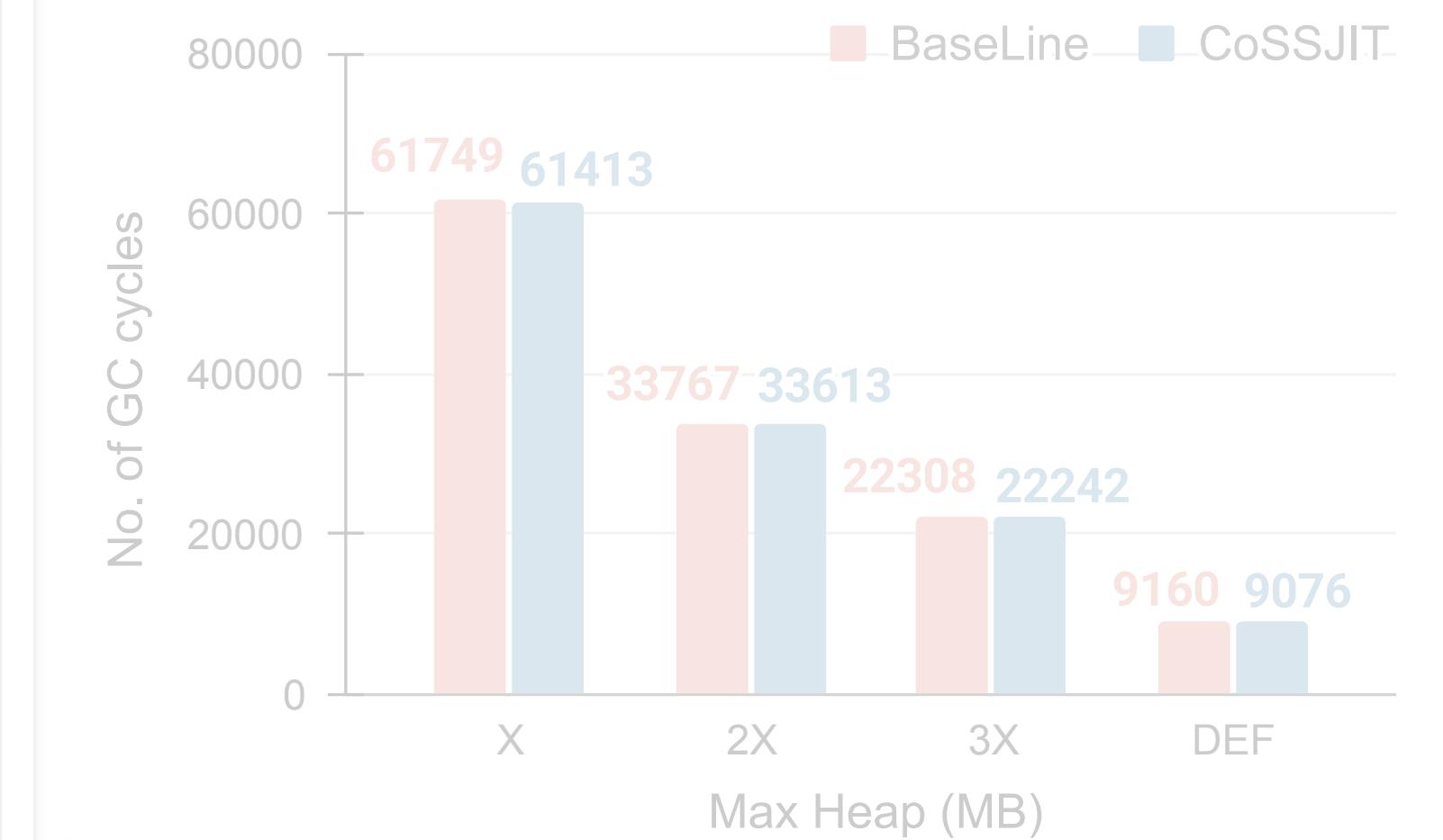
# Garbage Collection



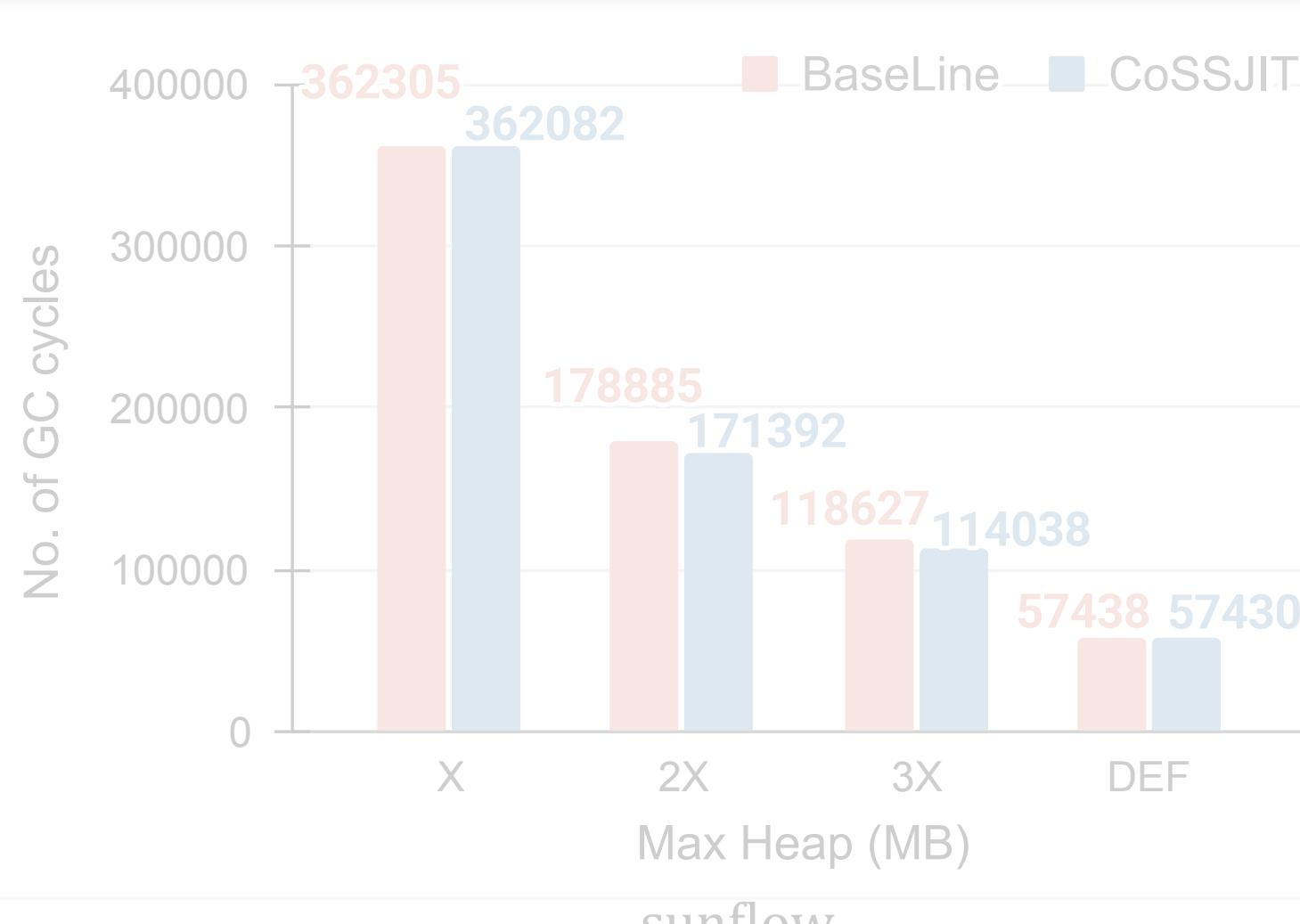
graphchi



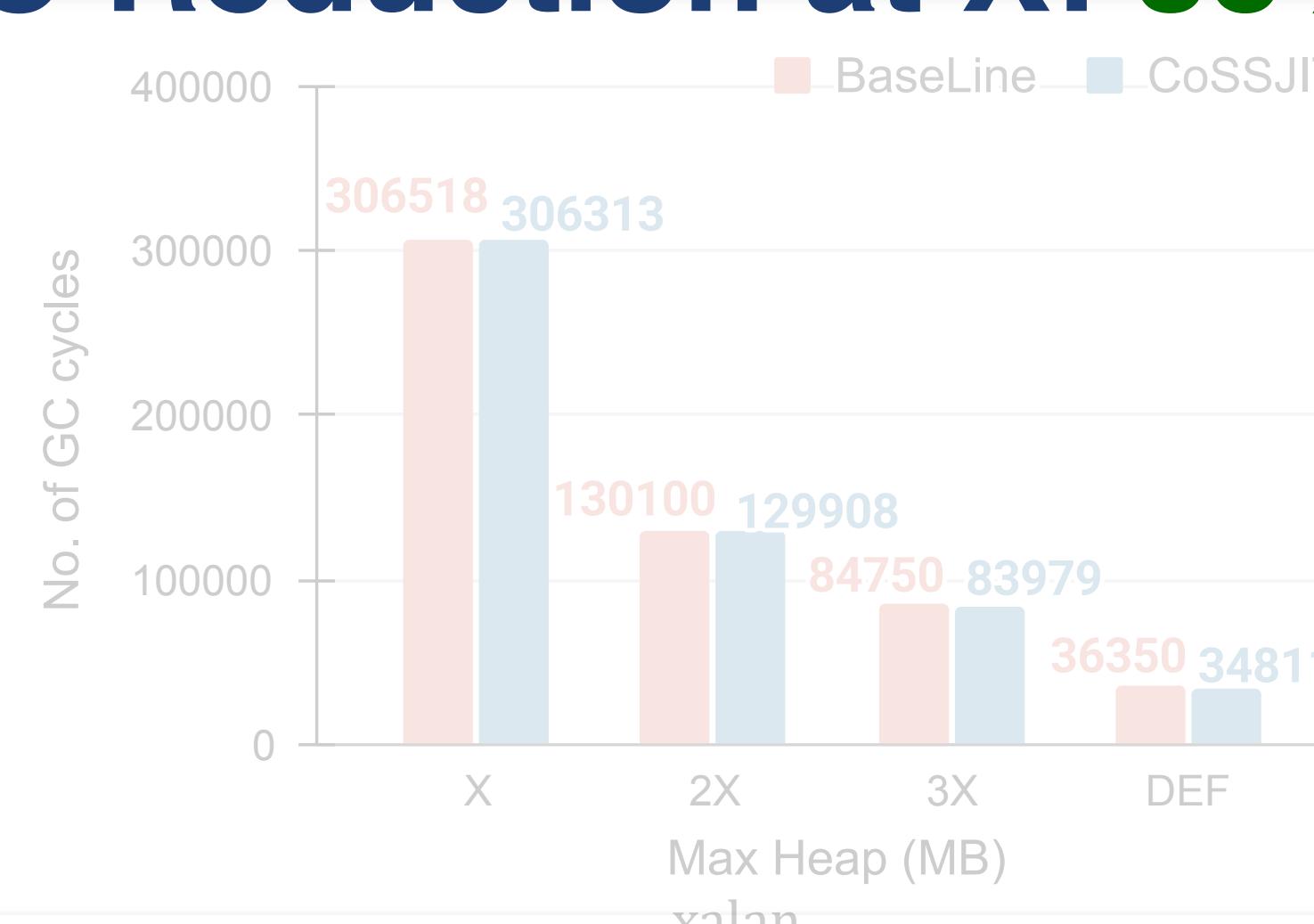
GC Reduction at X: 35% ↓



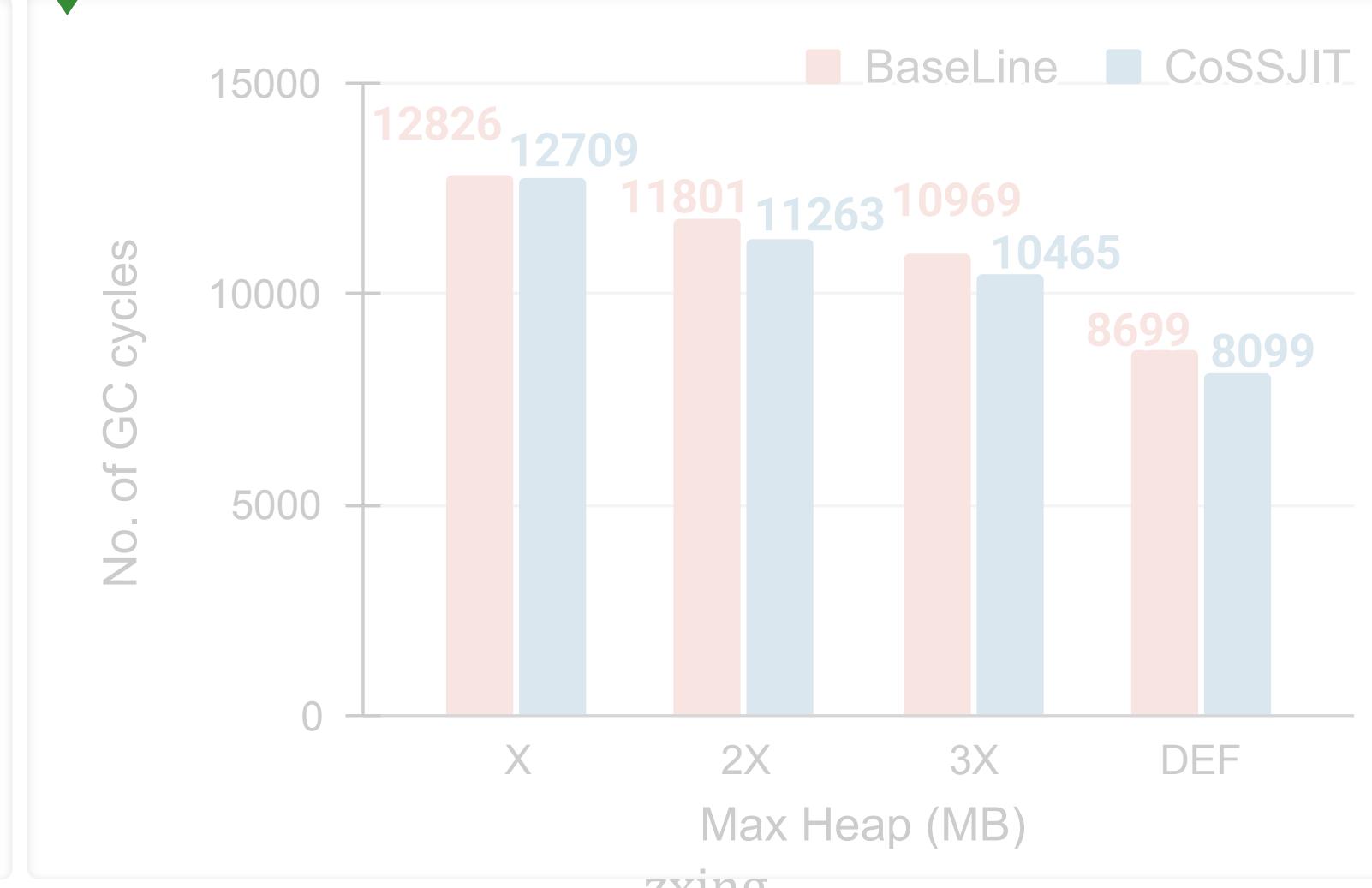
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sunflow

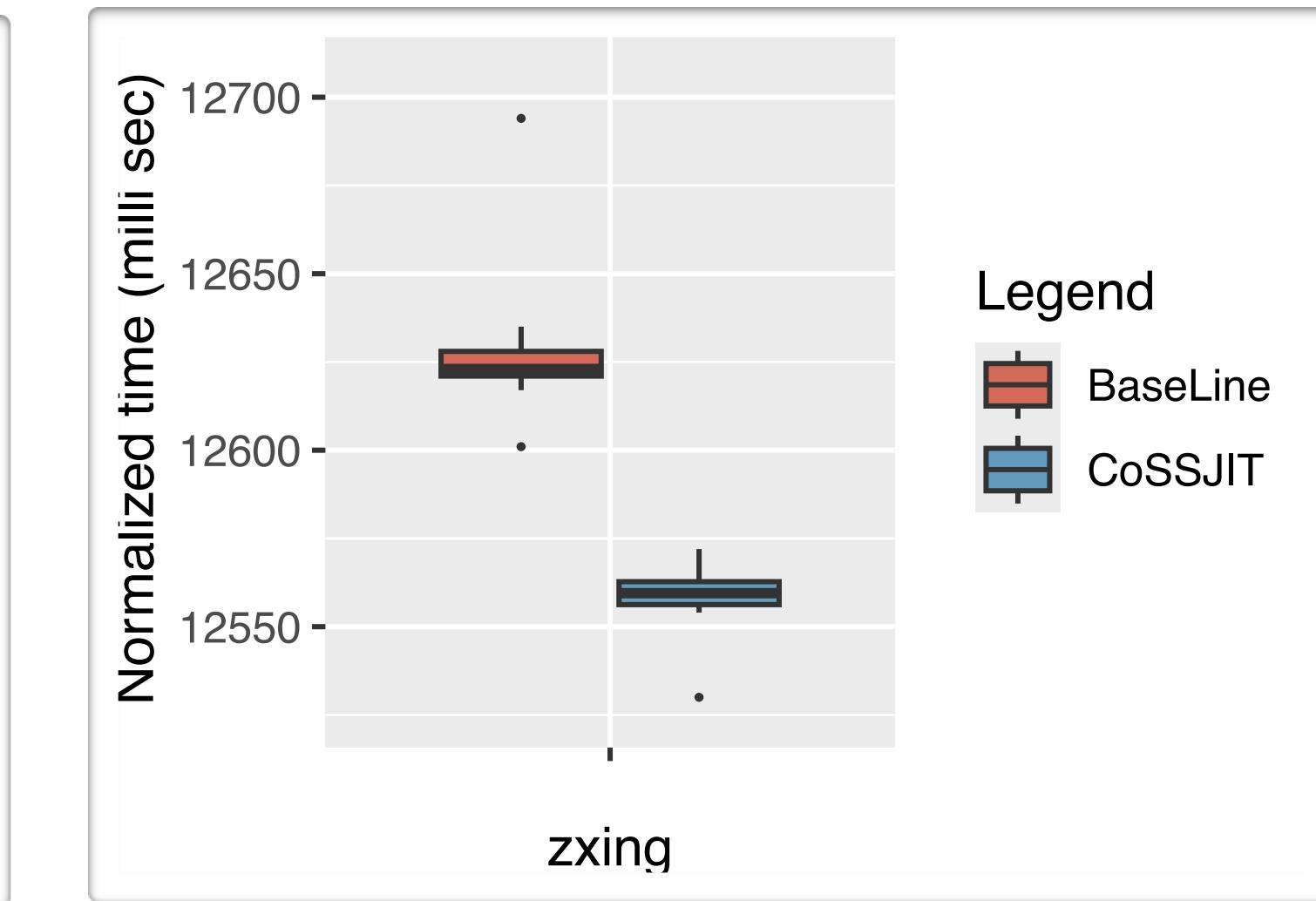
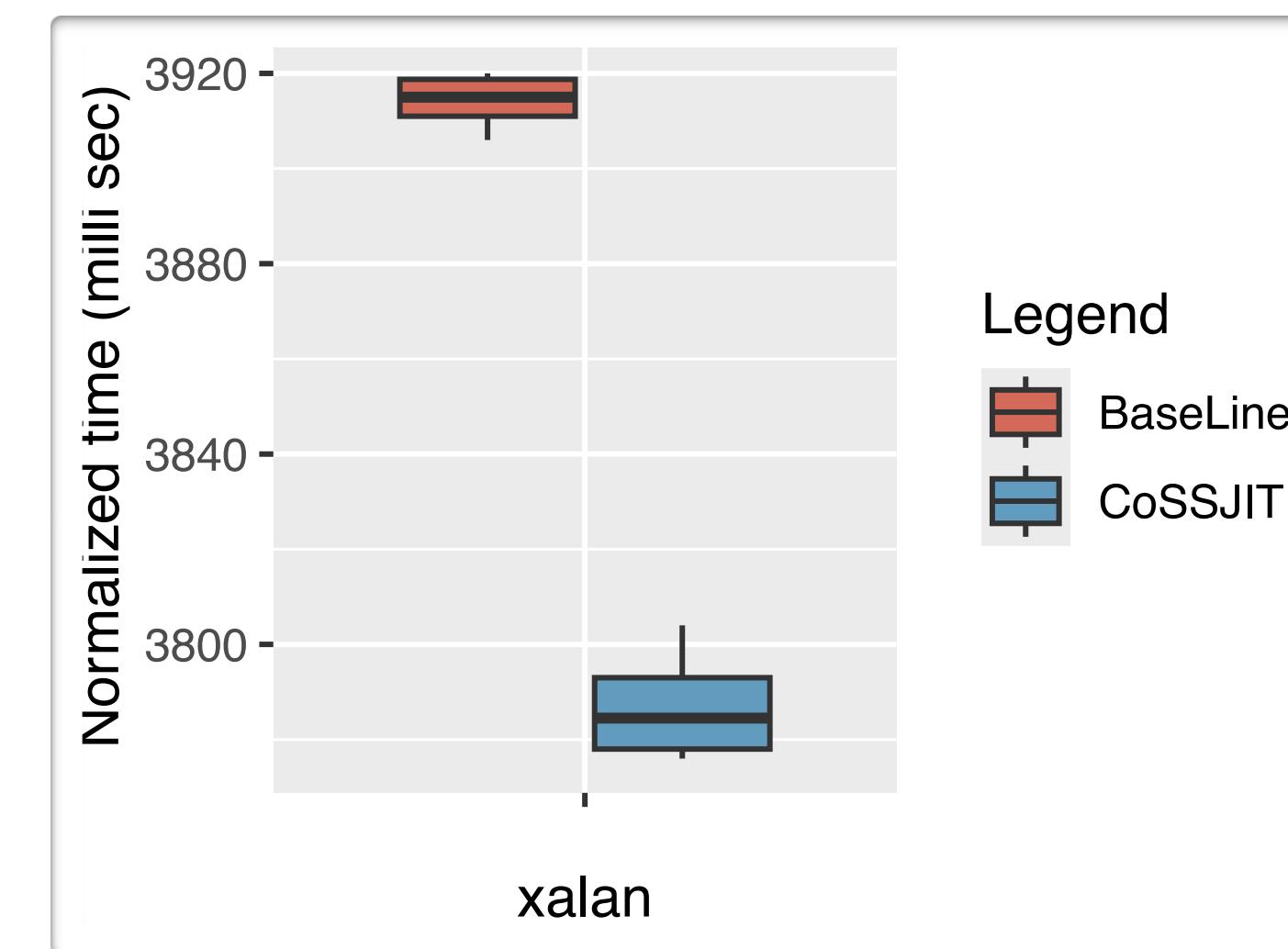
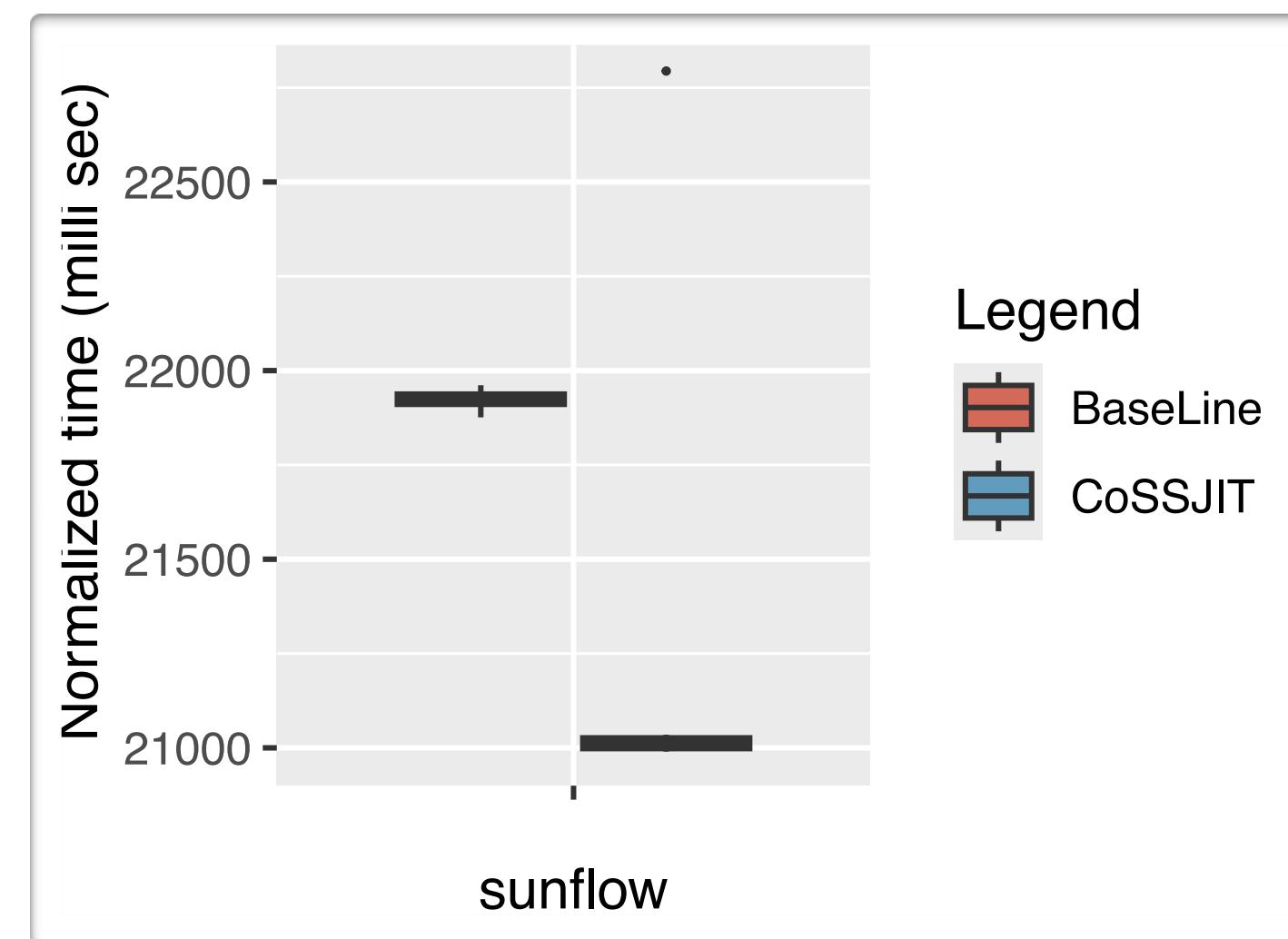
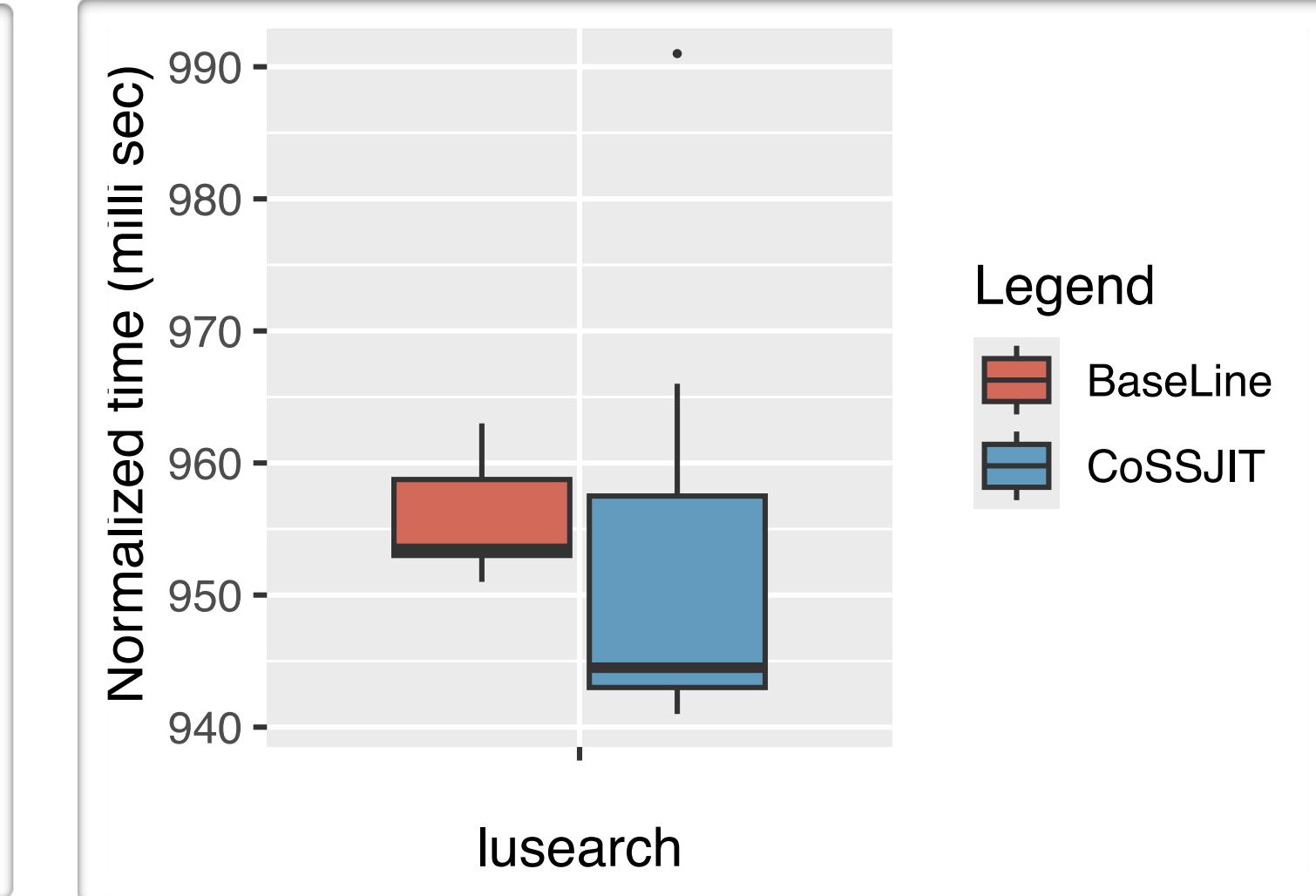
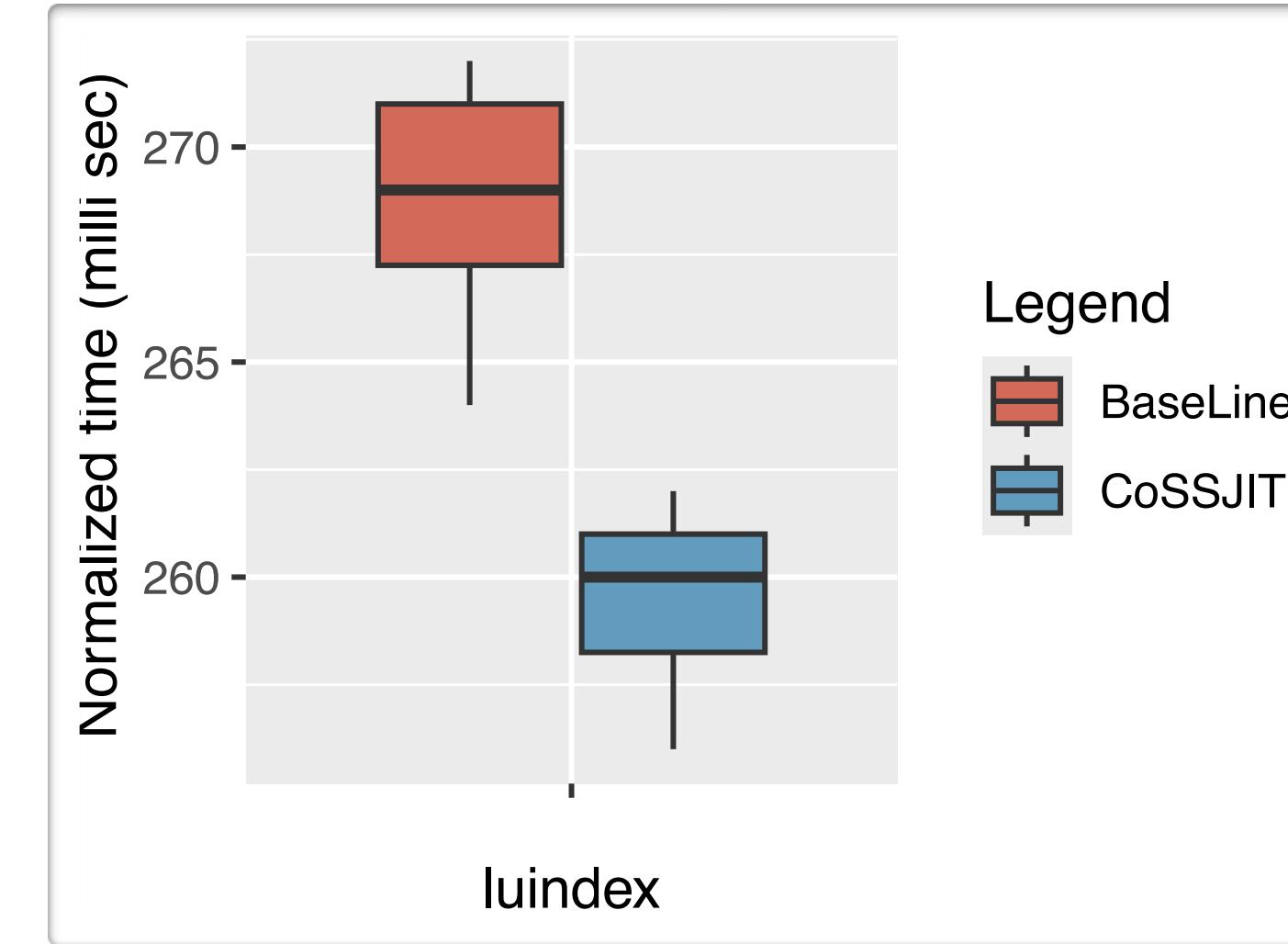
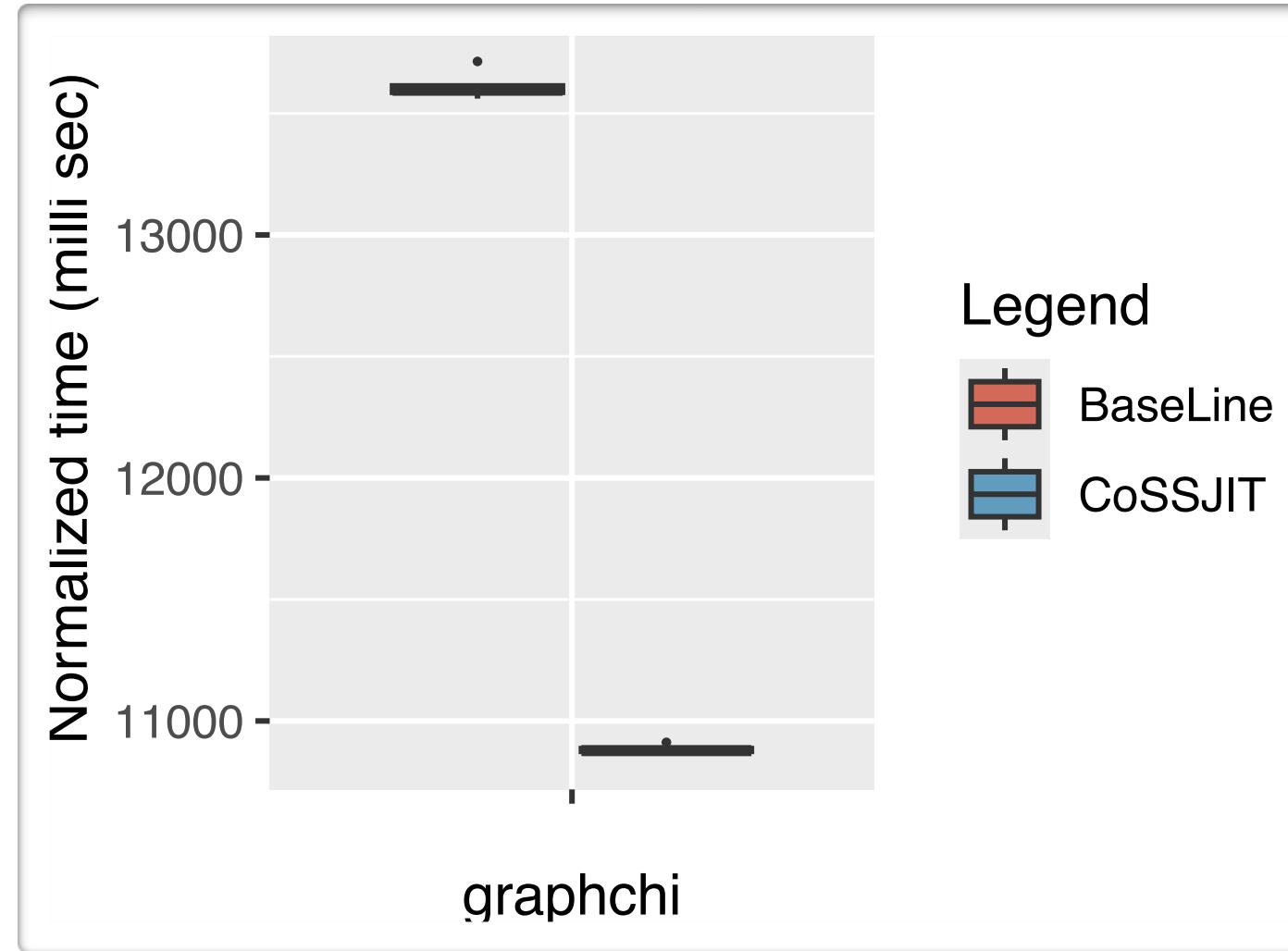


xalan

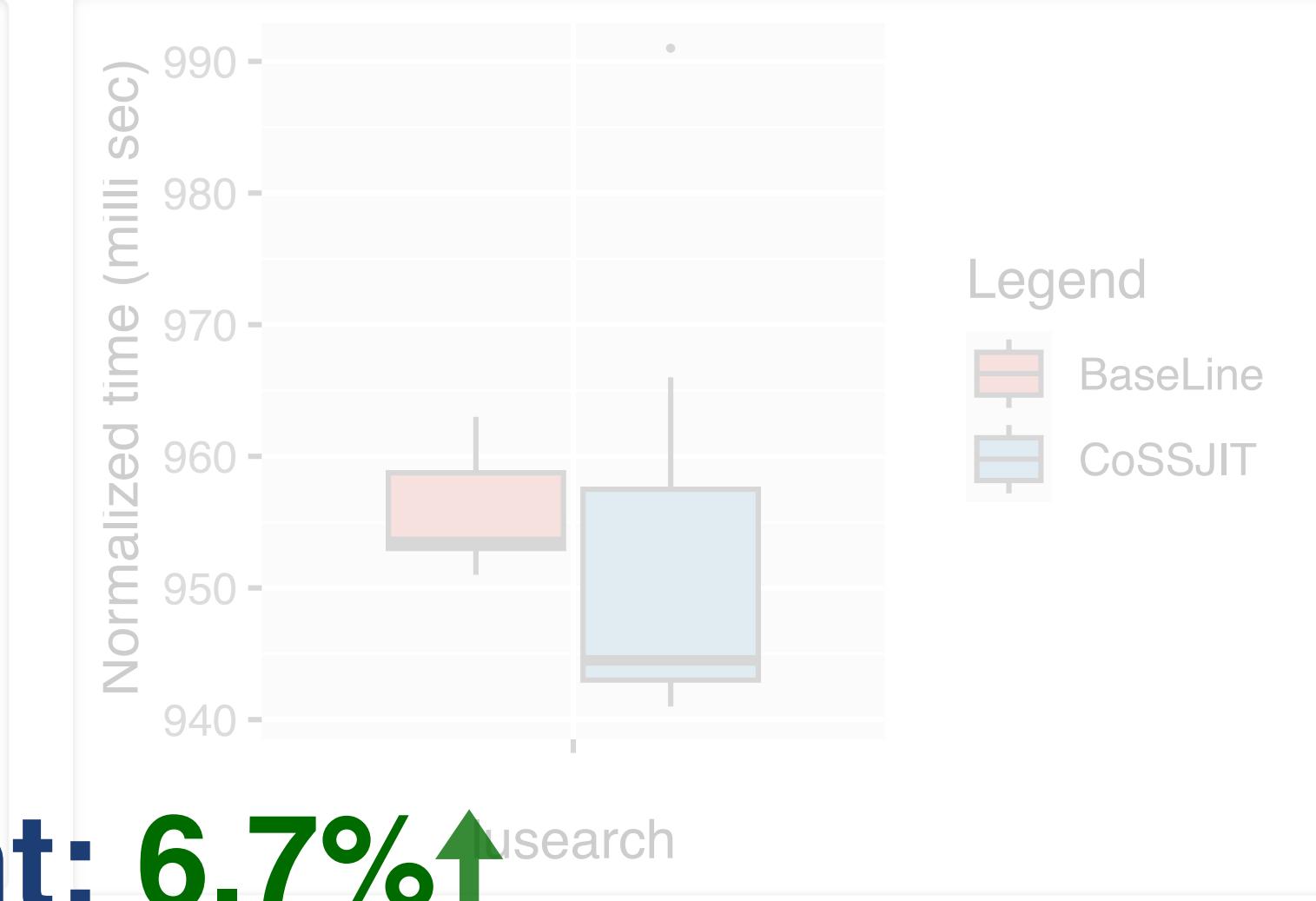
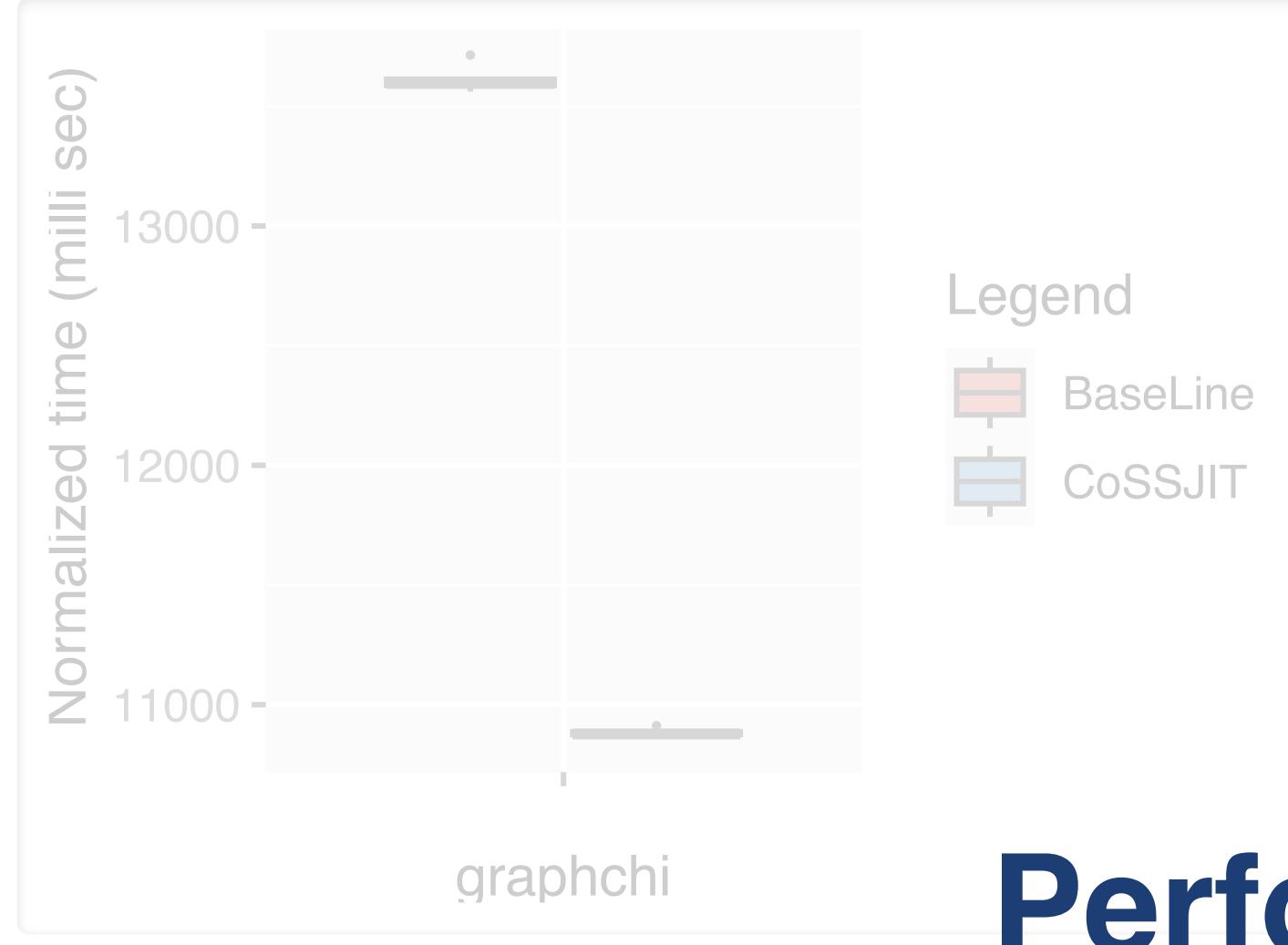


zxing

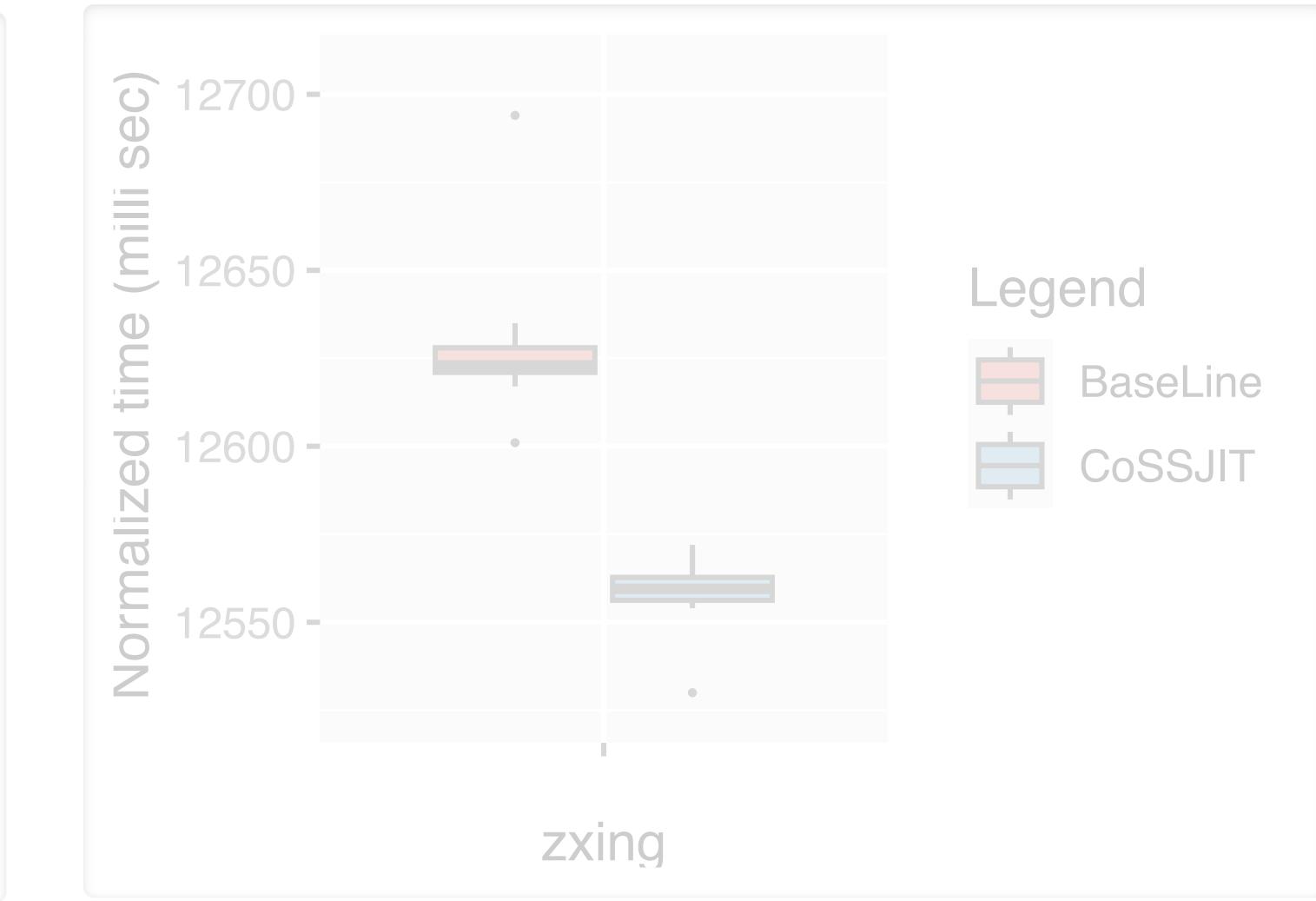
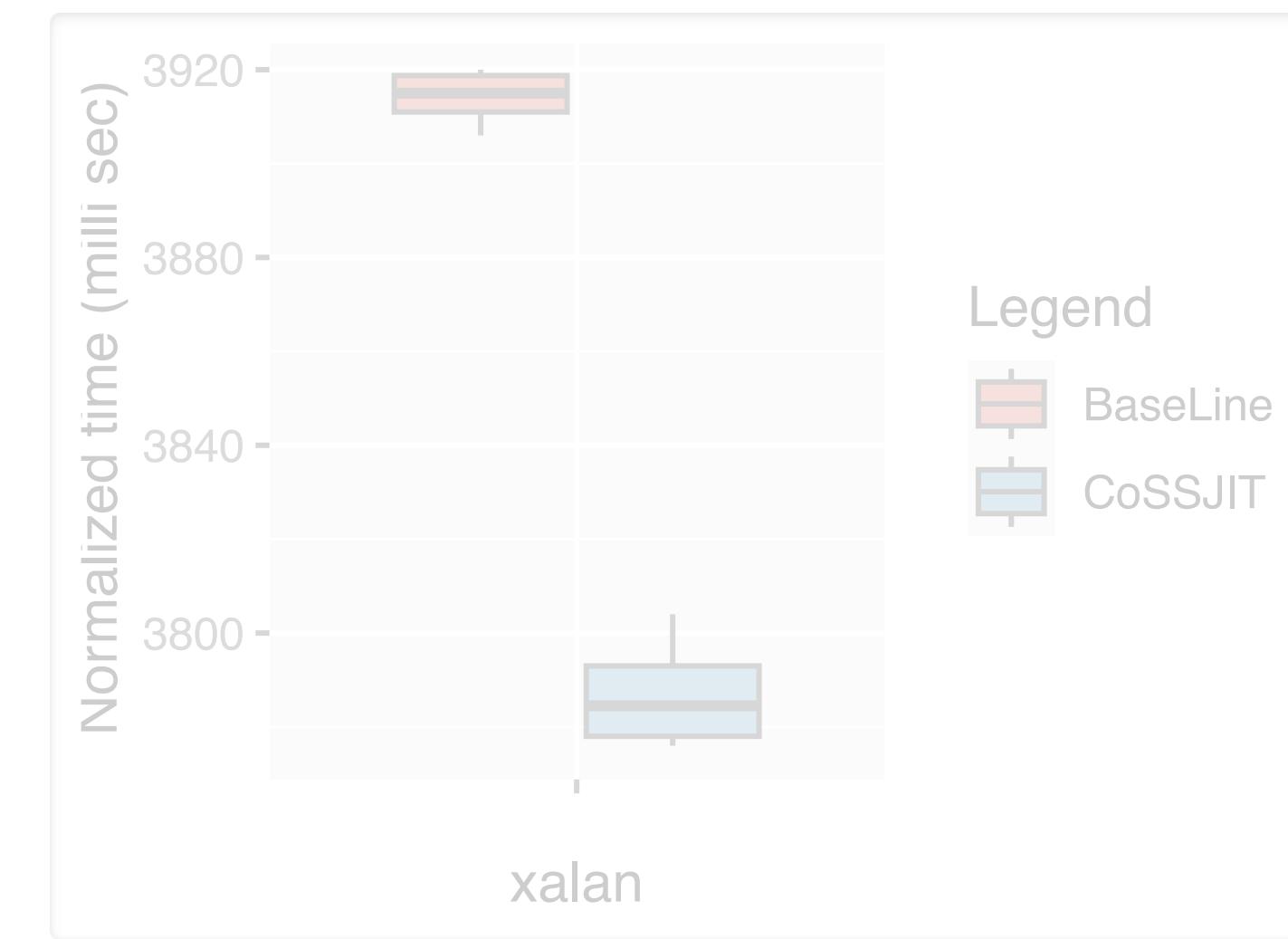
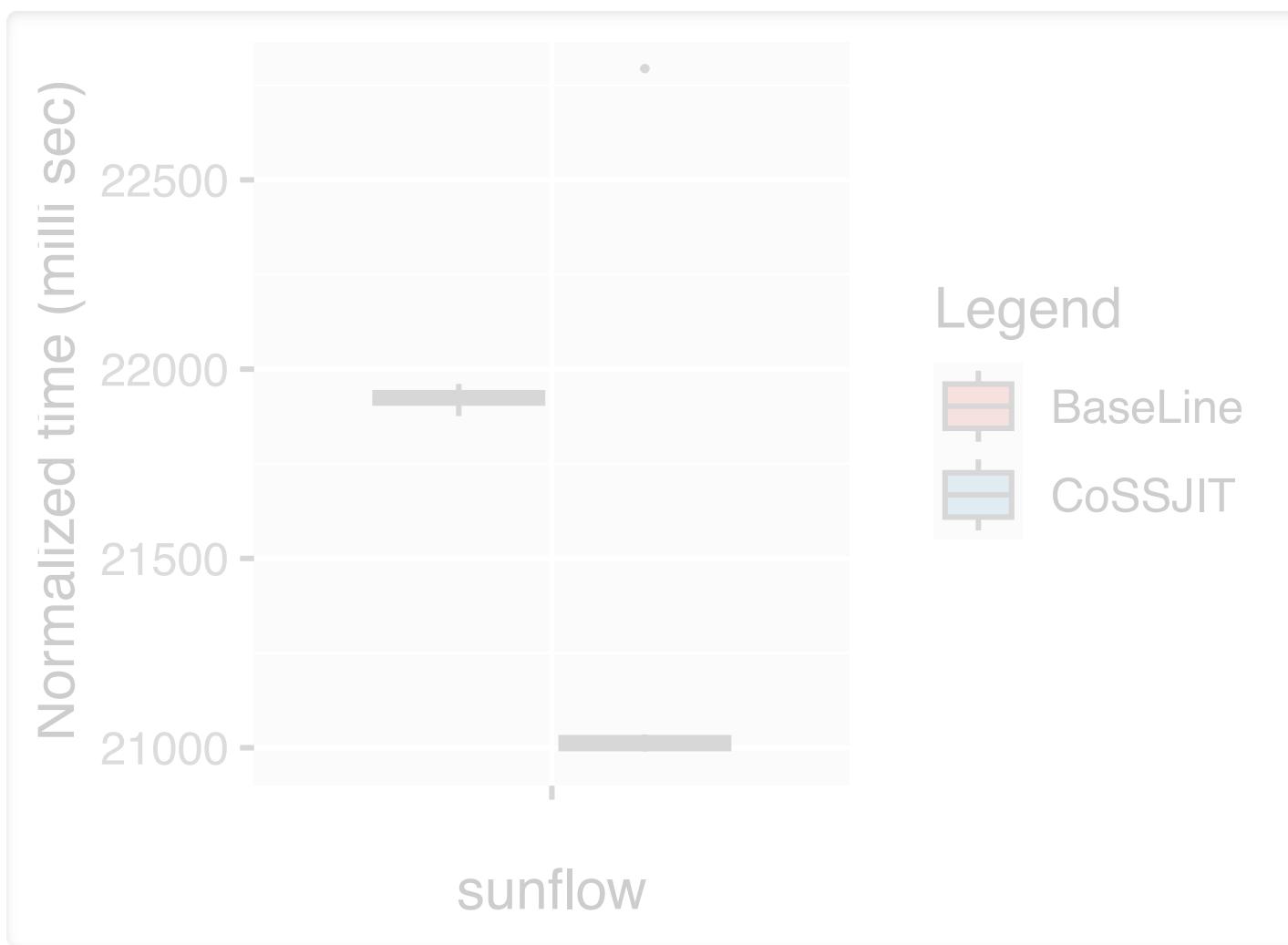
# Performance Improvement



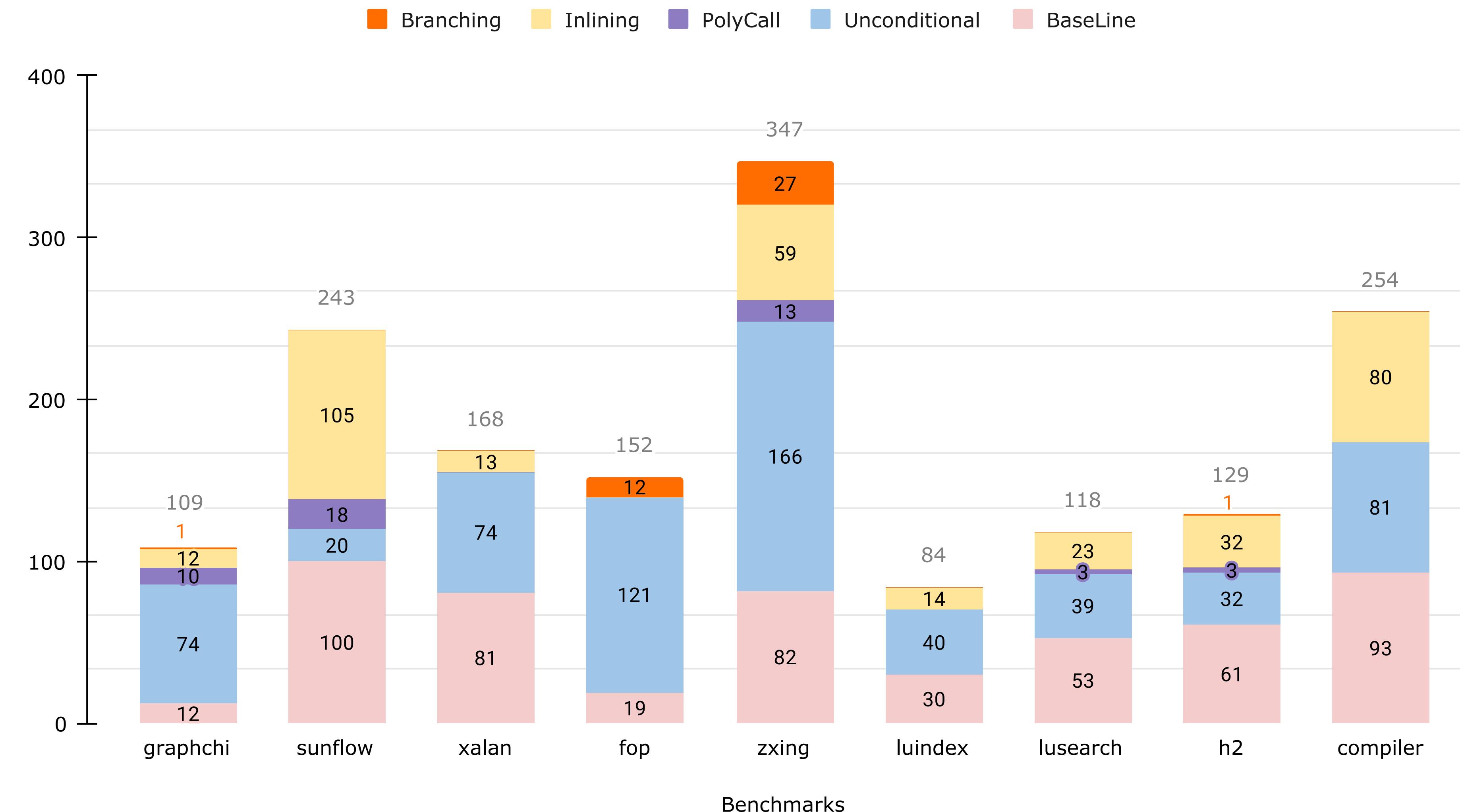
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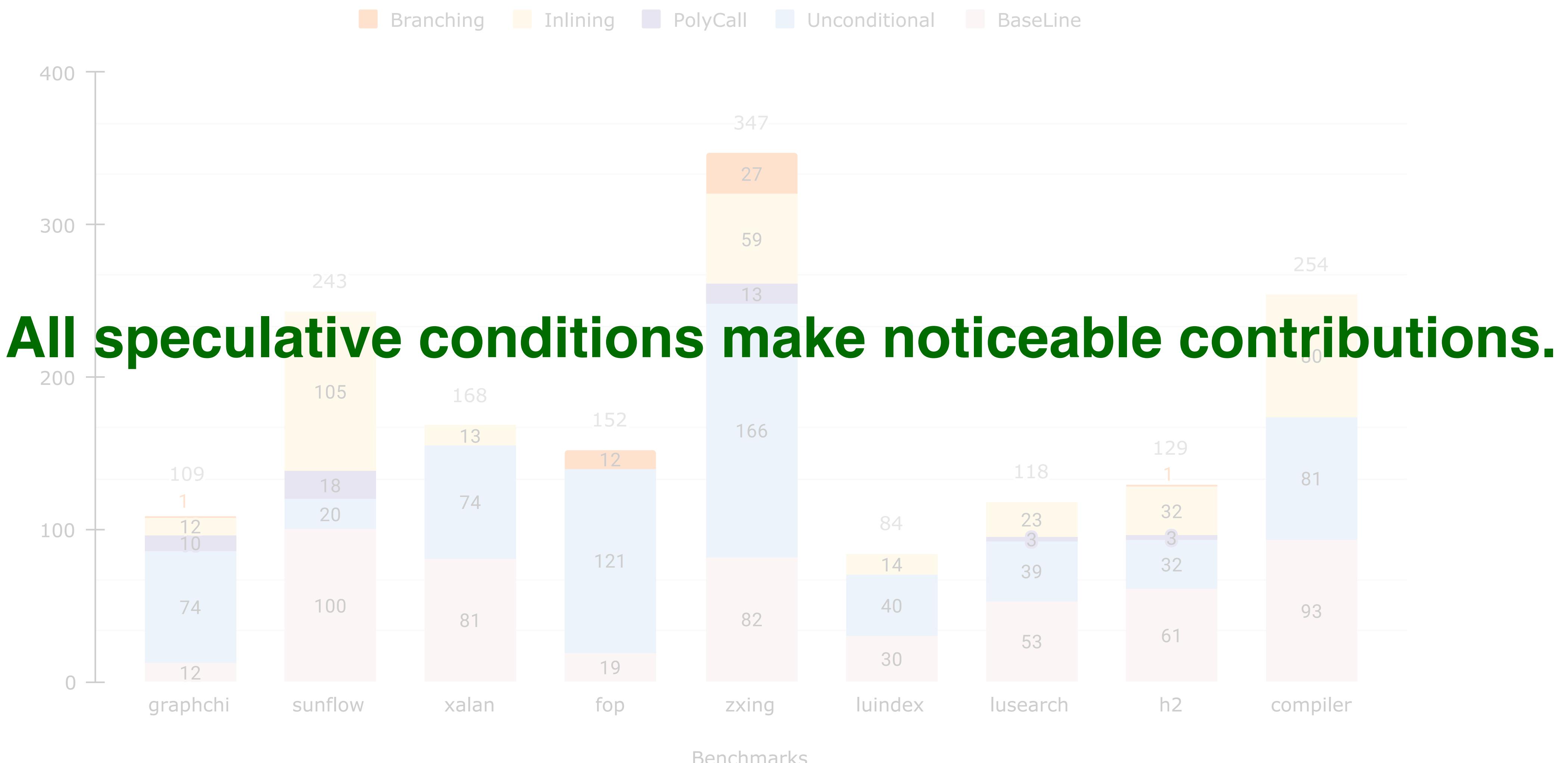
**Performance Improvement: 6.7%↑**



# Contributions by different Speculative Conditions

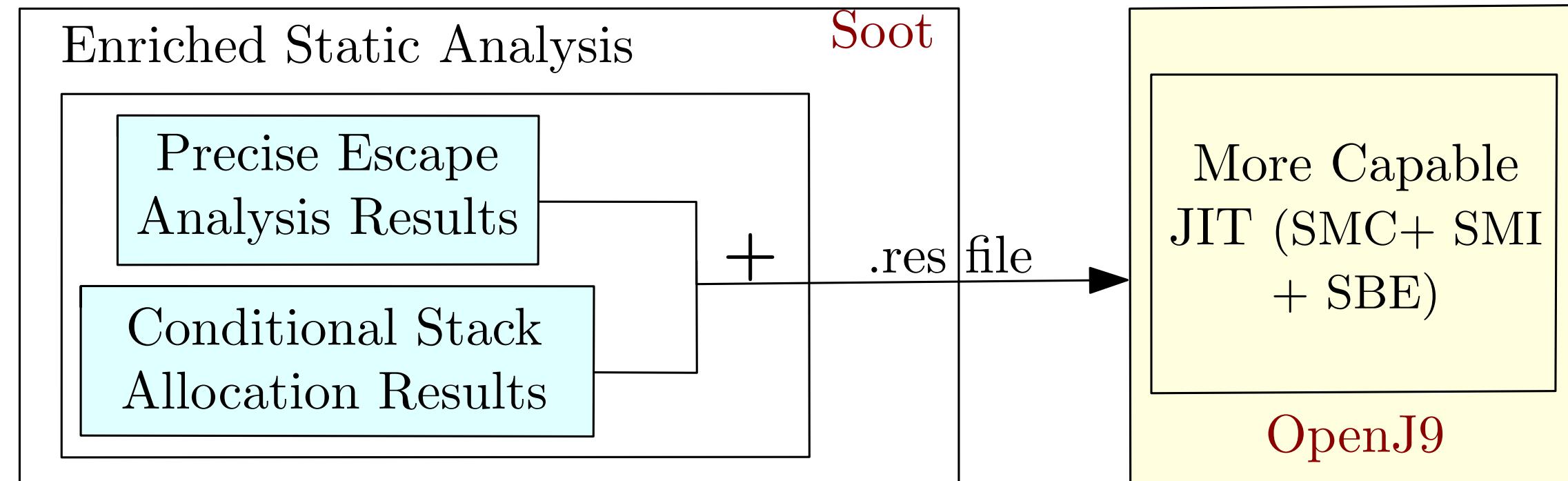


# Contributions by different Speculative Conditions



# CoSSJIT: Take Aways

- Enriched the static analysis with possibility of speculation at run-time.
- Mechanism in the JIT compiler to incorporate the conditional static analysis results.



- Overall, one of the first approaches that strike a balance between static analysis and JIT speculation, harnessing the best of both the worlds.

# CoSSJIT: Take Aways



## CoSSJIT: Combining Static Analysis and Speculation in JIT Compilers

ADITYA ANAND, Indian Institute of Technology Bombay, India

VIJAY SUNDARESAN, IBM Canada Lab, Canada

DARYL MAIER, IBM Canada Lab, Canada

MANAS THAKUR, Indian Institute of Technology Bombay, India

Just-in-time (JIT) compilers typically sacrifice the precision of program analysis for efficiency, but are capable of performing sophisticated speculative optimizations based on run-time profiles to generate code that is specialized to a given execution. On the contrary, ahead-of-time static compilers can often afford precise flow-sensitive interprocedural analysis, but produce conservative results in scenarios where higher precision could be derived from run-time specialization. In this paper, we propose the first-of-its-kind approach to enrich static analysis with the possibility of speculative optimization during JIT compilation, as well as its usage to perform aggressive stack allocation on a production Java Virtual Machine (JVM).

Our approach of combining static analysis with JIT speculation – named CoSSJIT – involves three key contributions. First, we identify the scenarios where a static analysis would make conservative assumptions but a JIT could deliver precision based on run-time speculation. Second, we present the notion of “speculative conditions” and plug them into a static interprocedural dataflow analyzer (whose aim is to identify heap objects that can be allocated on stack), to generate partial results that can be specialized at run-time. Finally, we extend a production JIT compiler to read and enrich static-analysis results with the resolved values of speculative conditions, leading to a practical approach that efficiently combines the best of both worlds. Cherries on the cake: Using CoSSJIT, we obtain 5.7 $\times$  improvement in stack allocation (translating to performance), while building on a system that ensures functional correctness during JIT compilation.



# CoSSJIT: Take Aways



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## Thank You !!