Aditya Wagh

Curriculum Vitae

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On a quest to make intelligent machines!

EDUCATION

Aug '15 - May '19 BITS Pilani, B.E.(Hons) in Electronics and Instrumentation Engineering.

EXPERIENCE

Professional

May '17 - Jul '17 The Ramco Cements Ltd, Jaggayapeta, India.

Software Engineering Intern ADVISOR: Mr. Chengal Rao

FOCUS: Full-stack design of an inventory management web-application.

May '18 – Jul '18 Apras Polymers & Engineering Co.Pvt. Ltd, Nashik, India.

Software Engineering Intern
ADVISOR: Mr. Prakash Dhanokar

FOCUS: Modern and responsive business website design.

Academic

Dec '18 - May '19 Central Electronics Engineering Research Institute, Pilani, India.

Undergraduate Research Assistant ADVISOR: Dr. Sumeet Saurav

FOCUS: ConvNet models for orientation-aware object detection on aerial images.

Jul '18 - Dec '18 Central Electronics Engineering Research Institute, Pilani, India.

Undergraduate Research Intern *ADVISOR*: Dr. Sumeet Saurav

FOCUS: Instance segmentation models for real-time power-cable fault detection using autonomous drones.

Aug '17 - Dec '17 BITS Pilani, Pilani, India.

Undergraduate Research Assistant ADVISOR: Prof. Anu Gupta

FOCUS: Design of fault tolerant Static RAM cells.

PROJECTS

2017 Variable Computation in Recurrent Neural Networks:

- Modified a RNN model to reduce the amount of required computation by ignoring redundant sequences.
- Implemented a scheduler for the RNN unit which decides the computation required at the current time-step.
- Reduced the number of operations for bit-level language modelling by 50%.

2019 RISC Processor Design using HDL

- Designed, programmed and tested 16-bit multi-cycle processor for a given set of instructions and constraints.
- Followed modular approach for programming the processor and its test bench, by dividing it into sub-modules such as memory, register file, ALU and control unit.

2018 FIR flter design using an adjustable window function.

- Implemented an adjustable window function based on the combination of Blackman and Lanczos window.
- Achieved a 75% better side-lobe roll off ratio than Lanczos window.

TECHNICAL SKILLS

 $\textbf{Languages} \quad \mathsf{Python} \cdot \mathsf{C} \cdot \mathsf{C}{+}{+} \cdot \mathsf{MATLAB} \cdot \mathsf{Shell} \cdot \mathsf{LATE} \mathsf{X} \cdot \mathsf{HTML} \cdot \mathsf{CSS} \cdot \mathsf{JavaScript}$

 $\textbf{Frameworks} \quad \mathsf{Keras} \, \cdot \, \mathsf{TensorFlow} \, \cdot \, \mathsf{PyTorch} \, \cdot \, \mathsf{OpenCV} \, \cdot \, \mathsf{Bootstrap} \, \cdot \, \mathsf{Git}$

Softwares Simulink · LTspice · LabVIEW · EagleCAD · Proteus

RELEVANT COURSEWORK

 $\textbf{Mathematics} \quad \text{Linear Algebra} \quad \text{Vector Calculus} \quad \text{Differential Equations} \quad \text{Probability \& Statistics} \quad \text{Discrete}$

Mathematics

 $\textbf{Data Science} \quad \mathsf{Machine \ Learning} \cdot \mathsf{Deep \ Learning} \cdot \mathsf{TensorFlow \ in \ Practice} \cdot \mathsf{Mathematics \ for \ Machine \ Learning} \cdot \mathsf{Deep \ Learni$

Neural Networks & Fuzzy Logic

 $\textbf{EECS} \quad \text{Control systems} \cdot \text{Signals} \,\,\&\,\, \text{Systems} \,\,\cdot\,\, \text{Digital Signal Processing} \,\,\cdot\,\, \text{Digital Electronics} \,\,\cdot\,\, \text{Analog}$

Electronics · Microprocessors & Interfacing

ACHIEVEMENTS

2015 Amongst the top 1.5% students in the India in Joint Entrance Examination - Mains

2015 Amongst the top 5% students in the India in Joint Entrance Examination - Advanced

2014 Amongst the top 300 students selected for INPhO (Indian National Physics Olympiad)

EXTRACURRICULARS

Jul '17 - May '18 IEEE Student Branch, BITS Pilani, Vice-Chairperson, Pilani, India.

Organized technical workshops, talks, and various student events throughout the year.

Aug '18 - Jul '19 Society for Students Mess Services, BITS Pilani, Governing Council Member, Pilani, India.

Responsible for drafting the food menu and coordinating with third party contractors to prepare food. Also drafted a quality, health & safety environment framework and conducted regular safety audits.

REFERENCES

Dr. Syed Zafaruddin, Assistant Professor, BITS Pilani

Dr. Sumeet Saurav, Scientist, Intelligent Systems Group, CEERI Pilani