CT3 SD

D'As electrons diffuse across the jenution of

point is reached where the orgathre charge sepels any
farther diffusion of electrons, potential difference organized

to more electrons through dectric field is Borrier potential

$$\frac{m_p}{n_n} = \frac{p_n}{p_r} = \exp\left(-\frac{eV_0}{kT}\right)$$

$$m_n = N_D, \quad m_p = \frac{n_i^2}{N_A} \qquad M_p \neq \text{ south of acceptors}$$

$$V_0 = \frac{k_B T}{e} \ln\left(\frac{N_A N_D}{n_i^2}\right) \qquad N_D \Rightarrow \text{ cover of acceptors}$$

b) 
$$V_b = \frac{K_{BT}}{e} lon \left( \frac{n_a n_b}{n_{\ell^2}} \right) . , \frac{n_{\ell^2}^2}{n_{M}} = \frac{2 s_b^2 (1.5 \times 10^{10})^2}{108}$$

$$= 0.026 lon \left( \frac{10^{15}}{2.25 \times 10^2} \right) = 2.25 \times 10^2 .$$

$$= 0.757 \text{ volts}.$$

Congenialed Semiconductor , a semiconductor in which donors and acceptors are related in such a which donors and acceptors are related in such a way that their opposing electrical effects are perhally cancelled.

Change neutrality is expressed by equality for density of regative charges to that of positive,

No + Na = po + Nd +

where no, to an friend-aguilibrium concer of electron and holy,

$$n_0 = (N_0 - N_0) + \sqrt{(N_0 - N_0)^2 + 9_1^2}$$

b) 
$$\frac{D_p}{u_p} = \frac{\kappa T}{q}$$

$$\frac{Dp}{1000} = 26 \text{ m}$$

$$E = \frac{h^2h^2}{8ml^2}$$
,  $M = \sqrt{\frac{8ml^2E}{h^2}}$ 

2ndn = 
$$8\frac{mL^2}{h^2}dE$$
 $du = (\frac{1}{4h})(\frac{8mL^2}{h^2})dE$ 
 $du = \frac{1}{2}(\frac{8mL^2}{h^2})^{1/2}dE$ 
 $R(E)dE = \frac{1}{2} \times \frac{8mL^2}{h^2}E \times \frac{1}{2}(\frac{8mL^2}{h^2})^{1/2}dE$ 
 $R(E)dF) = \frac{2h}{h^3}(2m)^{3/2}L^3E^{1/2}dE$ 

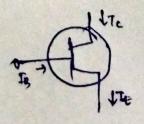
b) 
$$N_V = 1.04 \times 10^{25} \left( \frac{400}{300} \right)^{3/2} = 1.6 \times 10^{25} / \text{m}^3$$
  
 $KT = 0.0259 \left( \frac{400}{300} \right) = 0.03453 \text{ eV}$ 

$$P^{2} \text{ Ny exp} \left( -\frac{EF - EV}{KT} \right)$$
= 1.6x 10<sup>25</sup> e  $\left( -\frac{27}{0.03453} \right)$ 
= 6.43 x 10<sup>19</sup> / m<sup>3</sup>

En BIT, eperation is dependent on both the charge carriers.

offset voltage is required, and congramption of

power is more, gain is more.



En FET, the apost performed is due to the majority of constitution, no requirement for its vallage control. construption of power is less and least the goin, lessen the value of output impedence.

b) 
$$Z = 0.95$$
,  $IE = \frac{2}{3000}$ 
 $A = \frac{\pi}{4}$ 
 $\pi = ATE = 0.95 \times \frac{2}{3000}$ 
 $I_E = T_S + T_C$ 
 $I_B = I_E - T_C = \frac{2}{3000} - \frac{0.95 \times 2}{3000}$ 
 $I_{A} = \frac{2}{3000} - \frac{0.95 \times 2}{3000}$ 

AND PROPERTY.