

CMPE283 – Virtualization  
Quiz 1

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Select the best answer for each question numbered 1-5. 2 Points each.

1. A Under what circumstances will a processor wake from a HLT instruction?
  - A. A non-masked interrupt occurs
  - B. A system call occurs
  - C. A process context switch occurs
  - D. Either B or C can cause a wake
  - E. None of A, B, or C
  
2. A In a non-virtualized environment, the “move to CR3” instruction flushes which of the following? (For the purposes of this question, assume a unified data/instruction TLB).
  - A. All non-global translations from the processor's TLB
  - B. All translations from the processor's TLB
  - C. All non-global translations from all processors' TLBs
  - D. All translations from all processors' TLBs.
  - E. None of the above
  
3. C Which of the following operations does legacy system BIOS typically perform during startup?
  - A. Configuring paging
  - B. Determining process scheduling characteristics
  - C. Booting secondary processors
  - D. None of A, B, or C
  - E. Answers A, B, and C are all operations performed by the BIOS during startup
  
4. True True or False? VMX mode provides the virtualization systems architect with the capability to detect previously non trappable sensitive instructions?
  
5. False True or False? Kernel area address translations present in a process' page directory (page tables) must be global translations?