FIRST YEAR

ELECTRONICS ENGINEERING

SEMESTER - I

STUDENT'S NAME: Vipin Vasantkumar Bodele

UNIVERISTY OF MUMBAI SEAT NO: 4284

DATE OF PASSING: December 2005

SUBJECTS	LECTS & TUTS /						CAL		ORA	L		NAL MENT					
	WEEK	VVLLIX	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT
Applied Mathematics - I	5	0	-	-	-	100	40	40	-	-	-	-	-	-	-	-	-
Applied Sciences - I	4	0	25	10	22	100	40	50	-	-	-	-	-	-	-	-	-
Basic Electricity & Electronics Engg.	6	3	25	10	22	100	40	45	-	-	-	-	-	-	-	-	-
Basic work shop - I	0	4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Computer Programming - I	4	3	25	10	19	100	40	40	-	-	-	-	-	-	-	-	-
Engineering Mechanics	6	3	25	10	22	100	40	57	-	-	-	-	-	-	-	-	-
Total	25	13	100	40	85	500	200	232	0	0	0	0	0	0	0	0	0

TOTAL OF SEMESTER I: 317 / 600 #

PERCENTAGE = 52.83%

DATE OF PASSING: May 2006

SEMESTER - II

UNIVERISTY OF MUMBAI SEAT NO: 3515

INTERNAL LECTS & TERM WORK **THEORY PRACTICAL ORAL** PRACS/ **SUBJECTS ASSESMENT** TUTS / WEEK WEEK MAX MIN OBT Applied Mathematics - II 5 0 100 40 46 4 Ω 25 10 22 100 50 _ _ _ Applied Sciences - II 40 _ _ Basic work shop - II 0 4 100 40 75 _ _ _ _ _ _ Communication Skills 2 0 25 10 16 100 40 58 _ Computer Programming - II 4 3 25 10 22 100 40 48 **Engineering Drawing** 4 25 18 100 Total 19 13 200 80 153 500 200 279 0 0 0 0 0 0 0 0 0

TOTAL OF SEMESTER II: 432 / 700 #

PERCENTAGE = 61.71%

TOTAL OF SEMESTER I & II = 749/1300

PERCENTAGE = 57.62%

REMARKS: SECOND CLASS

LEGENDS USED : Lec: Lecture, Prac: Practicals, Min: Minimum, Max: Maximum, Obt: Obtained, #: Passed in One attempt, Engg: Engineering, Tuts: Tutorials, *: Grace Marks, P: Passed, \$: Carried Over

SECOND YEAR

ELECTRONICS ENGINEERING

SEMESTER - III

STUDENT'S NAME: Vipin Vasantkumar Bodele

UNIVERISTY OF MUMBAI SEAT NO: 310

DATE OF PASSING: November 2006

SUBJECTS	LECTS & TUTS / WEEK	PRACS / WEEK	TEF	RM W	ORK	Т	HEOF	RY	PR	ACTI	CAL	ORAL				INTERNAL ASSESMENT		
		*****	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	
Applied Mathematics III	4	0	-	-	-	100	40	57	-	-	-	-	-	-	-	-	-	
Basics of Communication Engineering	3	3	25	10	21	100	40	55	-	-	-	-	-	-	-	-	-	
Digital Design I	3	2	25	10	24	100	40	64	-	-	-	-	-	-	-	-	-	
Electrical Networks Analysis & Synthesis	3	2	25	10	21	100	40	54	-	-	-	-	-	-	-	-	-	
Electronic Circuts Analysis & Design I	4	3	25	10	16	100	40	46	-	-	-	-	-	-	-	-	-	
Numerical Techniques	3	2	25	10	21	100	40	40	-	-	-	-	-	-	-	-	-	
Total	20	12	125	50	103	600	240	316	0	0	0	0	0	0	0	0	0	

TOTAL OF SEMESTER III : 419 / 725 #

PERCENTAGE = 57.79%

SEMESTER - IV

UNIVERISTY OF MUMBAI SEAT NO: 318

DATE OF PASSING: May 2007

SUBJECTS	LECTS & TUTS /	PRACS / WEEK	TEF	TERM WORK			THEORY			PRACTICAL			ORA	L	INTERNAL ASSESMEN		
	WEEK	WEEK	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT
Applied Mathematics IV	4	0	-	-	-	100	40	62	-	-	-	-	-	-	-	-	-
Control Systems Engineering	3	2	25	10	20	100	40	40	-	-	-	25	10	16	-	-	-
Digital Design II	3	2	25	10	22	100	40	63	25	10	22	25	10	21	-	-	-
Electrical Machines & Instruments	3	2	25	10	20	100	40	61	-	-	-	25	10	23	-	-	-
Electronic Circuts Analysis & Design II	4	3	25	10	20	100	40	40	25	10	22	25	10	21	-	-	-
Principles of Microprocessor Systems	3	3	25	10	22	100	40	59	-	-	-	25	10	20	-	-	-
Total	20	12	125	50	104	600	240	325	50	20	44	125	50	101	0	0	0

TOTAL OF SEMESTER IV: 574 / 900 #

PERCENTAGE = 63.78%

TOTAL OF SEMESTER III & IV = 993/1625

PERCENTAGE = 61.11%

REMARKS: FIRST CLASS

LEGENDS USED : Lec: Lecture, Prac: Practicals, Min: Minimum, Max: Maximum, Obt: Obtained, #: Passed in One attempt, Engg: Engineering, Tuts: Tutorials, *: Grace Marks, P: Passed, \$: Carried Over

THIRD YEAR

ELECTRONICS ENGINEERING

SEMESTER - V

STUDENT'S NAME: Vipin Vasantkumar Bodele

UNIVERISTY OF MUMBAI SEAT NO: 316

DATE OF PASSING: November 2007

SUBJECTS	LECTS & TUTS /	PRACS / WEEK				THEORY			PRACTICAL				ORA	L	INTERNAL ASSESMENT		
	WEEK	VVLLIX	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT
Applied Mathematics V	4	0	-	-	-	100	40	48	-	-	-	-	-	-	-	-	-
Continuous Time Signals & Systems	6	0	25	10	18	100	40	42	-	-	-	-	-	-	-	-	-
Electronic Measuring Instrument	3	2	25	10	20	100	40	58	-	-	-	-	-	-	-	-	-
Electronics Workshop	0	3	-	-	-	-	-	-	-	-	-	25	10	22	-	-	-
Engg. Electromagetics	4	0	25	10	19	100	40	40	-	-	-	-	-	-	-	-	-
Microprocessors & Microcontrollers	3	3	25	10	22	100	40	64	-	-	-	-	-	-	-	-	-
Presentation & Communication Tech.	2	2	25	10	18	-	-	-	-	-	-	25	10	20	-	-	-
Total	22	10	125	50	97	500	200	252	0	0	0	50	20	42	0	0	0

TOTAL OF SEMESTER V: 391 / 675 #

PERCENTAGE = 57.93%

SEMESTER - VI

UNIVERISTY OF MUMBAI SEAT NO: 315

DATE OF PASSING: May 2008

SUBJECTS	LECTS & TUTS /	PRACS / WEEK	TEF	RM W	ORK	Т	HEOF	RY	PR	ACTI	CAL	ORAL				INTERNAL ASSESMENT		
	WEEK	WEEK	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	
Analog Integrated Circuits & Applications	4	3	25	10	16	100	40	44	-	-	-	25	10	17	-	-	-	
Communication System	3	3	25	10	19	100	40	43	-	-	-	25	10	19	-	-	-	
Computer Organization	3	2	25	10	18	100	40	65	-	-	-	25	10	19	-	-	-	
Discrete Time Signal Processing	3	2	25	10	19	100	40	51	-	-	-	25	10	11	-	-	-	
Industrial Economics & Magagement	3	0	-	-	-	100	40	64	-	-	-	-	-	-	-	-	-	
Microwave & Optical Fiber Communication	4	2	25	10	19	100	40	58	-	-	-	25	10	19	-	-	-	
Total	20	12	125	50	91	600	240	325	0	0	0	125	50	85	0	0	0	

TOTAL OF SEMESTER VI: 501 / 850 #

PERCENTAGE = 58.94%

TOTAL OF SEMESTER V & VI = 892/1525

PERCENTAGE = 58.49%

REMARKS: SECOND CLASS

LEGENDS USED : Lec: Lecture, Prac: Practicals, Min: Minimum, Max: Maximum, Obt: Obtained, #: Passed in One attempt, Engg: Engineering, Tuts: Tutorials, *: Grace Marks, P: Passed, \$: Carried Over

FOURTH/FINAL YEAR

ELECTRONICS ENGINEERING

SEMESTER - VII

STUDENT'S NAME: Vipin Vasantkumar Bodele

UNIVERISTY OF MUMBAI SEAT NO: 9453

DATE OF PASSING: December 2008

SUBJECTS	LECTS & TUTS /	PRACS / WEEK	TE	RM W	ORK	Т	HEO	RY	PR	ACTI	CAL		ORA	L		INTERNAL ASSESMENT		
	WEEK	VVLLIX	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	MAX	MIN	OBT	
Basics of VLSI	4	2	25	10	22	100	40	50	-	-	-	25	10	17	-	-	-	
Digital Communication	3	2	25	10	21	100	40	56	-	-	-	25	10	21	-	-	-	
Elective I : (Image Processing)	4	2	25	10		100	40		-	-	-	25	10		-	-	-	
Filter Theory & Applications	4	2	25	10	23	100	40	68	-	-	-	-	-	-	-	-	-	
Instrumentation Systems	3	2	25	10	22	100	40	54	-	-	-	-	-	-	-	-	-	
Project A	4	0	25	10	22	-	-	-	-	-	-	-	-	-	-	-	-	
Total	22	10	150	60	110	500	200	228	0	0	0	75	30	38	0	0	0	

PERCENTAGE = 68.14%

DATE OF PASSING: May 2009

SEMESTER - VIII

UNIVERISTY OF MUMBAI SEAT NO: 9456

TOTAL OF SEMESTER VII: 494 / 725 #

INTERNAL LECTS & **PRACTICAL** PRACS/ TERM WORK **THEORY ORAL SUBJECTS ASSESMENT** TUTS / WEEK WEEK MAX MIN OBT MIN OBT MAX MIN OBT MAX MAX MIN OBT MAX MIN OBT 25 2 25 100 52 Data Communication & Networking 10 23 40 10 23 4 2 25 67 25 19 Elective II: (Robotics) 10 20 100 40 10 Mechatronics 4 2 25 10 20 100 40 57 25 10 22 Power Electronics 10 100 40 25 0 Project B 50 20 49 50 20 48 Total 16 16 150 60 134 400 160 233 0 0 0 150 60 133 0 0 0

TOTAL OF SEMESTER VIII: 500 / 700 #

PERCENTAGE = 71.43%

TOTAL OF SEMESTER VII & VIII = 994/1425

PERCENTAGE = 69.75%

REMARKS: FIRST CLASS WITH DISTINCTION

LEGENDS USED : Lec: Lecture, Prac: Practicals, Min: Minimum, Max: Maximum, Obt: Obtained, #: Passed in One attempt, Engg: Engineering, Tuts: Tutorials, *: Grace Marks, P: Passed, \$: Carried Over