

Name: SHETE KAJOL RAJENDRA MANISHA

Examination: Third Year Engg. (Electronics & Telecomm. Engg.) (Sem VI: CBGS)

Held in: MAY-15 with Seat No: 4862

College Bell No. 19ET2022		Mini Project II	Communication Engg. Lab. IV	Communication Engg Lab III	Discrete Time Signal Processing Lab	VLSI Design	Operating Systems	Television Engineering	Computer Comm. & Telecom Net.	Discrete Time Signal Processing	Digital Communication		Title	Course
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6 of Marks Obtained	Letter	Grade	Grade Performance
0 and above	0	10	Outstanding
5.00 - 79.99	A	9	Excellent
0.00 - 74.99	В	8	Very Good
1	C	7	Good
0.00 - 59.99	0	6	Fair
	m	S	Average
1	7	4	Pass

nt,TH - Theory,TW-Termwork mance Index , P - Practical

SUPTD. EXAM CELL

Remark: Successful

INCHARGE EXAM CELL

PRINCIPAL