**Assembler**

The assembler is based on a 12-bit accumulator structure. It uses the two-pass algorithm to convert the assembly code to object code.

The following are the specifics and assumptions made in the Assembler

1. Only Single Line Comments are supported.
   * These comments should be in separate lines and can either begin by “//” or if they begin by “/\*” then they must end by “\*/”. Comments are not processed by the assembler
2. Instructions need to be stored in a separate text file with the name “input.txt” in the same folder.
   * If the file is not found the Assembler throws an Error.
3. As it is based on a 12-bit Accumulator Architecture it can store a maximum value of 4095 in the accumulator.
4. Word length is taken to be 24-bit (Including the OPCODE Bits). Addresses 0-4095 have been reserved for variables and Literals
5. Addresses are only taken in numbers with base 10.
6. All labels used in the accumulator must start with an Uppercase ‘L’ followed by a number of choice.
7. All variables name must contain only alphabets.
8. Assembler **is** Case – Sensitive and takes into account the number of whitespaces in the instruction. Any deviation from the specified input format would lead to Errors.
9. As soon as an Error is encountered, the Assembler reports the error and exits the execution.
10. If Addresses are entered as Variables, then they need to be Declared.
11. Variable Declarations are done at the end of the Instruction Codes.