1. Where do the MSB 4 bits of the jump target come from?

   (a) The higher order 4 bits of the instruction read
   (b) The lower order 4 bits of the instruction read
   (c) The higher order 4 bits of PC+4
   (d) The lower order 4 bits of PC+4

   Answer: (c)

2. In determining the jump target in the above fashion, are we using sequential components or combinatorial components?

   (a) Sequential
   (b) Combinatorial
   (c) Both
   (d) Neither

   Answer: (d)

3. What will the 2-bit PCSrc control line be a function of?

   (a) Only the opcode from the instruction
   (b) Opcode and funct-code of instruction
   (c) Opcode and immediate value in instruction
   (d) Opcode from instruction and zero from ALU

   Answer: (d)