

Core/src/stm32g0xx\_it.c

```
graph TD; A[Core/src/stm32g0xx_it.c] --> B[main.h]; A --> C[stm32g0xx_it.h]; B --> D[stm32g0xx_hal.h];
```

main.h

stm32g0xx\_it.h

stm32g0xx\_hal.h