Name	DSPs	LUTs		Round-off Error		Clock Period (ns)	Latency (cycles)	Latency (µs)	
			Ratio		Ratio				Ratio
sum	2	303	0.257	9.14E-04	7.93	2.54	4.10E+04	1.04E+02	12.8
	4	1,181		1.15E-04		2.54	3.21E+03	8.17E+00	
dotprod	5	411	0.231	9.26E-04	7.29	2.54	4.10E+04	1.04E+02	12.4
	10	1,781		1.27E-04		2.62	3.23E+03	8.44E+00	
tridiag	5	470	0.288	6.31E-05	1.06	2.54	1.78E+07	4.53E+04	3.42
	8	1,631		5.94E-05		2.69	4.93E+06	1.33E+04	
2mm	5	781	0.385	2.09E+02	3.40	2.79	2.04E+10	5.70E+07	7.46
	8	2,029		6.14E+01		2.92	2.62E+09	7.64E+06	
3mm	5	760	0.207	1.14E+02	6.76	2.55	3.23E+10	8.23E+07	9.13
	10	3,677		1.69E+01		2.82	3.19E+09	9.01E+06	
atax	5	627	0.507	3.53E-01	1.54	2.60	1.76E+08	4.57E+05	5.42
	5	1,237		2.30E-01		2.61	3.24E+07	8.43E+04	
bicg	5	427	0.304	8.87E-04	6.72	2.54	1.60E+08	4.07E+05	9.00
	5	1,406		1.32E-04		2.78	1.63E+07	4.53E+04	
gemm	5	524	0.234	1.99E+00	2.97	2.54	1.08E+10	2.74E+07	9.15
	10	2,240		6.71E-01		2.69	1.12E+09	3.00E+06	
gemver	5	809	0.382	7.28E+06	4.46	2.87	**2.31E+07**	6.62E+04	3.15
	5	2,120		1.63E+06		2.77	**7.60E+06**	2.10E+04	(8.29)
mvt	5	701	0.251	9.10E-05	3.32	2.56	**2.31E+07**	5.91E+04	7.49
	10	2,793		2.74E-05		2.80	**2.82E+06**	7.89E+03	(9.30)
seidel	7	734	0.278	1.07E-05	2.46	2.66	**7.37E+08**	1.96E+06	3.90
	19	2,642		4.35E-06		2.66	**1.89E+08**	5.03E+05	(7.01)
syr2k	5	709	0.259	2.50E-04	4.07	2.89	**1.40E+10**	4.03E+07	6.95
	10	2,740		6.14E-05		2.71	**2.14E+09**	5.80E+06	(7.62)
Geomean			0.289		3.69				6.84 (7.99)