## Operating System Concepts 06 July 2023 08:14 ( Need of 0s ( Medning Os. ( Compile it on flow ( · c -> e me) ( Booking process) ( Process manusment - Unat mean by process - Os Evalution ( )

- OS EVALUATION

- OS E

- (PU Sch Juling algo
- (ou Utilization (mon)
- moughout (mux)
- withing time (min)
- Response time (min)
- Torn amond time (min)
- Torn amond time (min)
- Torn Est

C ST

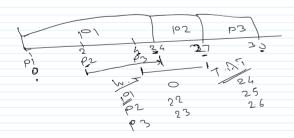
(i) FCFS EPU Schduling (It is non-premptive)



Process	Arrival Time	CPU Burst	Wait Time	Turn Around Time
P1	0	24		24
P2 2	0 -1-	- 3	24	27
P3 L	2 -	- 3		

A.W.T = P1 + 192 + P3 = 24 + 27 + 30 = 0 + 24 + 27 = 0

AT. AT = P+P2 P3



2 5 TF CPU Algo (minimum waiting time)

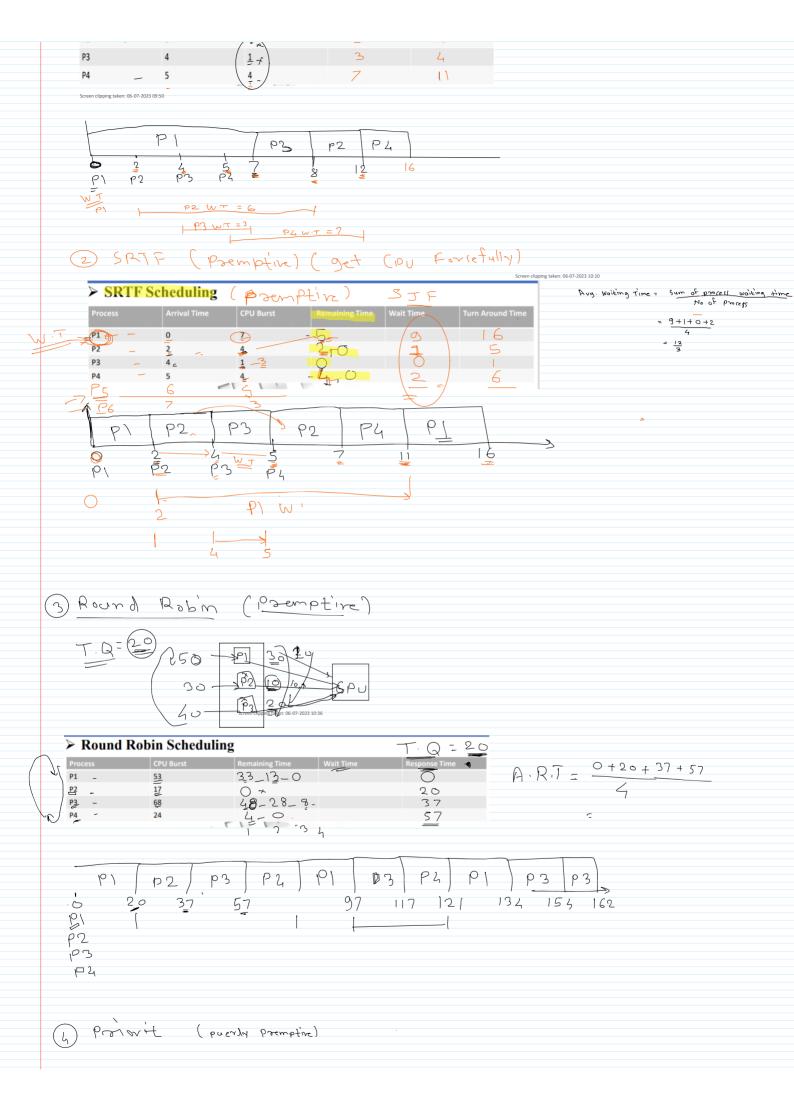
Lo Mon-Premptia (SMTE) (Arrival time diff fuil)

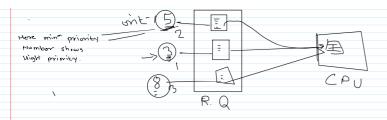
so we desigh SRTE

@ premptive (SRTF)

## ➤ SJF/SNTF scneauing

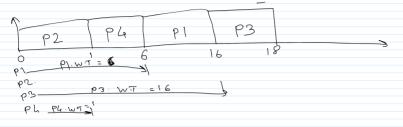
Process	Arrival Time	CPU Burst	Wait Time	Turn Around Time		
P1	0	7 ×	$\circ$	フ		
P2	2	4	6	10		
P3	4	1 7	3	4		
P4	5	4 -	7	( )		



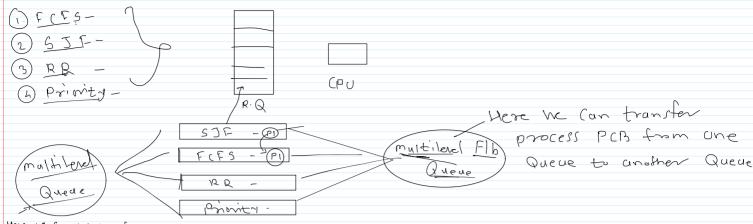


Process	Arrival Time	CPU Burst	Priority	Wait Time
(P1)	0	10	713	6
P2	0	1_	1	0
<u>P3</u>	<del>0</del> 3	2	1 4_	16
P4	0	5	2	1

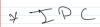
Screen clipping taken: 06-07-2023 10:54

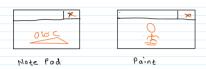




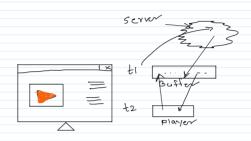


Hene we can not transfer process PCB from one Queae to another Queue



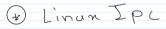


These two processes Hot depend on each othere it known as s Independent Process



Here process pl depends or share data with other process is known as

(0-operative process

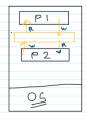


- 1 Shared Memory
- 2 Pipe
- (3) msg Que
- @ Signal
- (5) Sockel

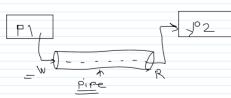




## 1 Shore memory Model







- Named pipe
- Unnamed pipe
- 2) message Queac

