

# Power

Remove this link if 3.3V power is being provided externally

USB-B mini

J3

VBUS

D-

D+

USBID

GND

SHLD

0V

VIN

D1

MBR120

C1

10uF

0V

R7

100K

IC2

AP2112K 3.3V

VIN

OUT

ON/OFF

GND

NC

5

4

3

1

2

0V

LK4

C2

10uF

0V

C3

100nF

0V

C6

100nF

0V

3.3V

D2

MBR120

R5

1K

LD1

LED

IC3

MCP73831

VDD

VBAT

PROG

STAT

GND

4

3

5

2

1

0V

R6

10K

0V

C7

10uF

0V

VBAT

TB1

TB-2

1

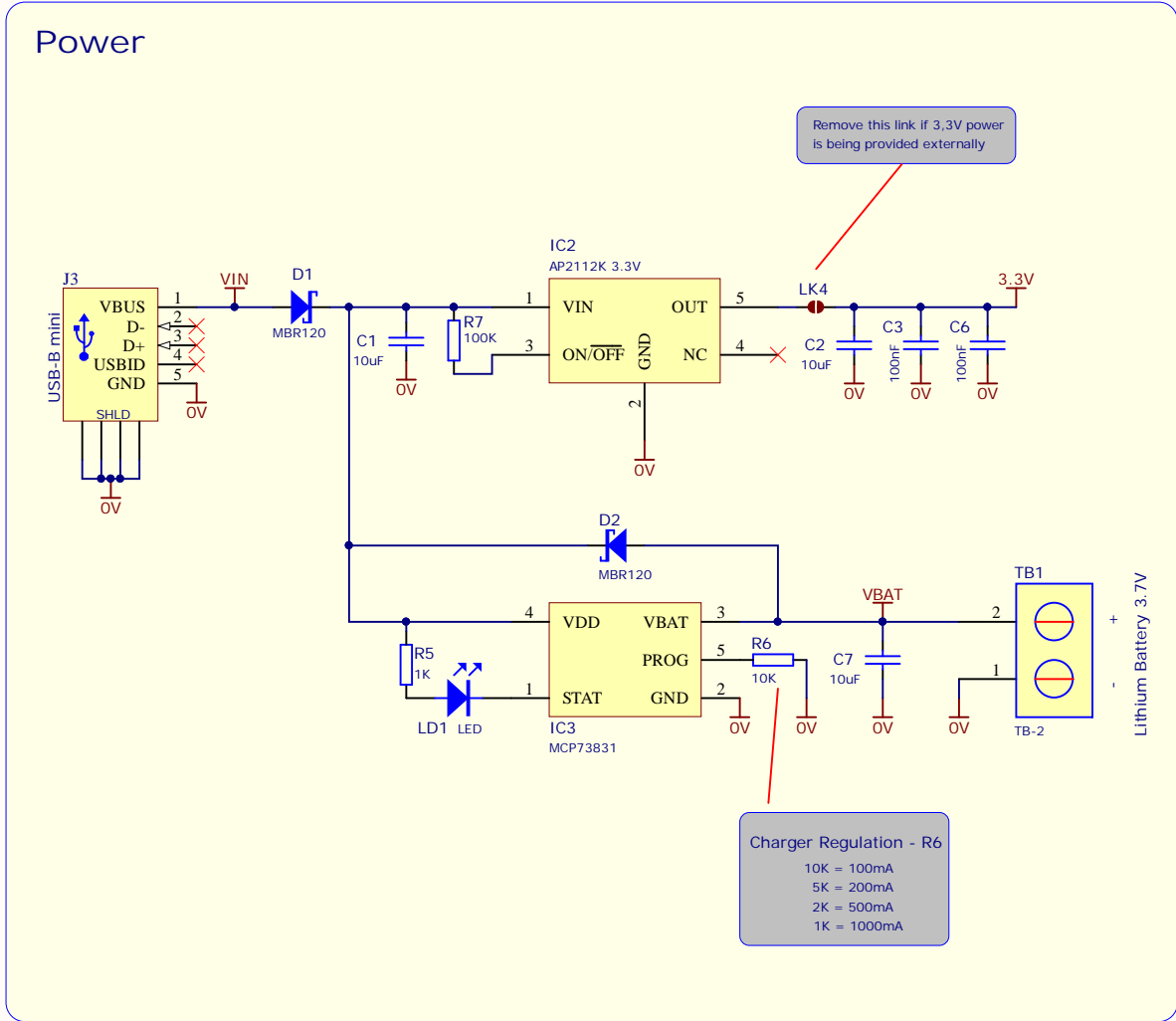
2

0V

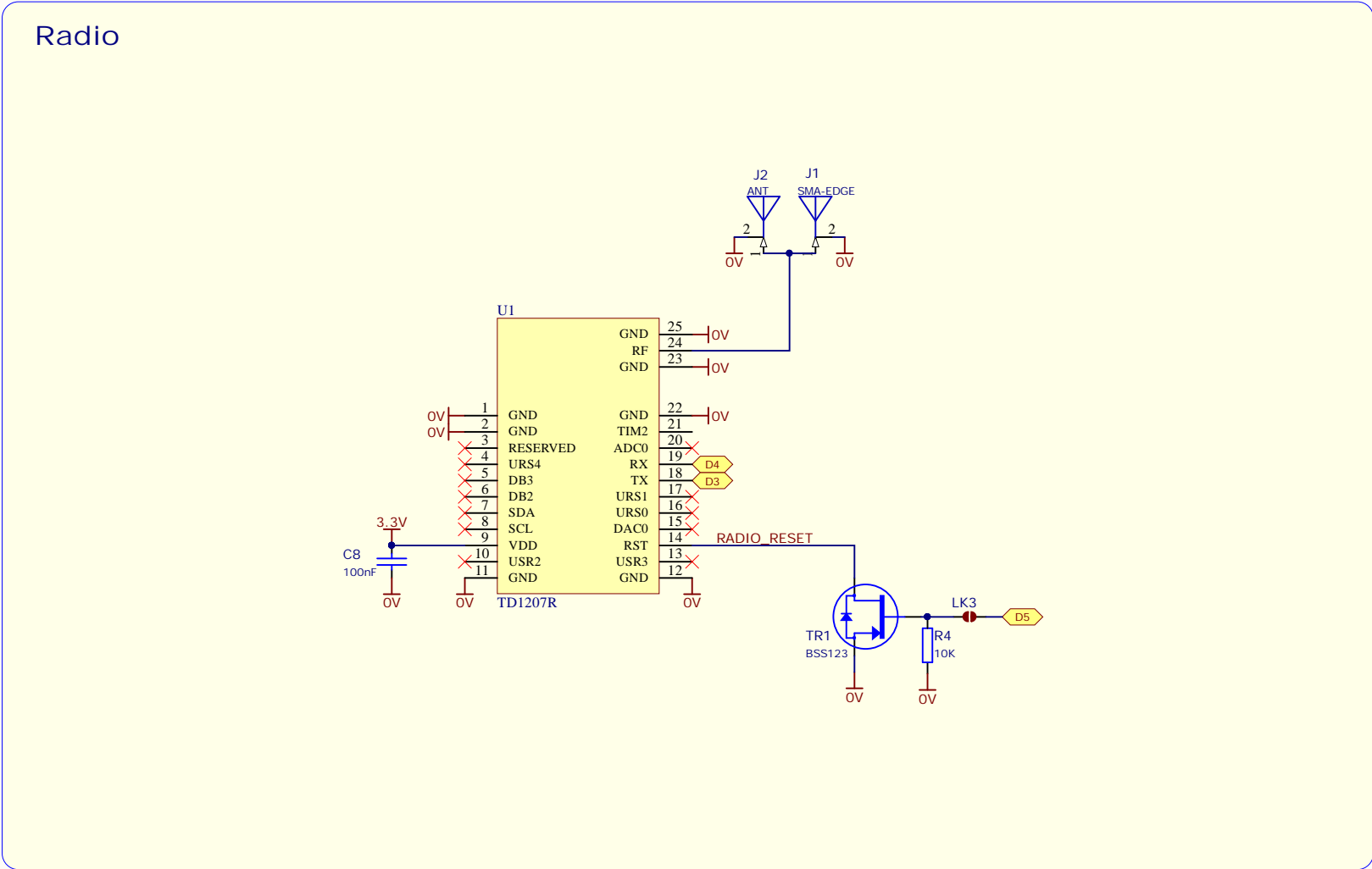
Lithium Battery 3.7V

Charger Regulation - R6

10K	= 100mA
5K	= 200mA
2K	= 500mA
1K	= 1000mA



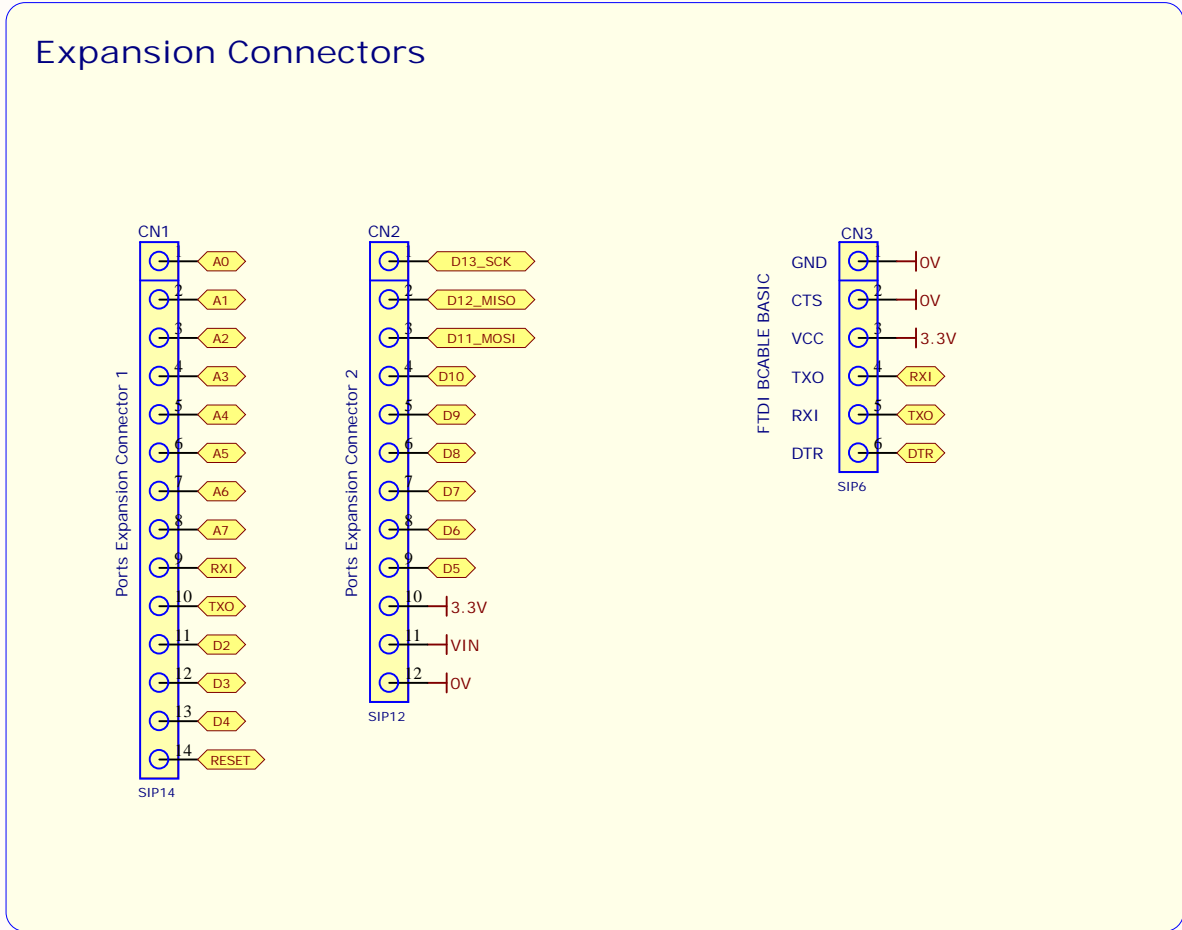
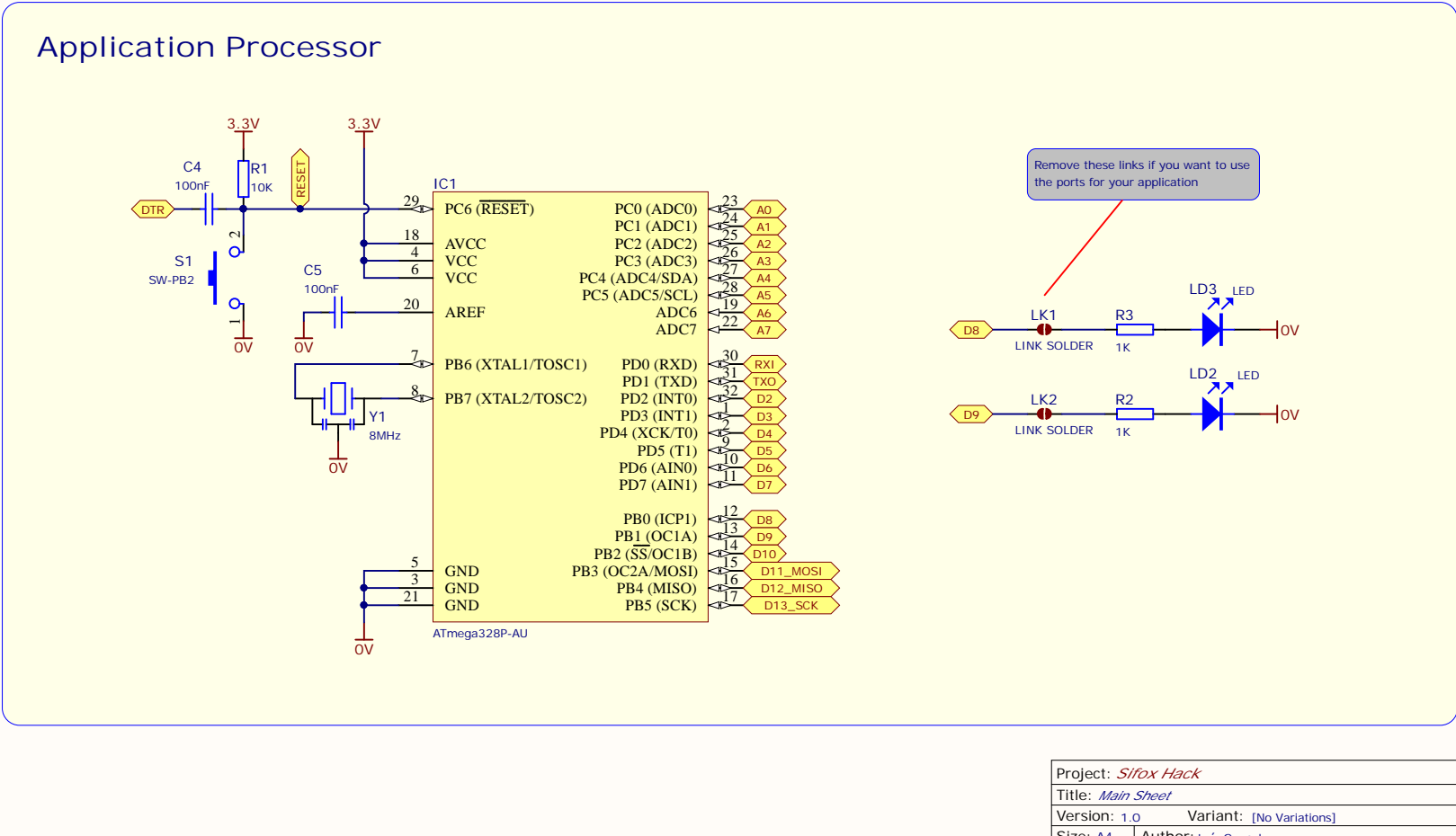
# Radio



# Expansion Connectors

The diagram illustrates three expansion connectors used for interfacing with various components:

- CN1 (SIP14):** A 14-pin connector labeled "Ports Expansion Connector 1". It includes 8 analog pins (A0-A7) and 6 digital/function pins (RXI, TXO, D2, D3, D4, RESET).
- CN2 (SIP12):** A 12-pin connector labeled "Ports Expansion Connector 2". It includes 4 digital pins (D13\_SCK, D12\_MISO, D11\_MOSI, D10), 4 digital pins (D9-D6), and 4 power pins (D5, 3.3V, VIN, 0V).
- CN3 (SIP6):** A 6-pin connector labeled "FTDI BCABLE BASIC". It includes 2 power pins (GND, 0V), 2 signal pins (CTS, 0V), and 2 signal pins (VCC, 3.3V), along with TXO, RXI, and DTR pins.

[illegible]

1

2

3

4

A

A

B

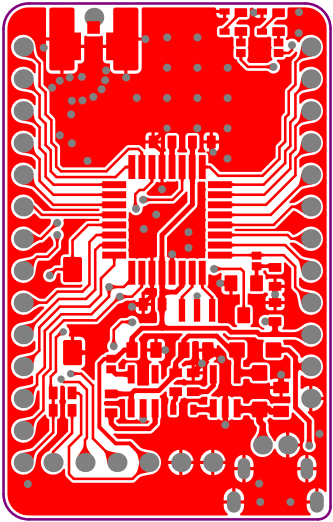
B

C

C

D

D



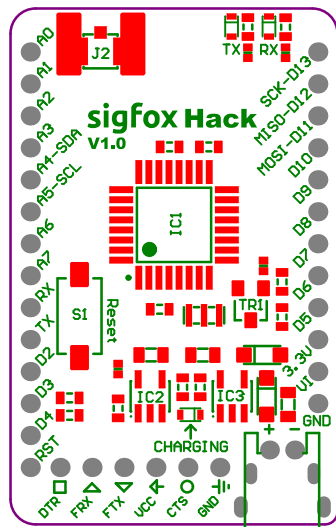
Project:	Sifox Hack
Version:	1.0
Variant:	[No Variations]
PCB Designer:	Luis Goncalves
Date:	2017-02-25
File Name:	hacker_sigfox.PcbDoc

1

2

3

4



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1

2

3

4

A

A

B

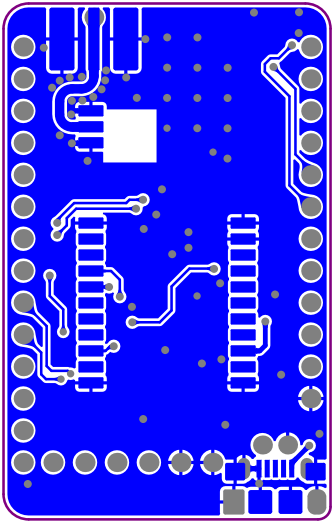
B

C

C

D

D



Project:	Sifox Hack
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Date:	2017-02-25
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1

2

3

4

1

2

3

4

A

A

B

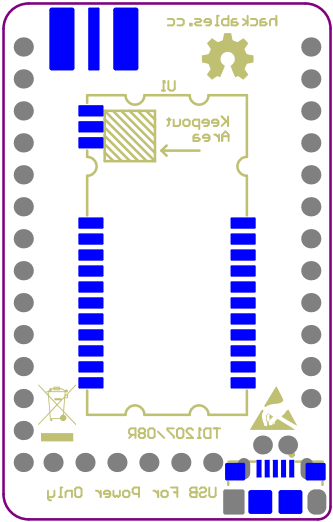
B

C

C

D

D



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1

2

3

4