

how-to-optimize-gemm

<https://github.com/flame/how-to-optimize-gemm/wiki#packing-into-contiguous-memory>

Intel® Intrinsic Guide

<https://www.intel.com/content/www/us/en/docs/intrinsics-guide/index.html>

Avoiding AVX-SSE Transition Penalties

<https://www.intel.com/content/dam/develop/external/us/en/documents/11mc12-avoiding-2bavx-sse-2btransition-2bpenalties-2brh-2bfinal-809104.pdf>

Design and Implementation of a Highly Efficient DGEMM for 64-bit ARMv8 Multi-Core Processors

<https://www.cs.ucy.ac.cy/courses/EPL221/Fall2017Files/Design%20and%20Implementation%20of%20a%20Highly%20Efficient%20DGEMM%20for%2064-bit%20ARMv8%20Multi-Core%20Processors.pdf>

The Science of Programming Matrix Computations

<https://www.cs.utexas.edu/users/rvdg/tmp/TSoPMC.pdf>