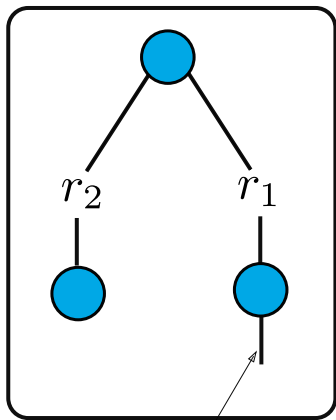
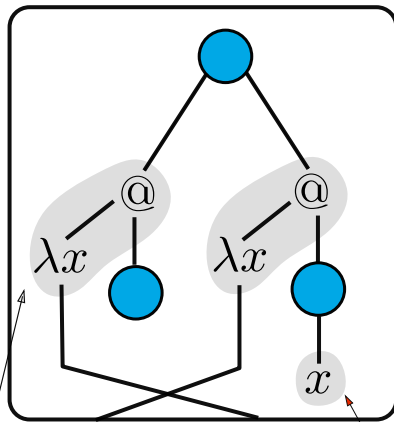


Register update of arity 2



All registers are of arity 1

Its λ -representation



r_2 becomes a redex
and gives port 2

port becomes x