



Building Linux for the ADMXRC7Z1/2

Introduction

This guide describes how to build the Open Source Embedded Linux distribution "Poky" using the Yocto build system, and "Das U-Boot", for an ADM-XRC-7Z1 or ADM-XRC-7Z2.

Building Linux Kernel and Root File System

Required Tools

To follow this procedure you will require a PC or Virtual Machine running a Desktop Linux distribution. The system must be setup with the correct configuration before starting a build. See the The Linux Distribution' and The Build Host Packages' sections in http://www.yoctoproject.org 5: for instructions on setting up the required packages on your Linux Desktop ovstem.

Sources

After setting up a Linux desktop environment for the Yocto build system, **git** can be used to acquire the source code required. Start by creating a new directory for your Embedded Linux project. From inside the new directory use the following commands to acquire a cooles of the required code bases.

```
git clone -b fido git://git.yoctoproject.org/poky.git
git clone -b fido https://github.com/xliin/meta-xliinx.git
git clone -b fido https://github.com/openembedded/openembedded-core.git
git clone -b master https://github.com/adps/meta-adlnx.git
git clone -b master https://github.com/adps/meta-adlnx.git
git clone -b master https://github.com/adps/meta-adlnx.git
```

The last two code bases, meta-adinx and meta-admxrc7z, are Alpha Data's example root files system, and Alpha Data's board support package for the ADM-XRC-7Z1 and ADM-XRC-7Z2.

Building

From the -/poky sub-folder in your Embedded Linux project directory, enter the following command to initialise the build environment:

source oe-init-build-env

This will create a build directory, **build**, which will be switched into to as the active directory after the script completes execution

Build setup

Before a build can be started, two files must be edited in the -/poky/build/conf directory; poky/build/conf/bblayers, and poky/build/conf/local.config

poky/build/conf/bblayers

The bblayers file must be modified to include the full path of additional layers that need to be added to the Yocto



Linux build system. Edit bblayers to be similar to the following, including the meta-adinx and meta-admxrc7z and layers and the openembedded-core/meta layer. Note the path /home/my_home/yocto_linux should be changed to the full path of your Embedded Linux project directory.

poky/build/conf/local.config

The local.config file must be modified to specify the target machine. Edit this file to include the following line to select the target machine:

```
machine ?= admxrc7z
```

Build

Use the following command in the ~/poky/build directory to start a build of the Embedded Linux Kernel and root file system. This will take some time to complete.

bitbake adlnx-image

The output should look similar to the following:

/home/my home/yocto linux/poky/meta-yocto \

```
Build Configuration:
                = "1.26.0"
BB VERSION
BUILD SYS
                 = "x86 64-linux"
NATIVELSBSTRING = "openSUSE-project-13.2"
TARGET SYS
                = "arm-poky-linux-gnueabi"
                = "admyrc7z"
MACHINE
DISTRO
                 = "poky"
                 = "1.8"
DISTRO VERSION
TUNE FEATURES
                 = "arm armv7a vfp neon cortexa9"
TARGET FPU
                 = "vfp-neon"
```

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meta

meta-yocto

= "fido:f366ff2c03885f0ac17415dfbc8f25b2b760b841" = "fido:cd3da9c95f48899e134a5b7ed1754fd18985df4f"

meta = "fido:cd3da9c95f48899e134a5b7ed1754fd18985df4f" meta-xilinx = "fido:276717514962728501de0b69aee0d9052d9a1366"

meta-adlnx = "master:107fff62lea6ef730c9bc6644800a976c66b482" meta-admxr7z = "master:36a452-754726-20-4210c.0140600a976c66b482"

meta-admxrc7z = "master:a36ad52e7fb72fc30e43b9fe0b4259f22c8d4767"

NOTE: Preparing runqueue

NOTE: Executing SetScene Tasks NOTE: Executing RunQueue Tasks



Output files

After the build is completed, several output files are created in poky/build/tmp/deploy/images/admxrc7z/

ulmage-admxrc7z.bin

The Linux Yocto Poky Fido kernel image. This is the core of the operating system, but requires a device tree to provide details of the hardware configuration.

admyrc7z1 dth

Device tree blob. This provides details of the hardware configuration to the kernel.

The root file system as a RAM disk containing a CPIO image prepended with a Das U-Boot header.

Building a standalone toolchain

adlnx-image-admxrc7z.cpio.gz.u-boot

In addition to building the root file system and Kernel a standalone toolchain might be needed for developing application outside the Yocto environment. This can be build with the following command.

bitbake adlnx-image -c populate sdk

This will create the a toolchain installer script poky/build/tmp/deploy/sdk/

poky-glibc-x86 64-adlnx-image-cortexa9-yfp-neon-toolchain-1.8.2.sh. Running this on a Linux system will allow the user to install cross compiler tools customised to target the adlnx reference image.

After running the toolchain installer script, the environment can be setup for cross-compiling by running

source /opt/pokv/1.8.2/environment-setup-cortexa9-vfp-meon-pokv-linux-gnueabi

After this, compiling as normal (e.g. "make clean all") will create executable files for the Zyng.



Emulation with QEMU

To build an image that will run on the QEMU virtual machine name in the local.conf file to **qemuzynq**. After modifying the local.conf file use **bitbake adinx-image** to build a kernel and root file system that will be compatable with the QEMU.

To run the QEMU with the built image execute the following command

runqemu qemuzynq nographic qemuparams="-m 512"



Das U-Boot

To make it easier to develop and update Embedded Linux images, the FSBL (First Stage Boot Loader) will load Das U-Boot. Das U-Boot is a second stage boot loader that allows a number of boot options, including booting from QSPI, SD card, and TFTP.

Sources

Acquire the sources to build Das U-Boot using the following command:

git clone -b master https://github.com/adps/u-boot-ad-zyng7.git

Building Das U-Boot

Execute the following commands inside the u-boot-ad-zvng7 directory:

To set up the system for cross-compiling, source the environment setup script that was generated when building the standalone toolchain

```
source /opt/pokv/1.8.2/environment-setup-cortexa9-vpf-neon-pokv-linux-gnueabi
source /opt/Xilinx/SDK/<SDK VERSION>/settings64.sh
export BUILD DIR=build
make admxrc7zl config
make all
```

make LDFLAGS="" CC="\$CC"

Output files

After building has completed, the following files can be found in the build directory:

Note: Running "make all" on some systems does not work, and instead you should run:

u-boot elf This file is an ELF version of the built U-Boot binary.

This file is a bootable image containing both a basic FSBL for the ADM-XRC-7Z1 and the U-Boot BOOT.bin binary.



Testing

The default environment variables in Das U-Boot will attempt to boot the admxrc7z1.bin kernel image with the adlnx-image-admxrc7z.cpio.gz.u-boot root file system and admxrc7z.dtb device tree.

Preparing an SDCard

To test the built Das U-Boot and Embedded Linux image, follow this procedure:

- Prepare a micro SD card with a FAT32 primary active partition as the first partition.
- Copy the following files into FAT32 partition of the micro SD card:
- admxrc7z.bin, adlnx-image-admxrc7z.cpio.gz.u-boot, admxrc7z.dtb, BOOT.bin Configure the ADM-XRC-7Z1 for boot from uSD, by setting SW2-2 to On and SW2-1 to off.
- With the ADM-XRC-7Z1 powered down, install the uSD card in the uSD card slot.
- Depending on the carrier and break out board being used, connect a serial cable to the S0 serial output
- from the ADM-XRC-771. See the User Manual for the ADM-XRC-771 for details of switches and connectors After powering on the ADM-XRC-7Z1 it should boot from the uSD card, first showing information about the ADM-XRC-7Z1 in the FSBL, before proceeding to load and execute U-Boot. U-Boot will wait for three

seconds for a user to intervene before starting to load Embedded Linux. After Linux has booted, the user name root can be used to log into the system (without a password).



Typical Boot Output

Brdman Ver : 1.1.1.0

```
999999999999999
    9999 999999999999
        99999 99999
00 00000000 00 00000
00000 000000000 0
                    0.0
                          888 8 8
                                          0000
99 99 999999 99
                 8889
                         88 8 8888 8 88
                                          88 88 88
                                                    9.9
                                                         0 0
         8888
                88888 88
                         000 0000 0000
                                          00 00 00000 00
99999999999
                99 99 99 99 99 99 99
                                          99 99 99 9 99 99
   999999
```

Alpha Data Parallel Systems Ltd. Zyng7 FSBL Release 1.1.0.0

Board is in application mode. System monitor reporting:

```
Board Type : ADM-XRC-721
                              Serial Number : 102
        P6 : Fitted
                                         P4 : Fitted
 Micro-USB : Fitted
                                    Micro-SD : Fitted
  Proq Clk : 150000000Hz
                               Tqt Stepping :
Speed Grade : 1
                                  Tgt Device : 72045
Temp Grade : Industrial
                                         PCB : 1.0
      CPLD : 1.0
                                    PS SDRAM : 512MiB@800.0MHz
     OSPIO : 32MiB
                                       OSPI1 : 32MiB
 PL SDRAMO : 256MiB@800.0MHz
                                   PL SDRAM1 : 256MiB@800.0MHz
      VPWR : 11.837V (OK)
                                   12V0 DIG : 11.816V (OK)
   5V0 DIG : 5.028V (OK)
                                    3V3 DIG : 3.264V (OK)
   2V5 DIG :
             2.494V (OK)
                                 AUX IO 2V0 :
                                               1.980V (OK)
   1V8 DIG :
              1.790V (OK)
                                 MGT_AUX_1V8 :
                                               1.819V (OK)
  VCC DRAM :
              1.497V (OK)
                                     XRM VIO :
                                               0.000V (OK)
             0.994V (OK)
   1V0 DIG :
                                     TGT 1V2 :
                                               1.196V (OK)
   TGT 1V0 :
              0.990V (OK)
                                 3V3 DIG IC : 3.260V (OK)
  AVR Temp : 32.350C (OK)
                                   PCB Temp : 32.725C (OK)
Target Temp : 31.475C (OK)
                                 Alarm State : OK
  PSU OK 0 : TRUE
                                    MezPres0 · FALSE
 FPGADone0 : TRUE
                                Force 2V5 0 : FALSE
 PCIe Reset : TRUE
                                XRM Present : FALSE
XRM VIO En : FALSE
                                 XRM I2C Er : FALSE
XRM Chks Er : FALSE
                                 XRM Data Er : FALSE
 Force 2V5 : FALSE
                                 SI5338 OK : TRUE
Introd OK : FALSE
                                    Op 1 OK : FALSE
        EC : 811
                                         ETC : 19d:7h:31m:0s
```

Booting from SD card ...

The last line of the above output represents the point at which Alpha Data's FSBL hands over control to Das U-Boot. Note: There may also be some additional debug output from the FSBL, if it is compiled for debug. Following the FSBL output is output from Das U-Boot, which takes the following form:



```
U-Boot 2014.01-g67f6167-dirty (Aug 11 2014 - 15:28:55)
I2C: ready
Memory: ECC disabled
DRAM: 512 MiB
     zyng sdhci: 0
SF: Detected N250256A with page size 256 Bytes, erase size 4 KiB, total 64 MiB
      serial
Out: serial
Pres.
      serial
     Gem.e000b000
Net:
Hit any key to stop autoboot: 0
Device: zyng sdhci
Manufacturer ID: 2
OPM: 5444
Name: SA08G
Tran Speed: 50000000
Rd Block Len: 512
SD version 3 0
High Capacity: Yes
Capacity: 7.3 GiB
Bus Width: 4-bit
reading uEnv.txt
** Unable to read file uEnv.txt **
Copying Linux from SD to RAM...
reading uImage-admxrc7zl.bin
3578392 bytes read in 324 ms (10.5 MiB/s)
reading uImage-admxrc7z1.dtb
23630 bytes read in 20 ms (1.1 MiB/s)
reading core-image-minimal-admxrc7zl.cpio.gz.u-boot
2419107 bytes read in 224 ms (10.3 MiB/s)
## Booting kernel from Legacy Image at 03000000 ...
   Image Name: Linux-3.14.2-xilinx
   Image Type: ARM Linux Kernel Image (uncompressed)
                3578328 Bytes = 3.4 MiB
   Data Size:
   Load Address: 00008000
   Entry Point: 00008000
   Verifying Checksum ... OK
## Loading init Ramdisk from Legacy Image at 02000000 ...
   Image Name: core-image-minimal-admxrc7z1-201
   Image Type: ARM Linux RAMDisk Image (gzip compressed)
   Data Size:
                2419043 Bytes = 2.3 MiB
   Load Address: 00000000
   Verifying Checksum ... OK
## Flattened Device Tree blob at 02a00000
   Booting using the fdt blob at 0x2a00000
   Loading Kernel Image ... OK
   Loading Ramdisk to 1f8e1000, end 1fb2f963 ... OK
   Loading Device Tree to 1f8d8000, end 1f8e0c4d ... OK
Starting kernel ...
Uncompressing Linux... done, booting the kernel.
```

- 0.0000001 Booting Linux on physical CPU 0x0 0.000000] Linux version 3.14.2-xilinx (dd@dd4-linux) (qcc version 4.8.2 (GCC)) #1 SMP PREEMPT Wed Aug 6 15:45:04 BST 2014
- 0.000000] CPU: ARMv7 Processor [413fc090] revision 0 (ARMv7), cr=18c5387d



```
0.000000] CPU: PIPT / VIPT nonaliasing data cache, VIPT aliasing instruction cache 0.000000] Machine model: Xilinx Zynq
```

The above output ends with Das U-Boot handing over execution to the Embedded Linux kernel. As execution continues various debug information will be printed reporting the status of drivers being loaded and the operating system being confloured.

The boot stage of Embedded Linux will end logging similar output to that shown below.

```
Mon Aug 4 16:01:00 DTC 2014
IMIT: Entering runievel: 5
Configuring network interfaces... udhcpc (v1.22.1) started
Sending discover...
Sending discover...
Sending discover...
Sending discover...
Sending discover...
(1.125361) anch e000b000.ps7-ethernet eth0: link up (1000/Full)
Sending select for 192.169.2.70...
Sending select for 192.169.2.70...
Lease of 192.168.2.70 obtained, lease time 691200
/act/udhcpc.d/Sodefault: Adding DNS 192.168.2.15
/act/udhcpc.d/Sodefault: Adding DNS 192.168.2.8
done.
Stopping Bootlog daeson: bootlogd.
Poky (Yotto Project Reference Distro) 1.6.1 admxrc7z1 /dev/tty880
admxrc7z1 login:
```

Extending and developing

Alpha Data's board support meta layer (meta-ad-zynq7) is only a basic example. If your FPGA design requires custom drivers and modifications to the device tree, the meta layer can be modified to include these.



Revision History

Revision	Date	Description of Change
1.0	11th August 2014	Initial draft
2.0	14th May 2015	Updated for using Fido Poky release.
2.1	23th Mar 2018	Minor corrections to file names.

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