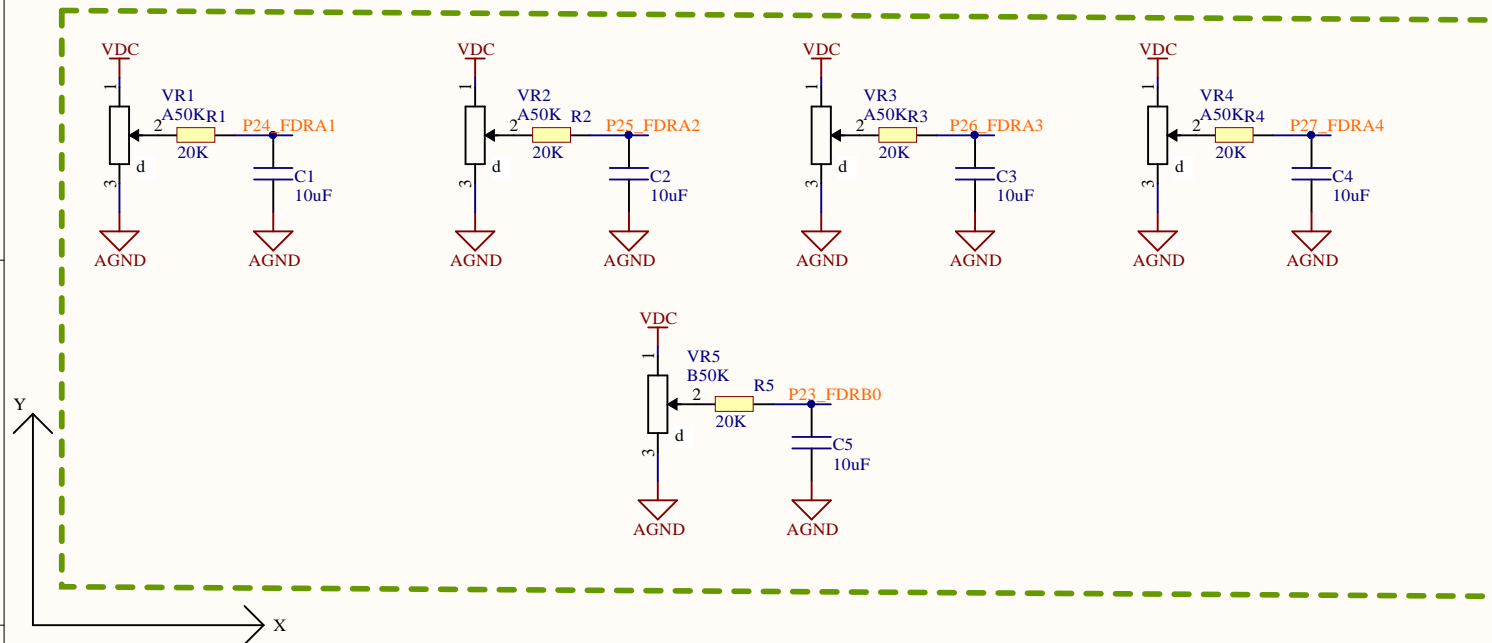


1

2

3

4



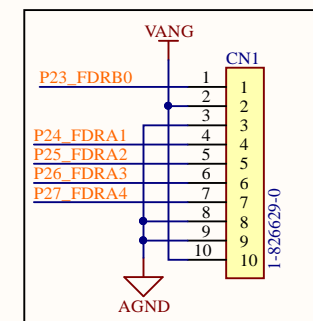
### FADERS

P23\_FDRB0  
P24\_FDRA1  
P25\_FDRA2  
P26\_FDRA3  
P27\_FDRA4

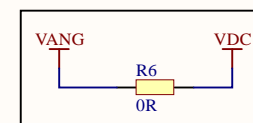
XFADER  
 CHANNEL1  
 CHANNEL2  
 CHANNEL3  
 CHANNEL4

### Faders

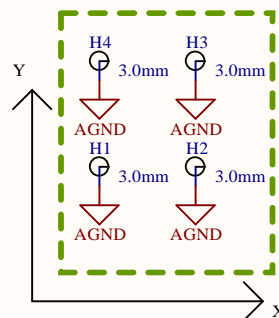
Fader



Board connection



Current Limit, Debugging and Splitting supply rails.



Title **Faders board**

Size: **A4**

Number: **2**

Revision: **\***

Date: **17/01/2023** Time: **21:12:46**

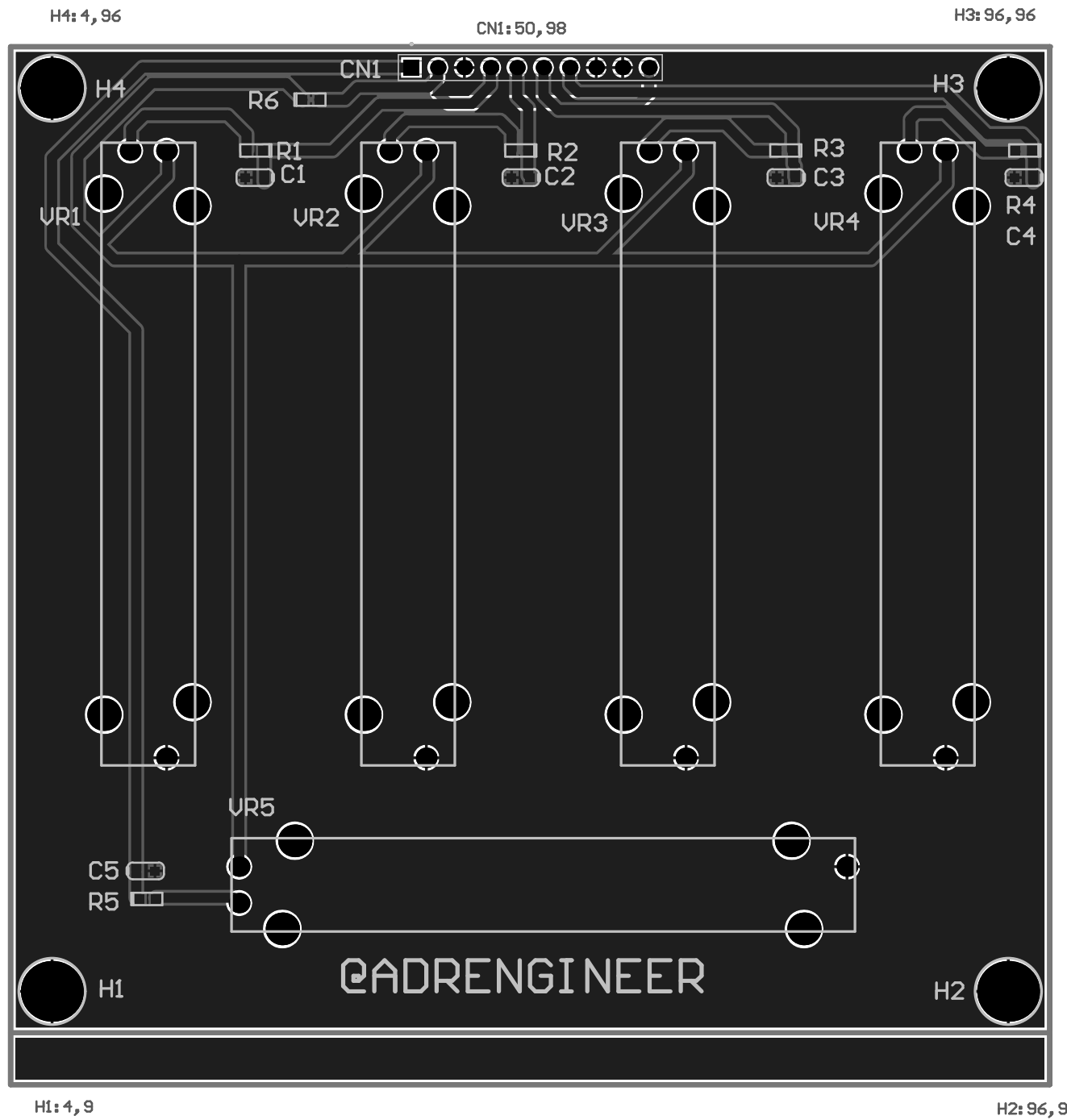
Sheet **2** of **6**

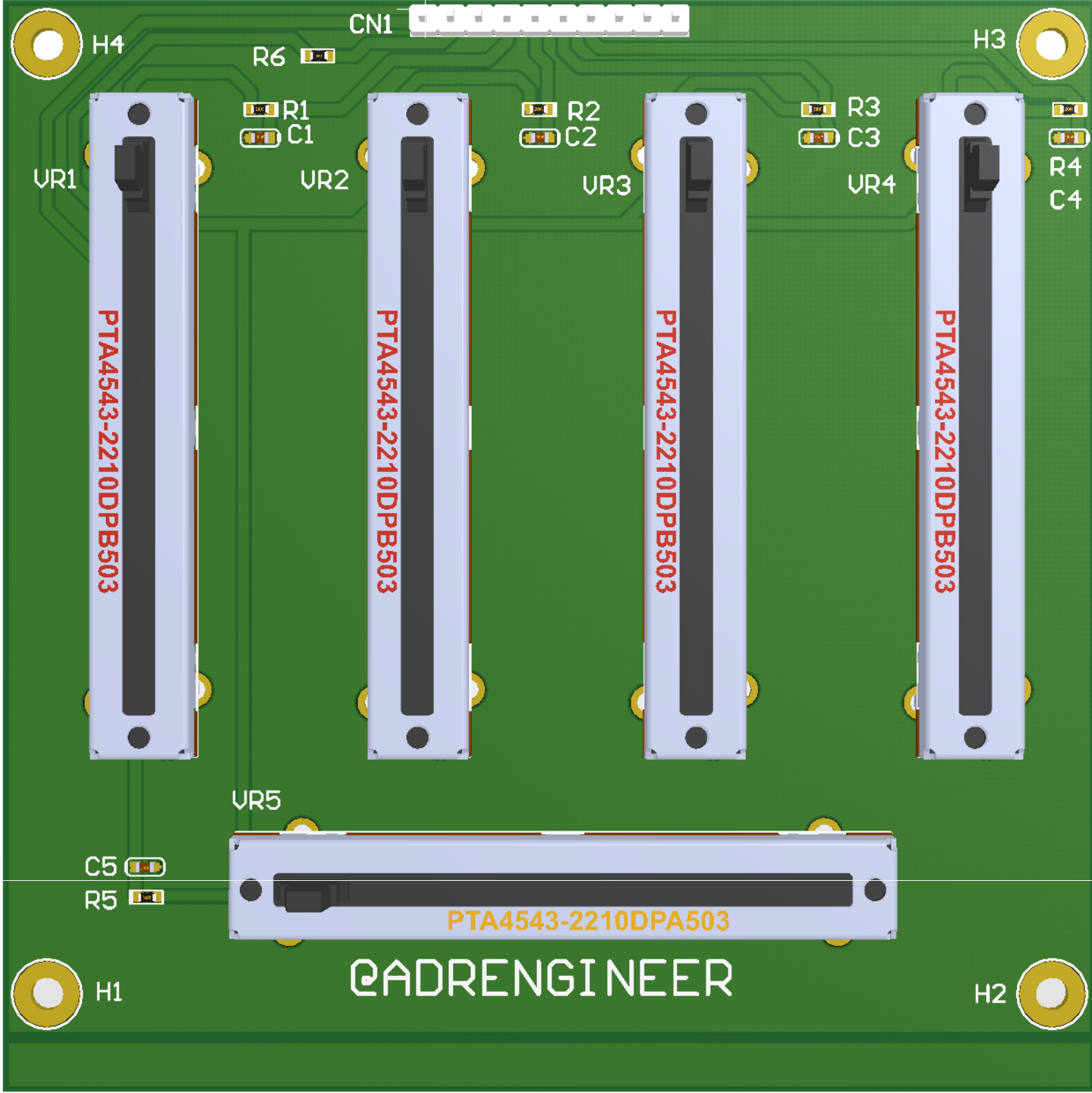
File: **LinearFaders.SchDoc**



# UPSTREAM

## Potentiometers Board





H4

CN1



H3

R6

R1  
C1

VR2

R2  
C2

VR3

R3  
C3

VR4

R4  
C4

VR1

PTA4543-2210DPB503

PTA4543-2210DPB503

PTA4543-2210DPB503

PTA4543-2210DPB503

VR5

C5  
R5

PTA4543-2210DPA503



H1

QADRENGINEER



H2

