

Bitsy Expander RP2040 Pin Map

PWM Slices and Channels



PWM Slice

Pin Name

Shared Pin
(see note 1)

CHANNEL A

CHANNEL B

I²C

5

D13~

4

D10~

3

D7~

5

D13~

2

D4

3

D7~

2

D3~

2

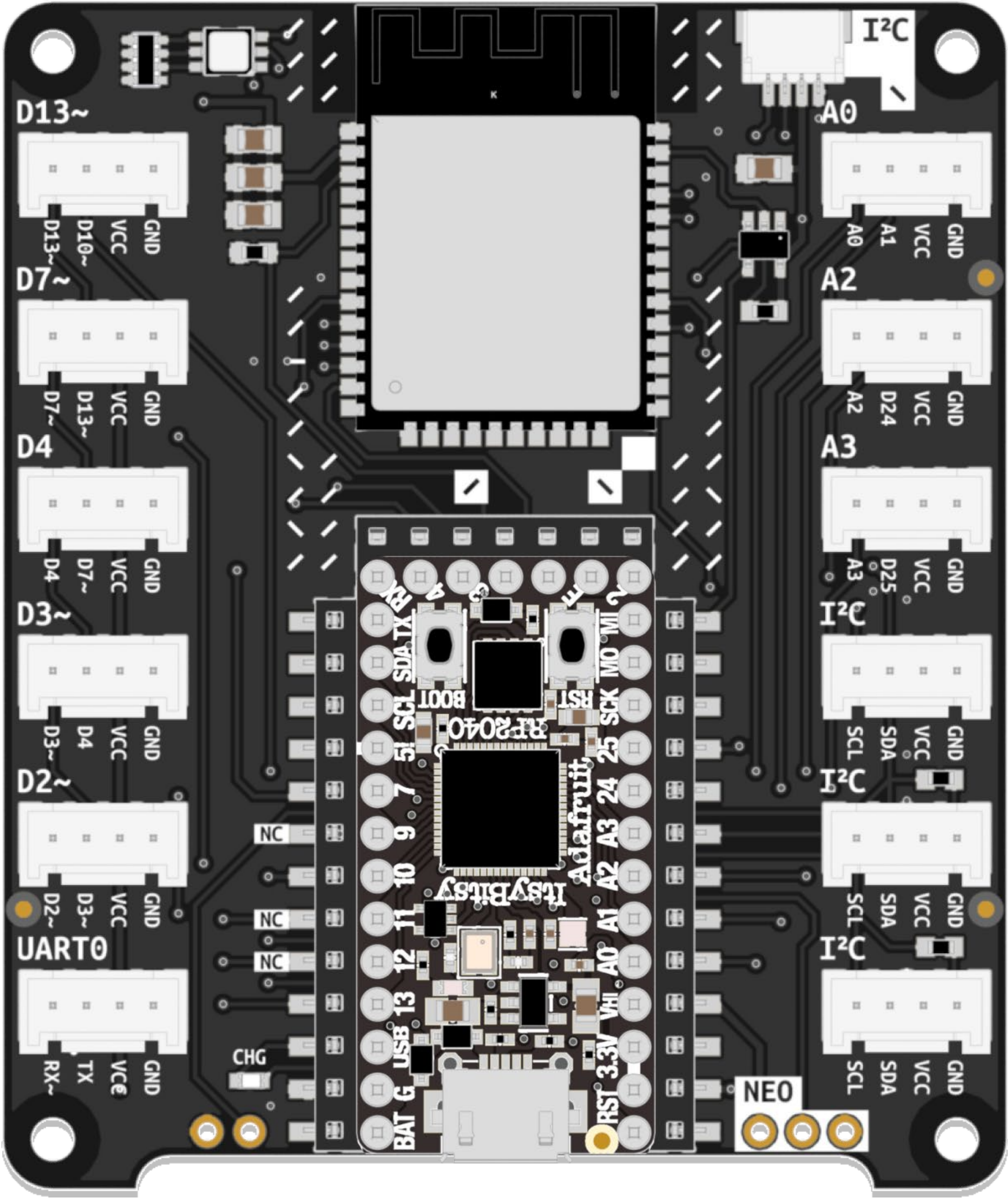
D4

6

D2~

2

D3~



5

A0

5

A1

6

A2

4

D24

6

A3

4

D25

I²C

I²C

I²C

1

SCL

1

SDA

7

D5~

Notes:

- (1) In overlapping (shingled) headers, inner pins are marked grey since they also serve as primary pins in the header above. Usage in one header negates their availability in the other.
- (2) Pins marked with a ~ in this diagram may be used without restrictions to generate PWM signals. The ~ is not part of the pin name and must be omitted in code.
- (3) All pins can generate PWM signals. For simultaneous use of pins that share a common PWM slice, identical frequency settings must be used (duty cycles can be controlled independently). Each of the 8 PWM slices has two channels (A, B). Some CircuitPython libraries (e.g., countio) rely on edge detection or frequency/duty cycle measurements only supported on pins on Channel B. In this case, no other pin on the same slice may be used for PWM.