

Lucrarea 3

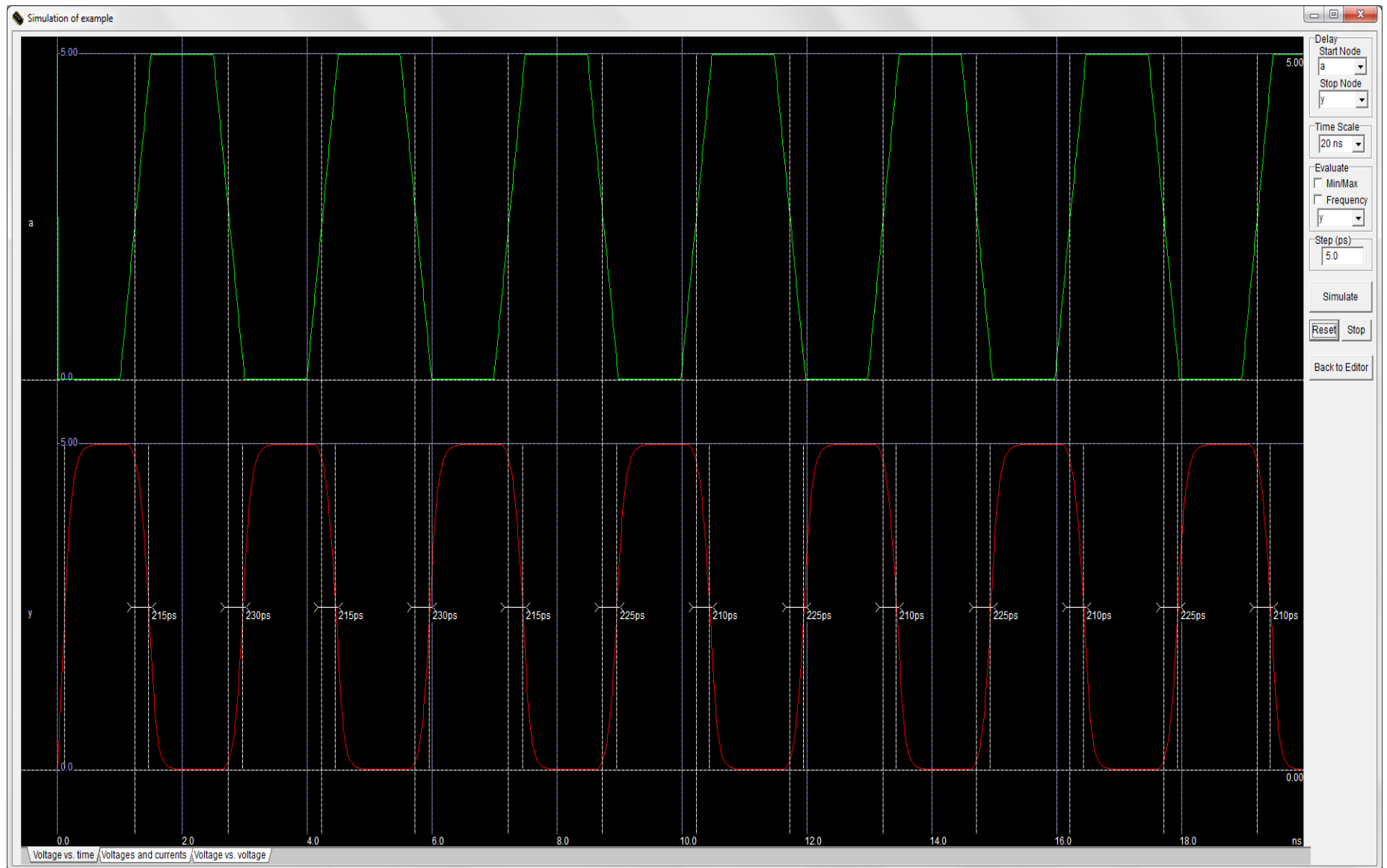
Simulare în Microwind

Nicolau Adrian

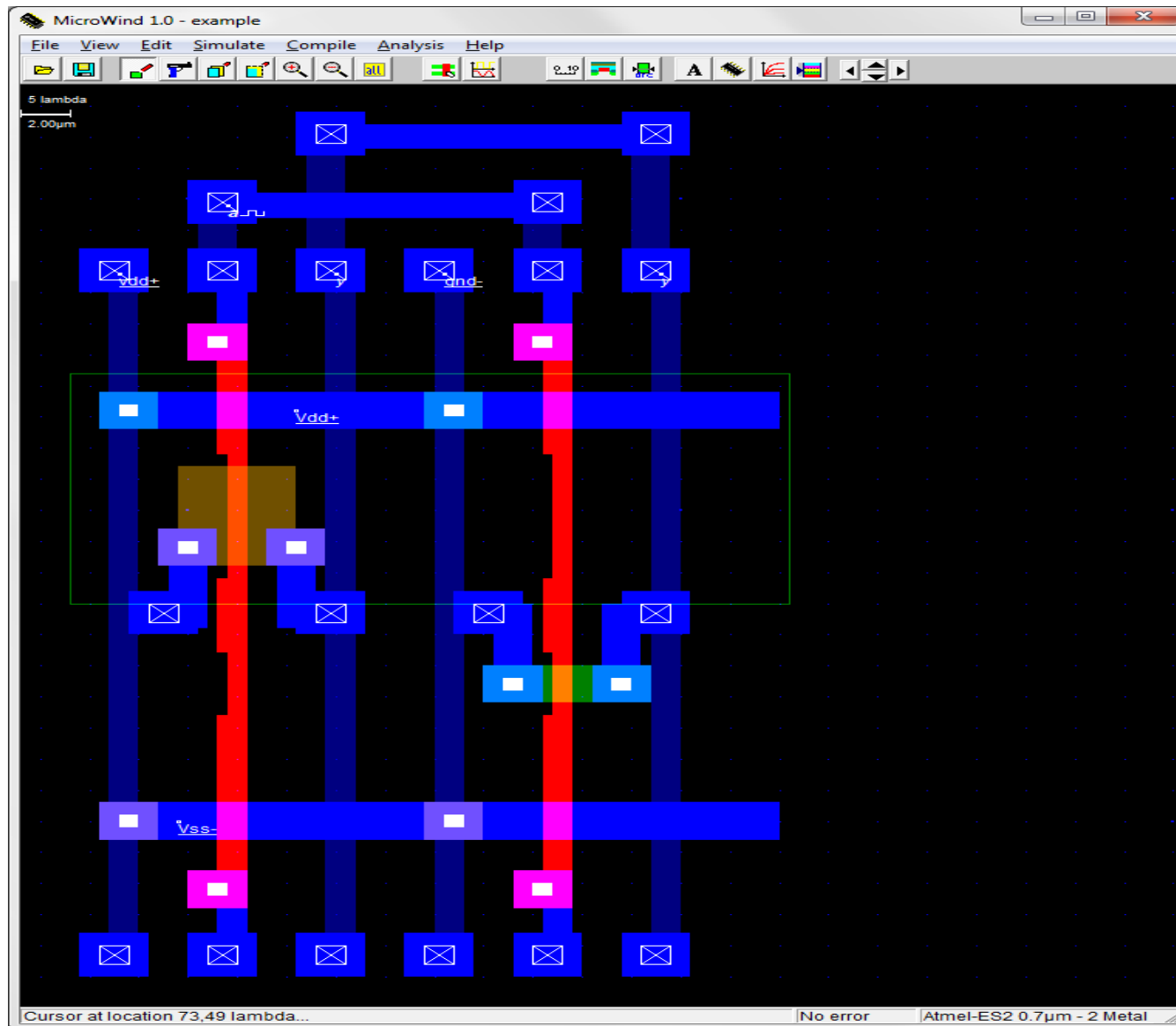
Nistor Valeri

342C1

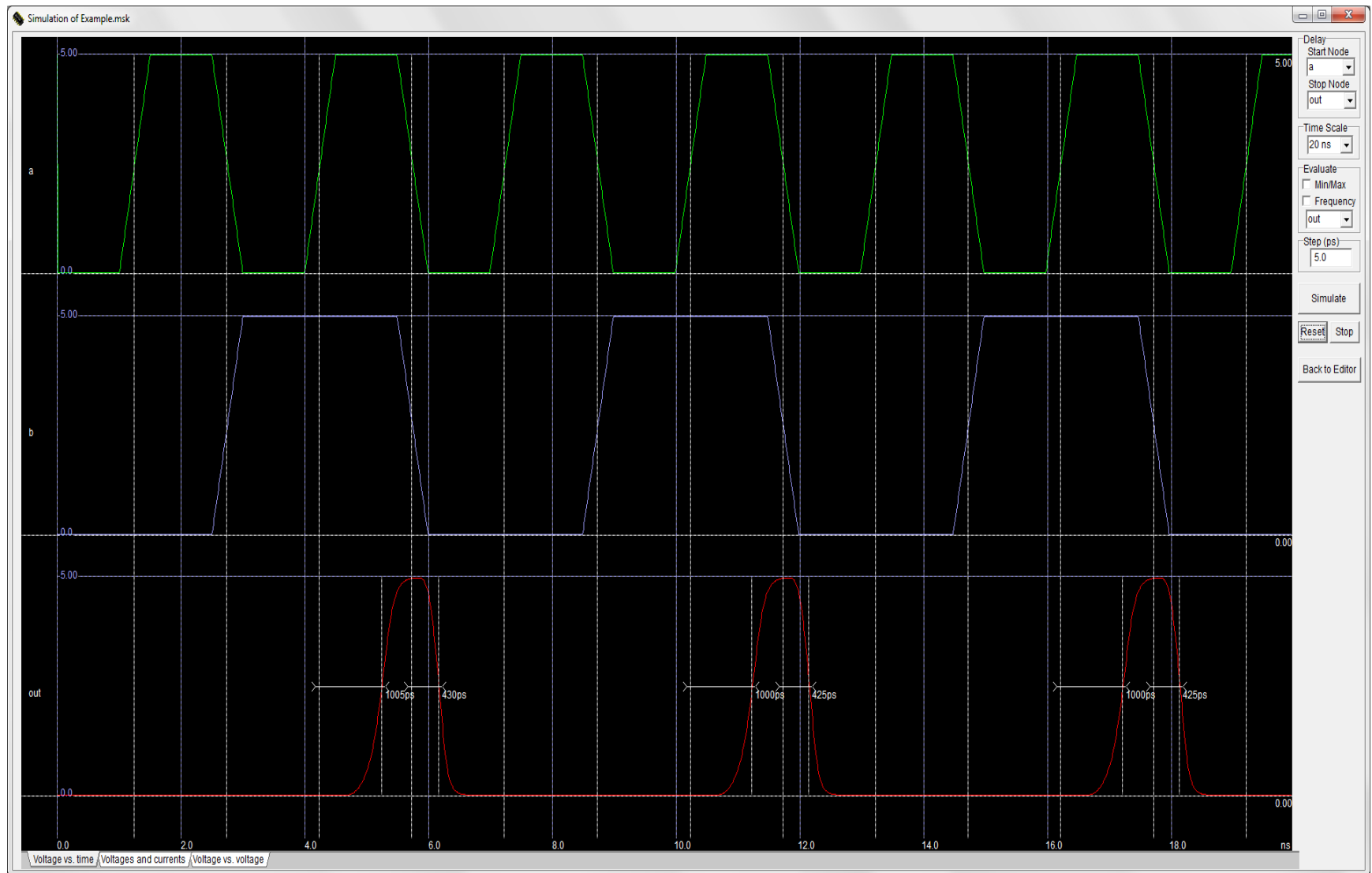
Inversor - simulare



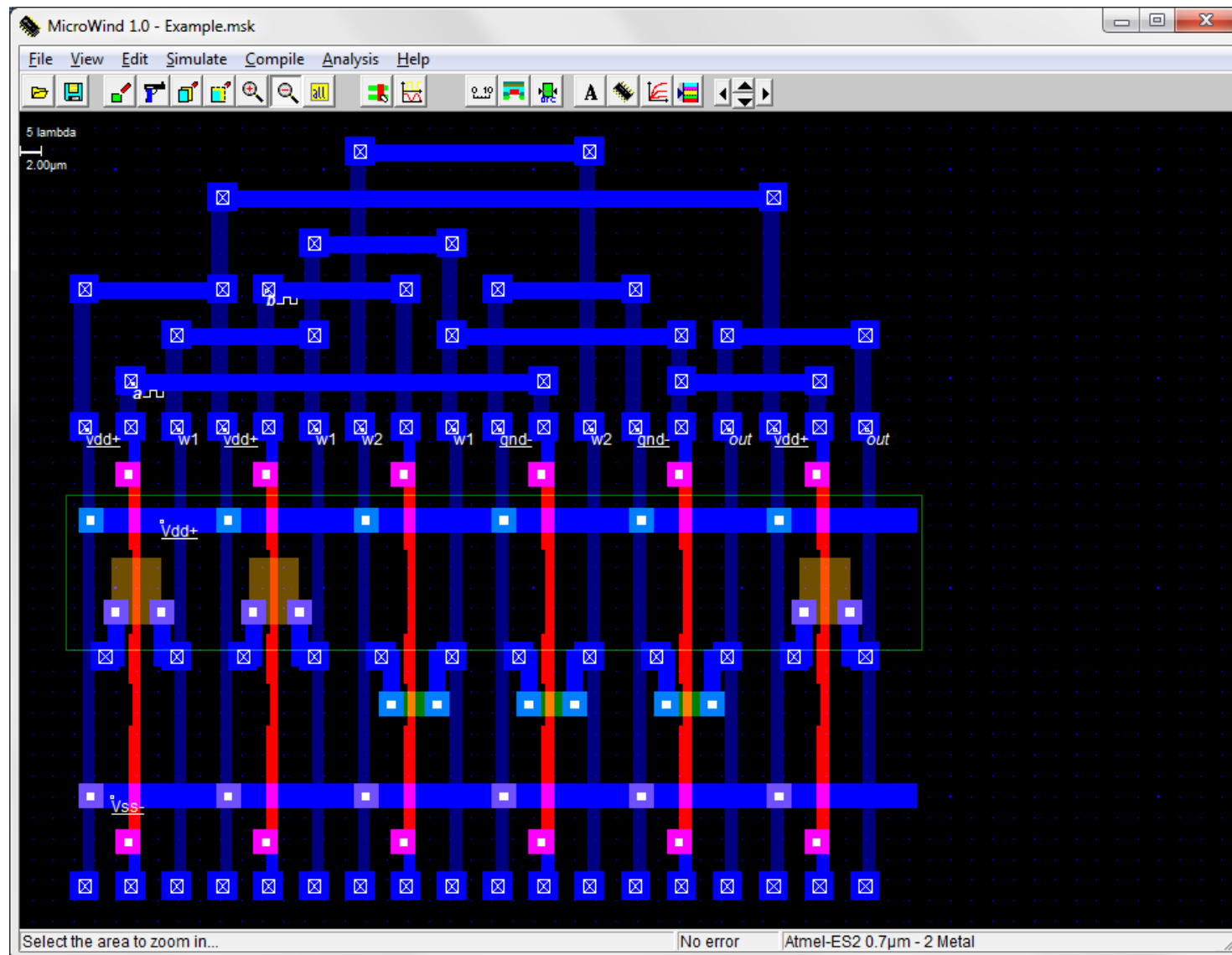
Inversor - design



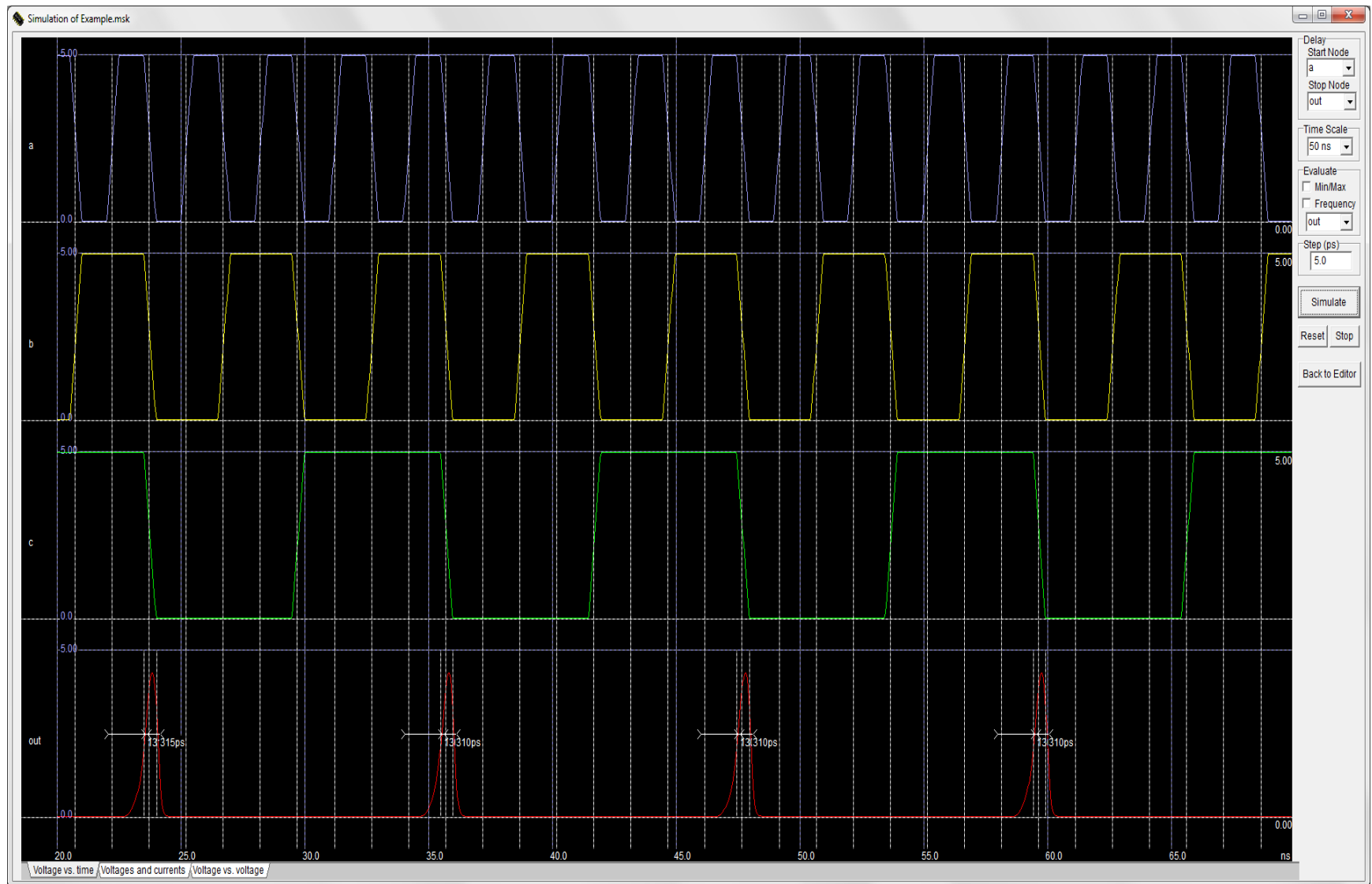
AND2 - simulare



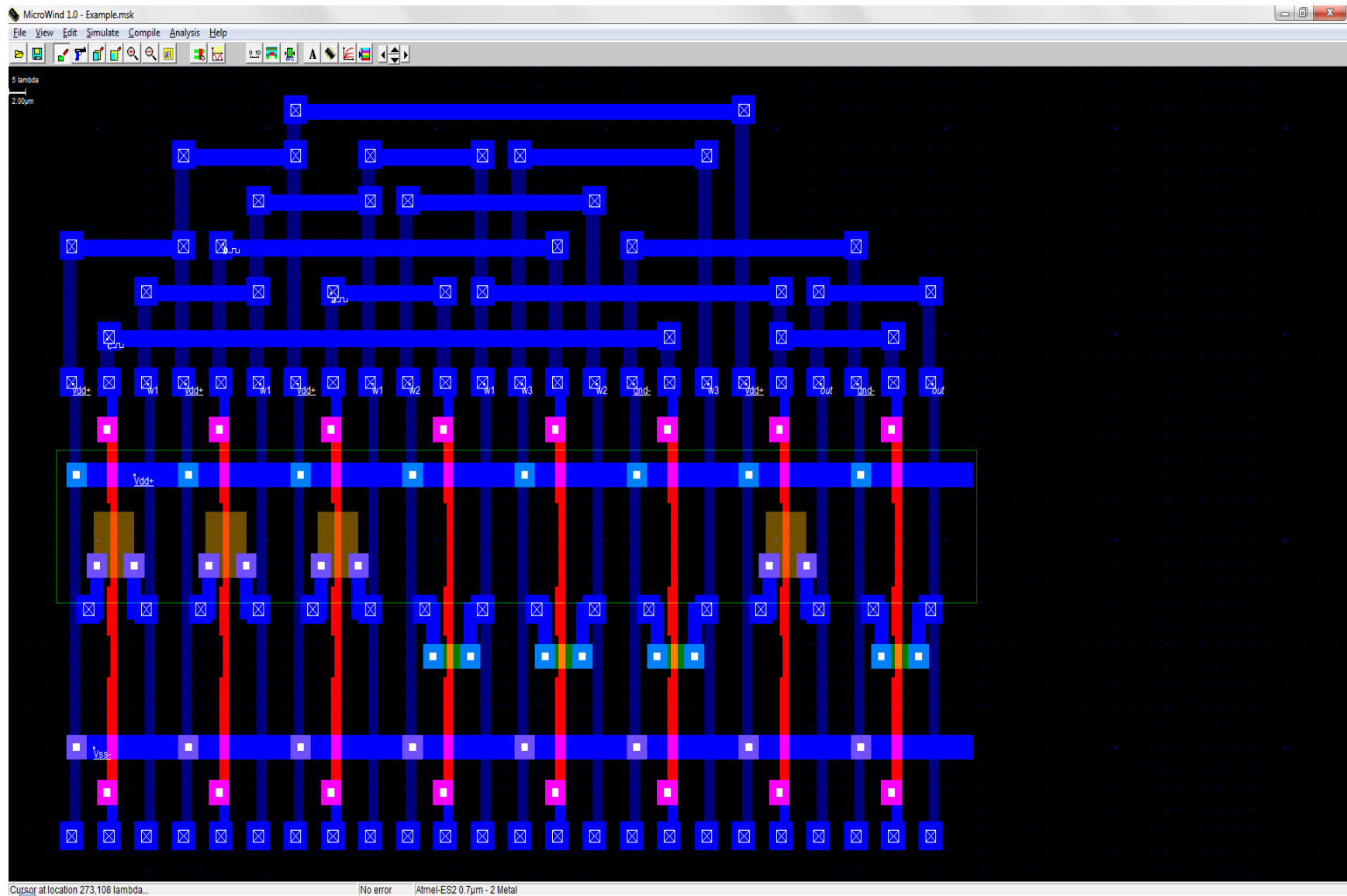
AND2 - design



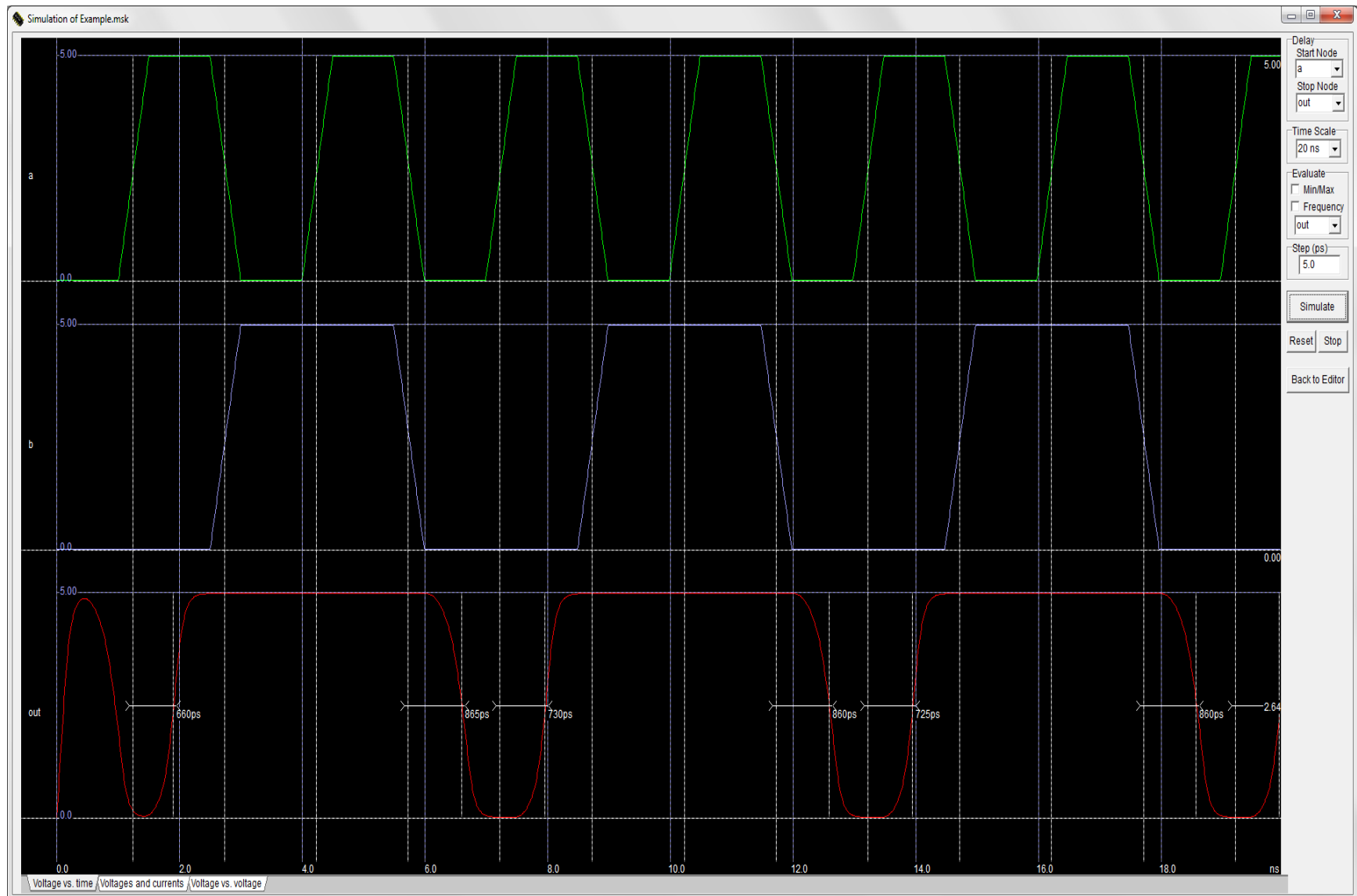
AND3 - simulare



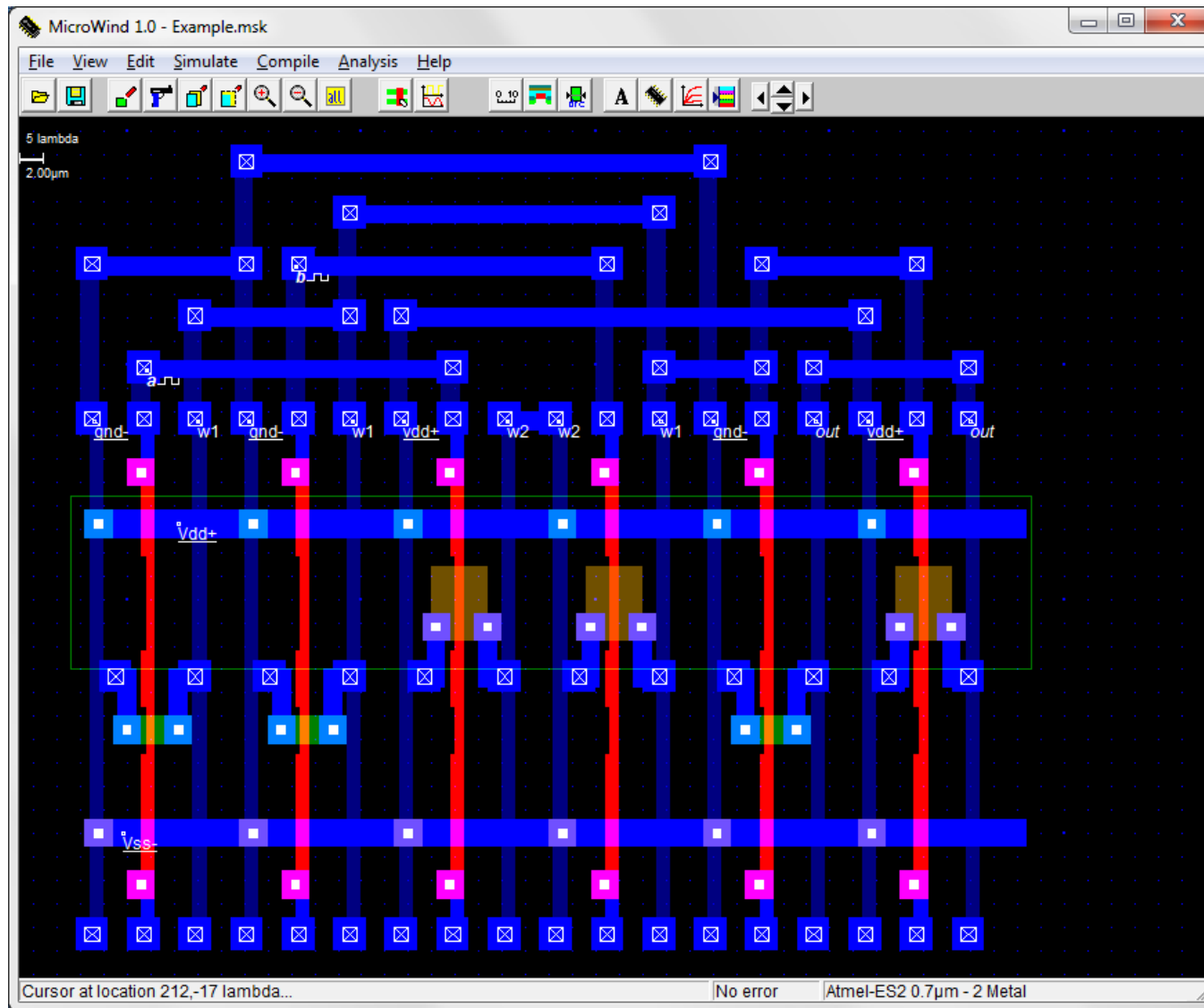
AND3 - design



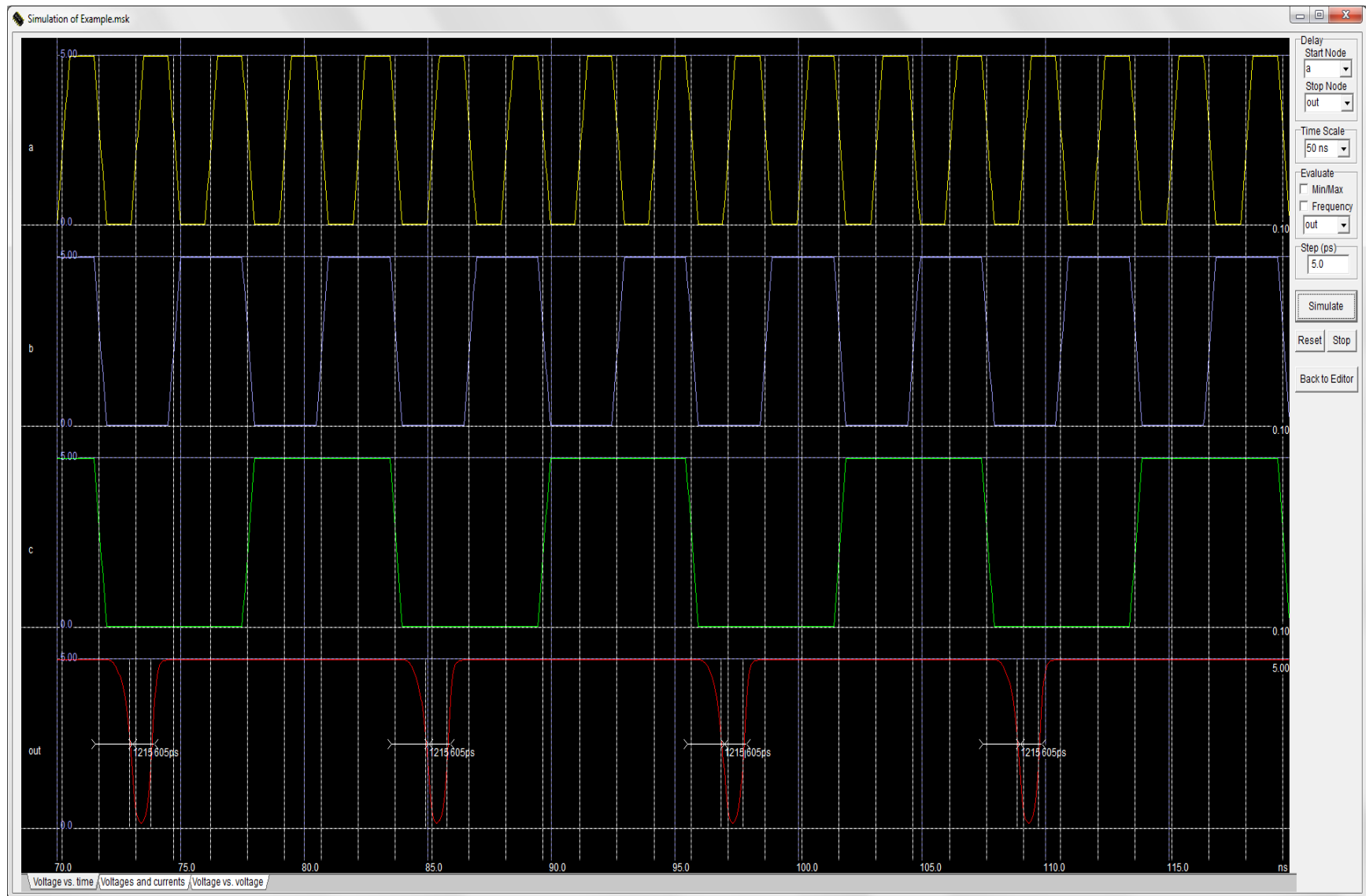
OR2 - simulare



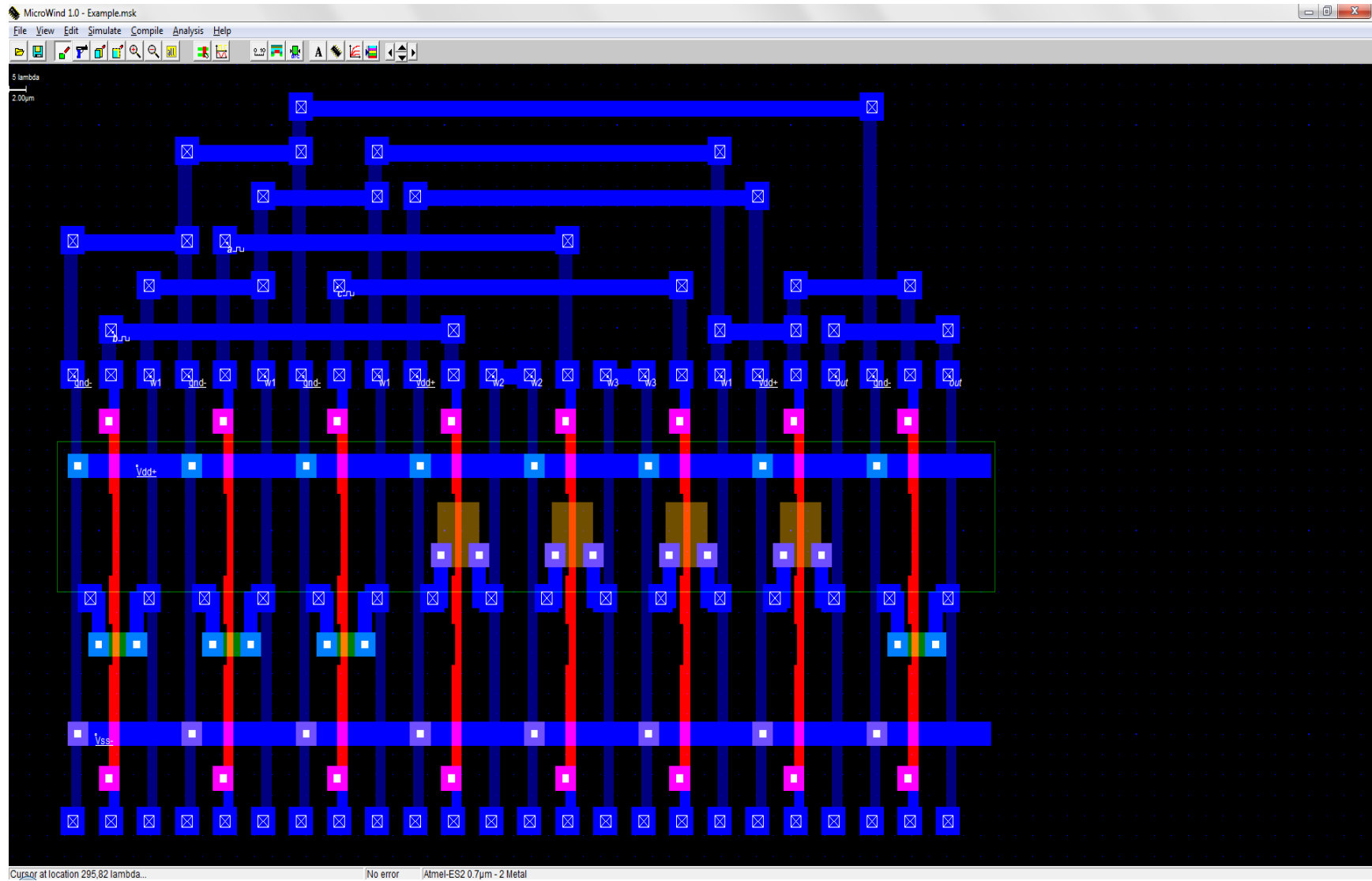
OR2 - design



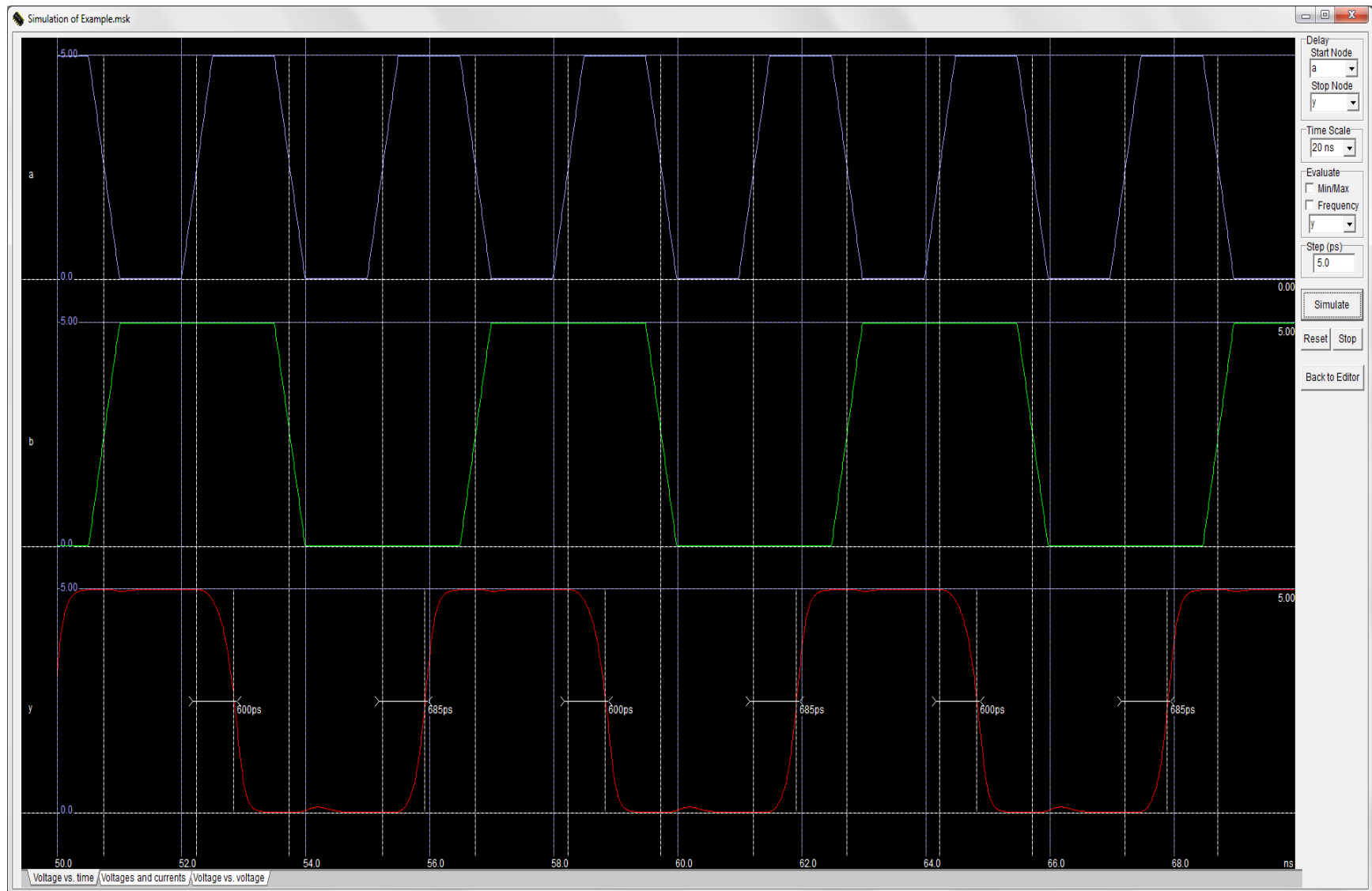
OR3 - simulare



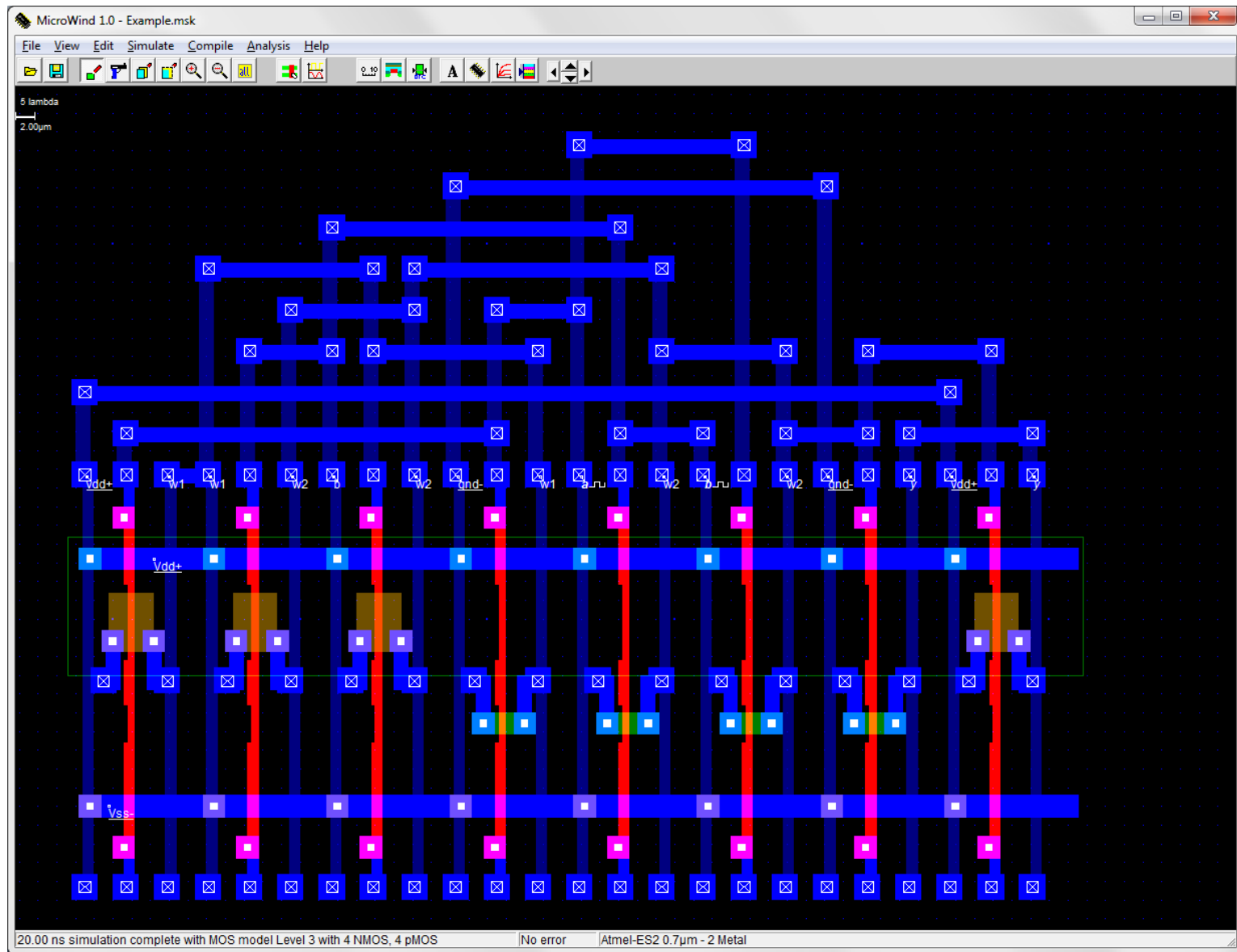
OR3 - design



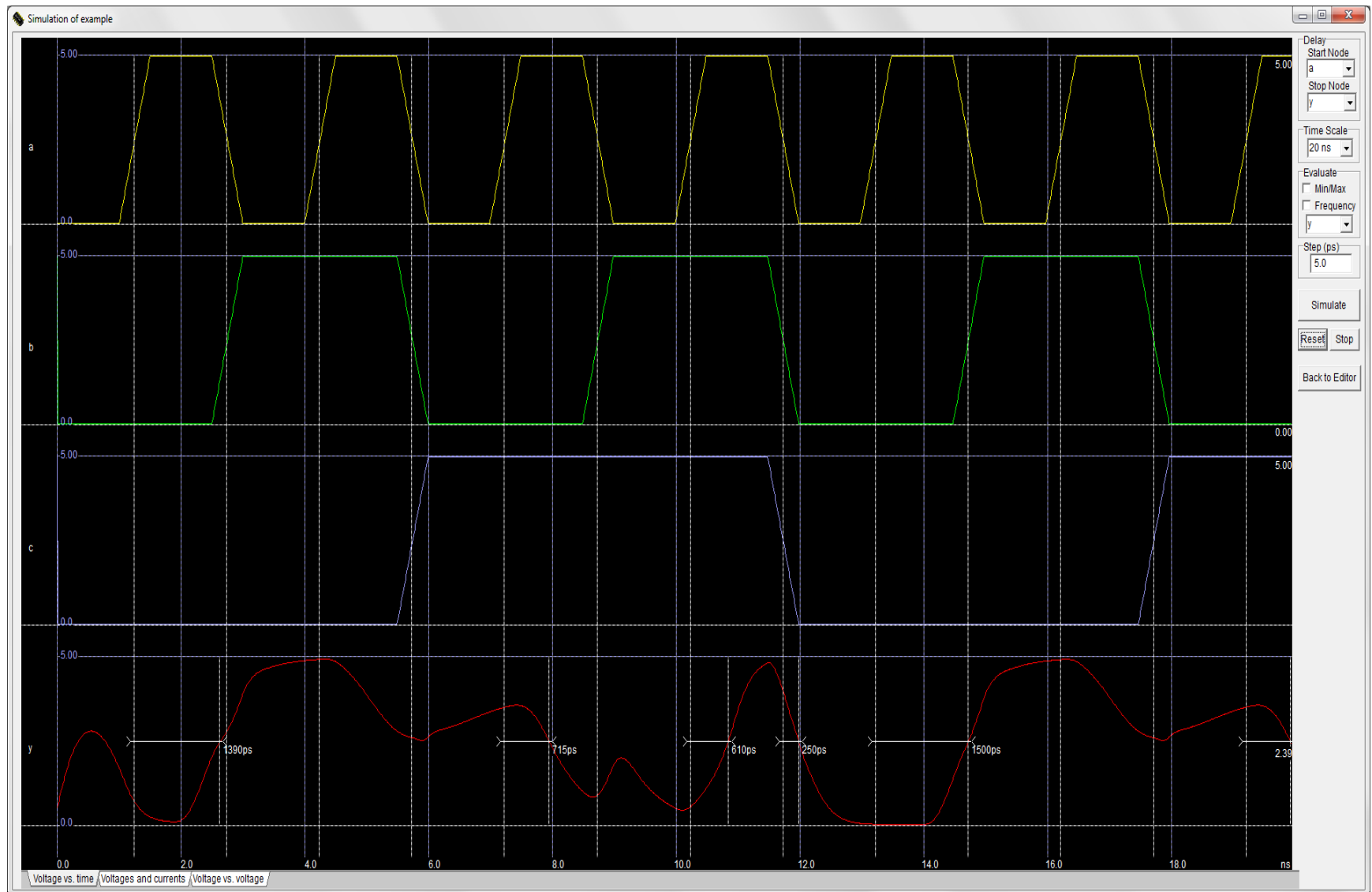
XOR2 - simulare



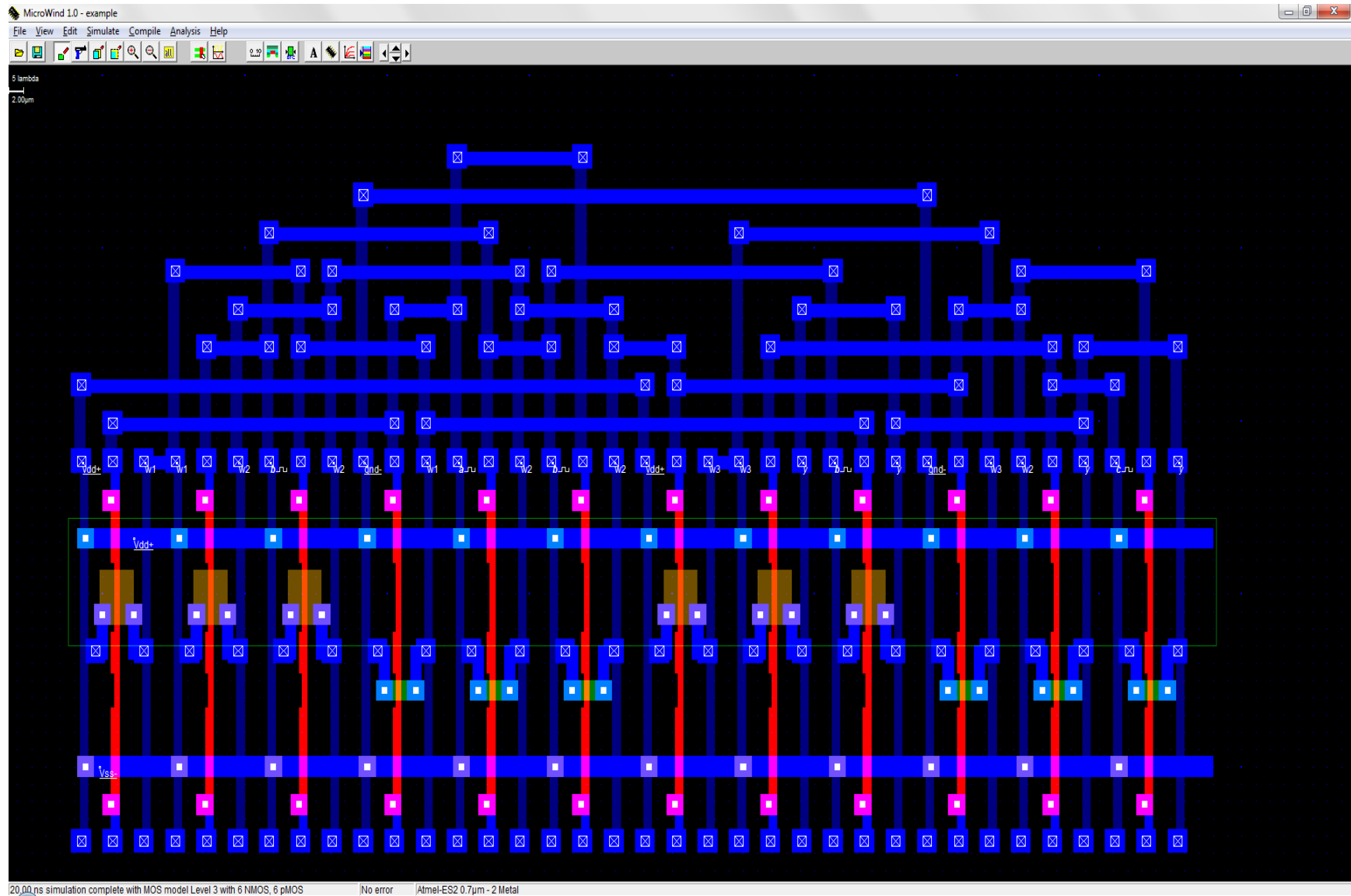
XOR2 - design



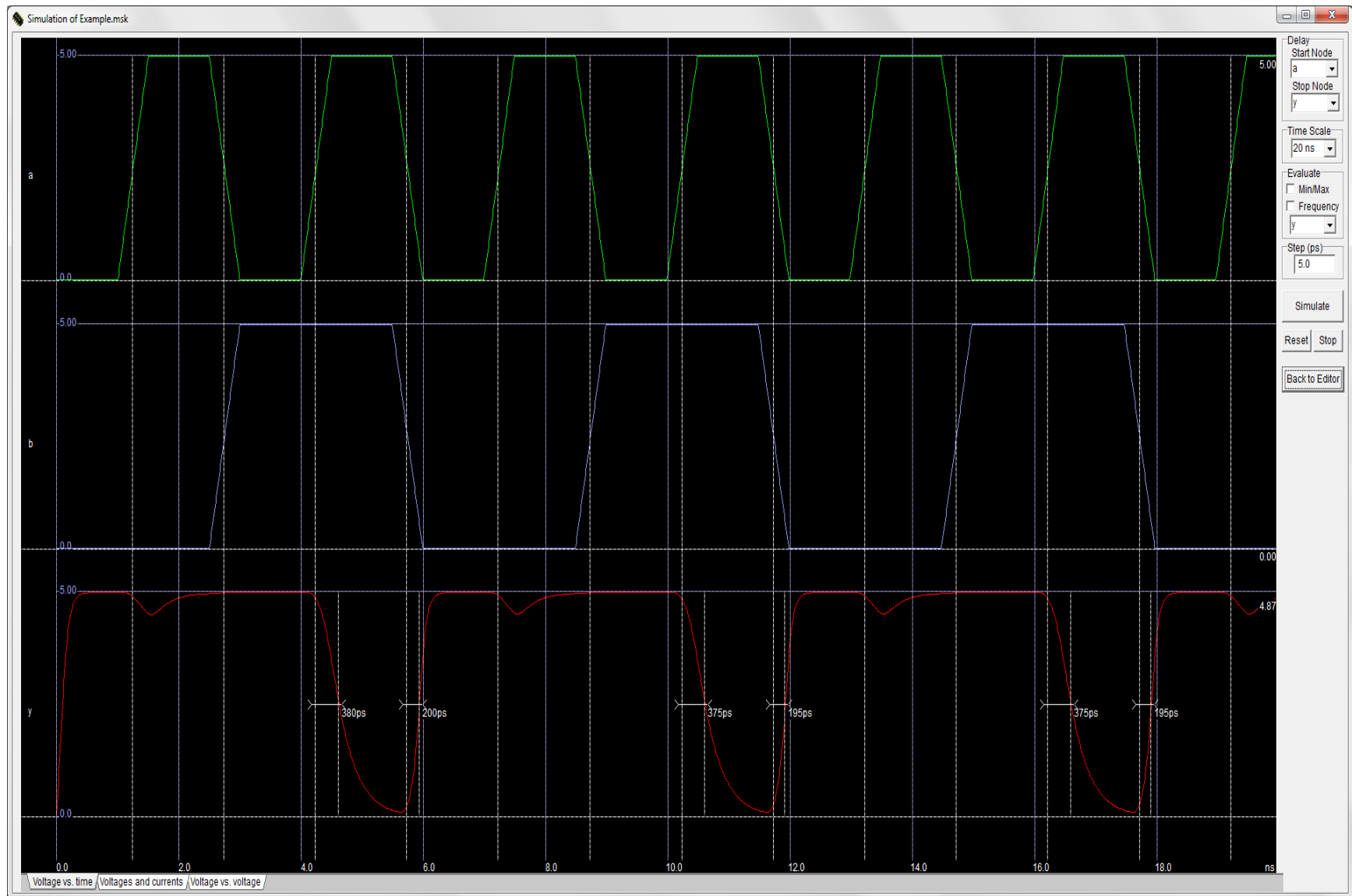
XOR3 - simulare



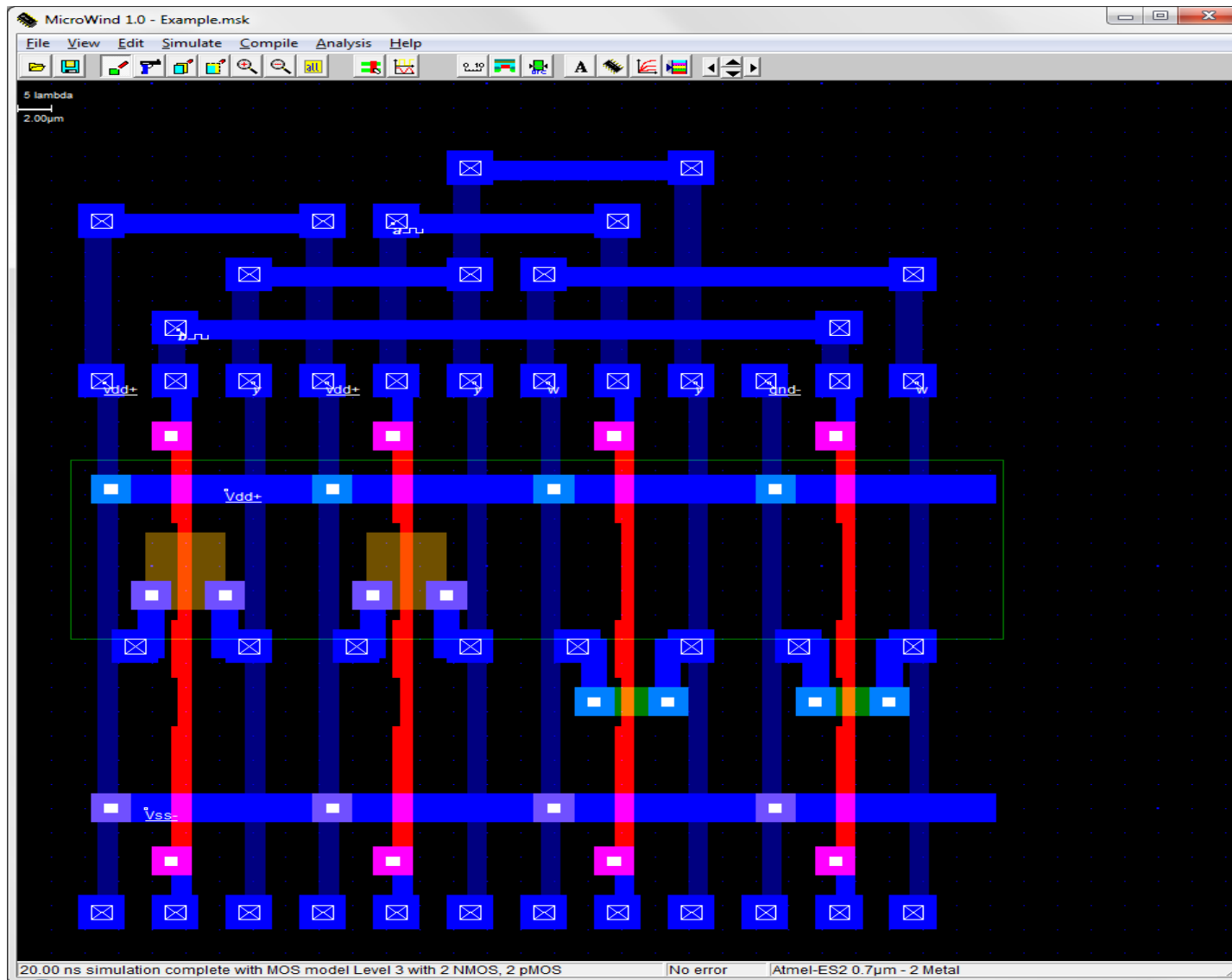
XOR3 - design



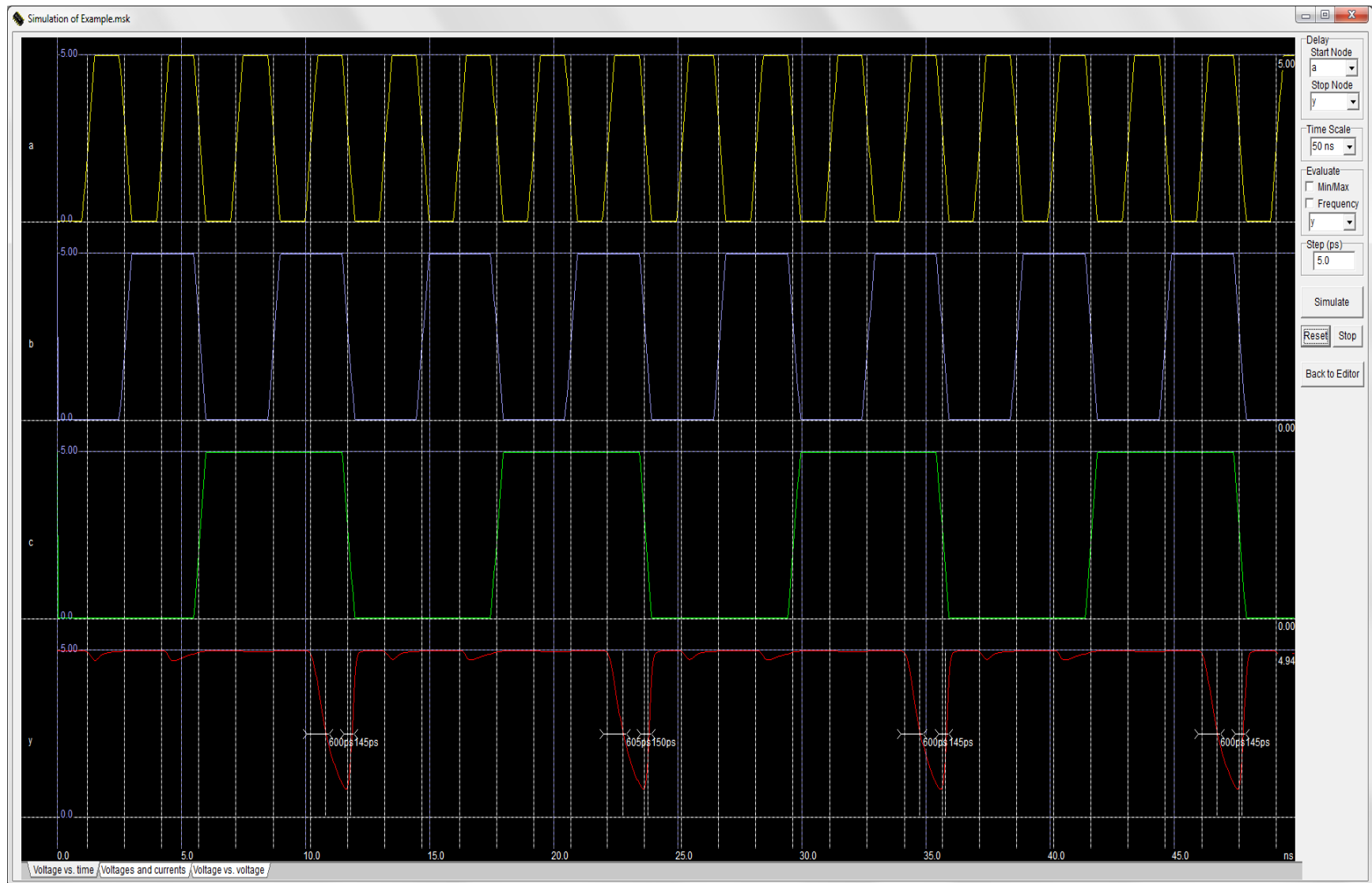
NAND2 - simulare



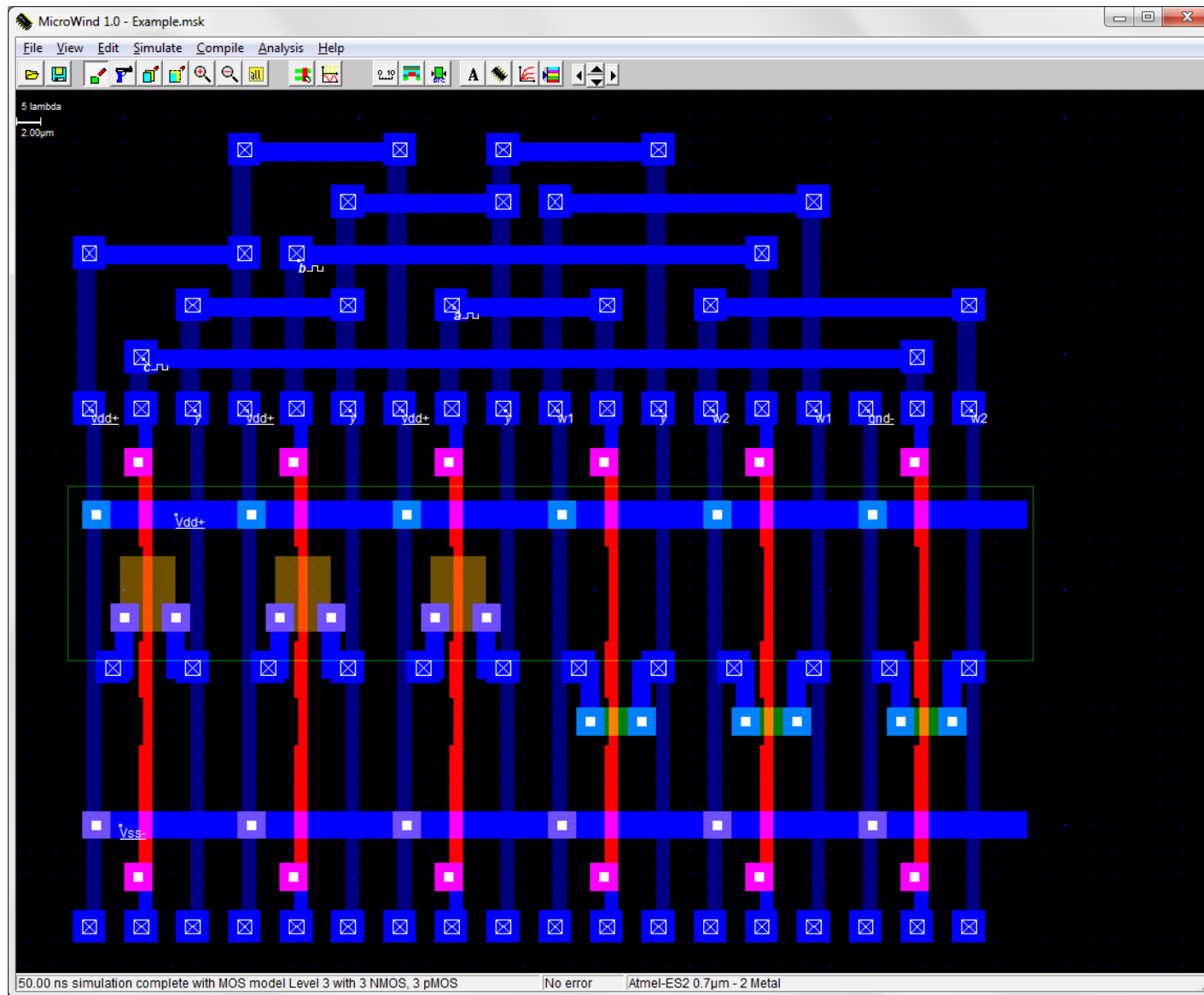
NAND2 - design



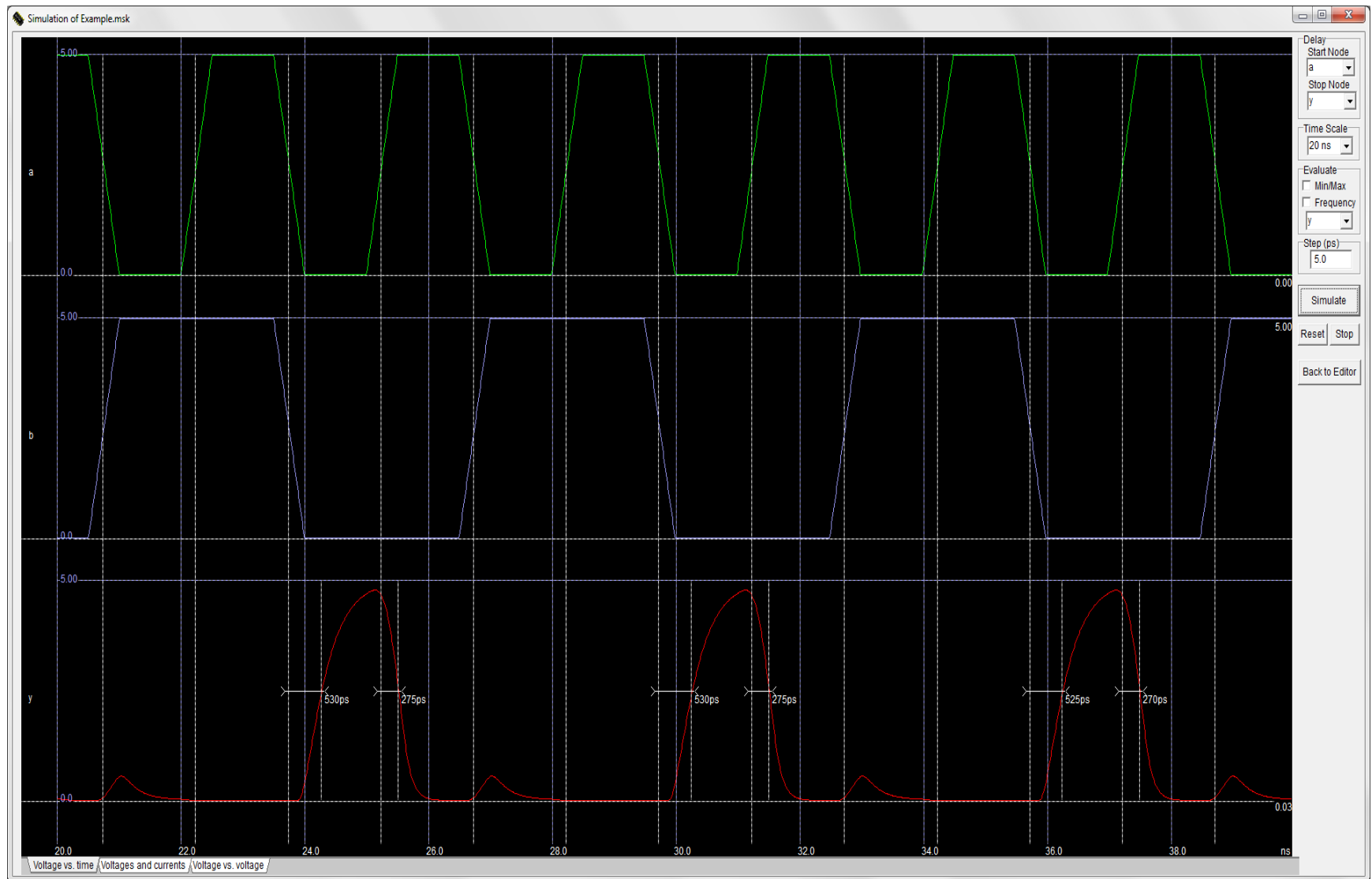
NAND3 - simulare



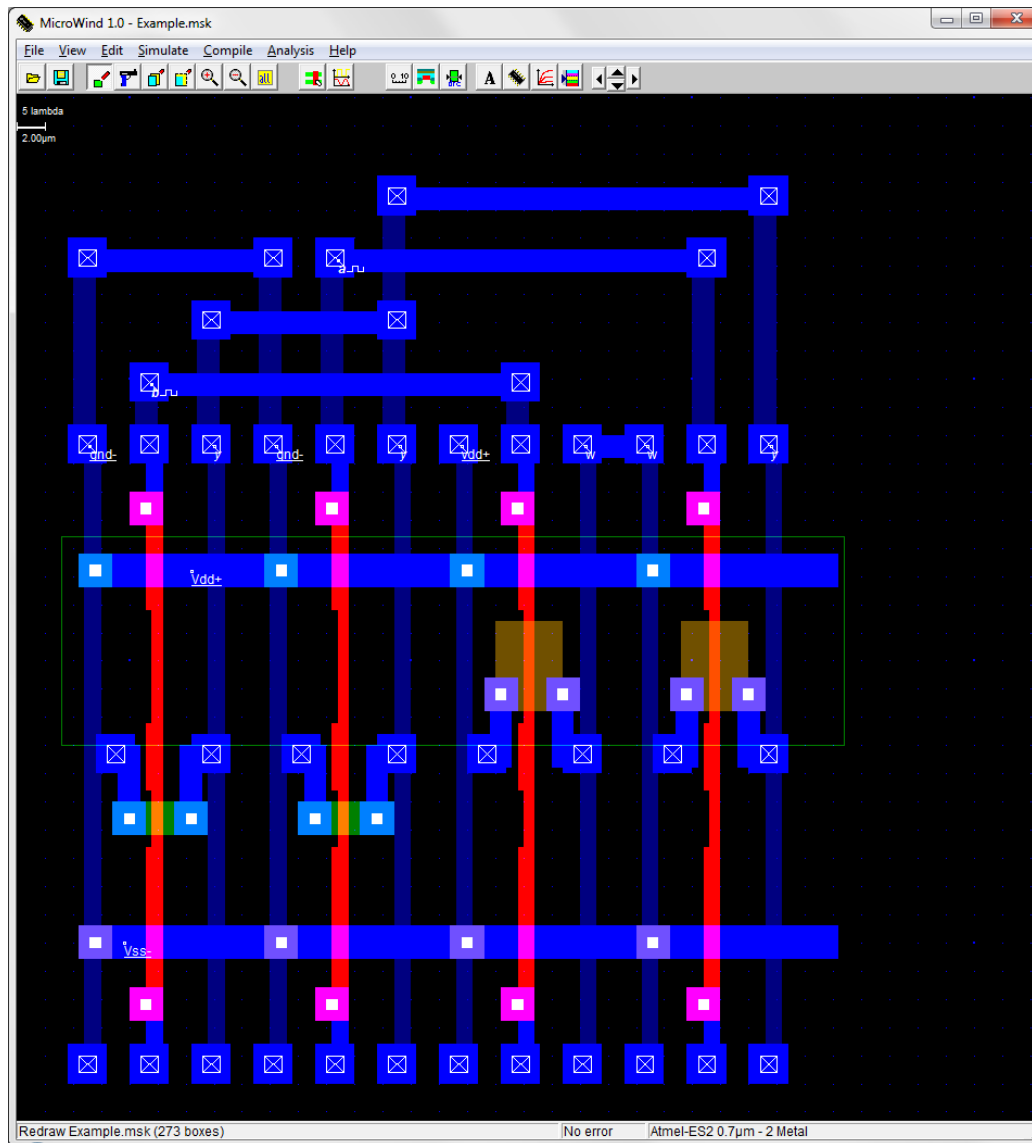
NAND3 - design



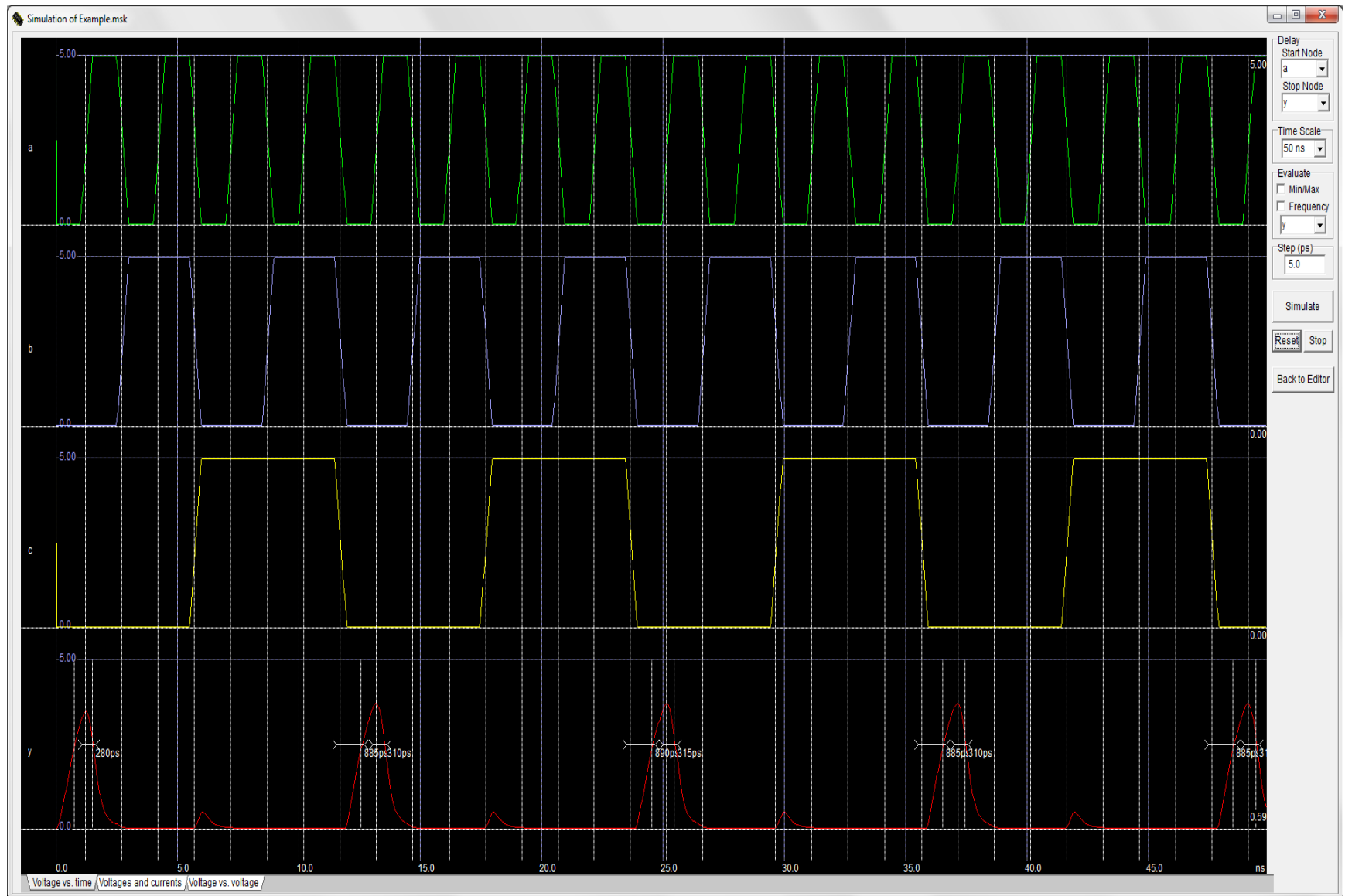
NOR2 - simulare



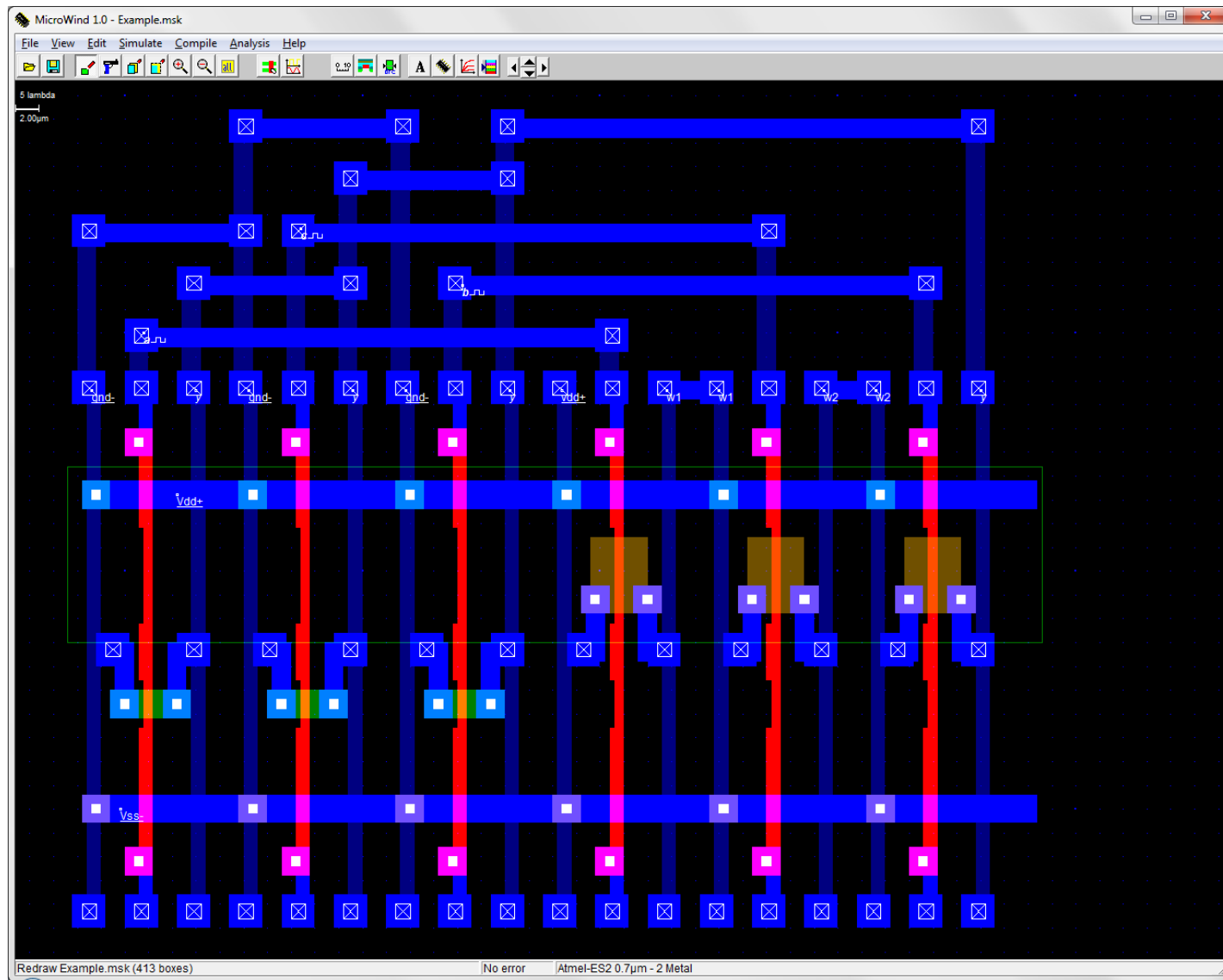
NOR2 - design



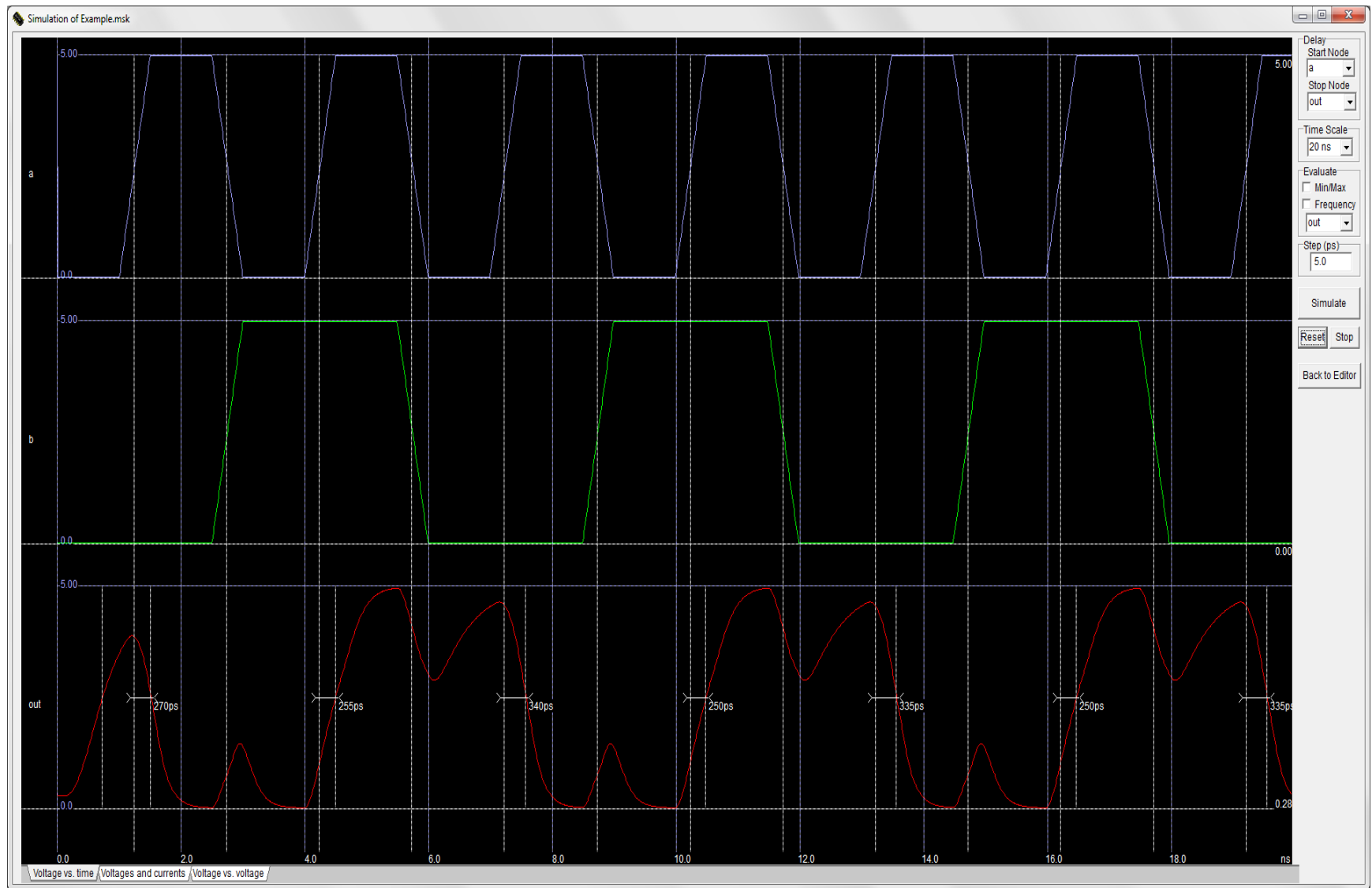
NOR3 - simulare



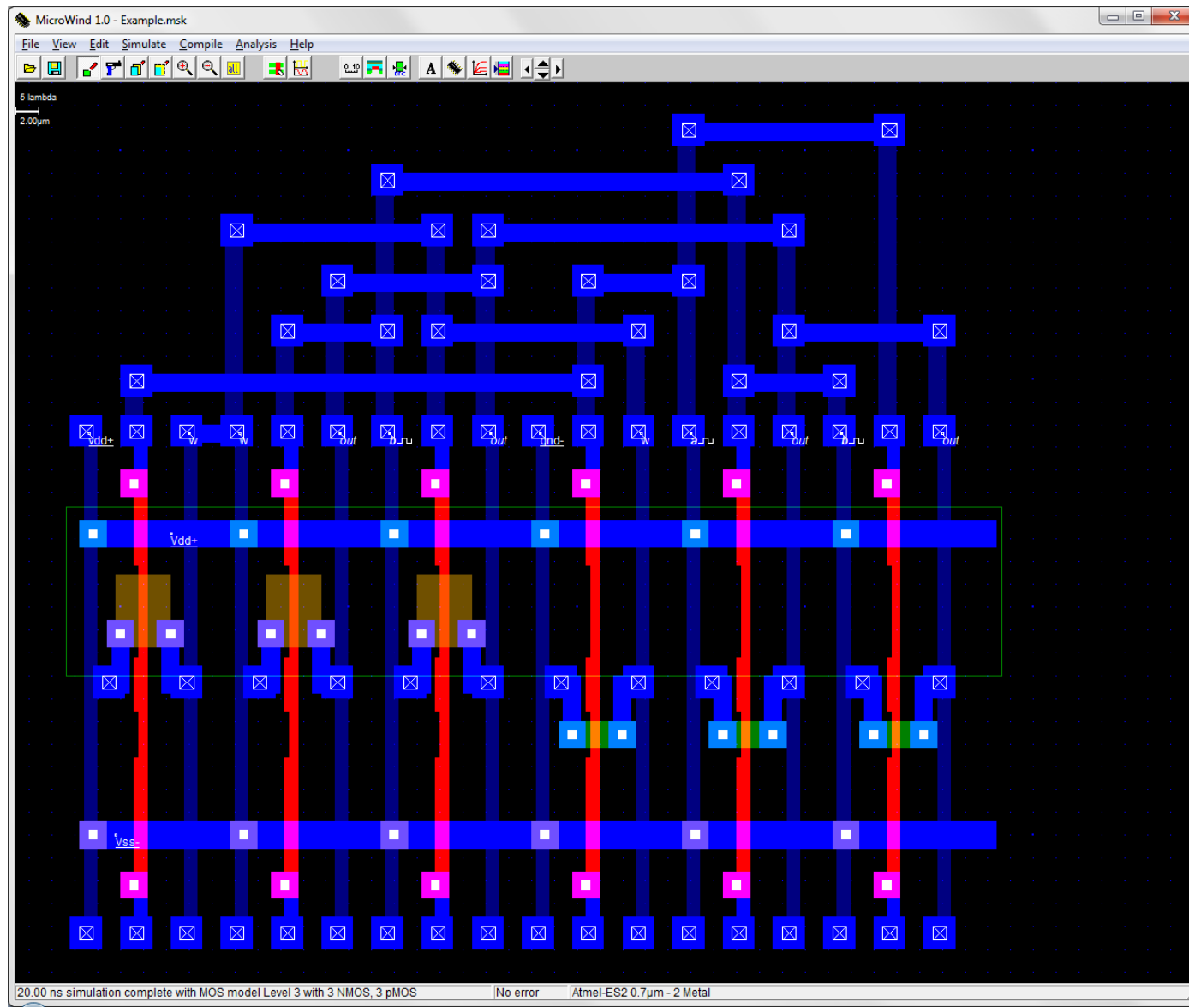
NOR3 - design



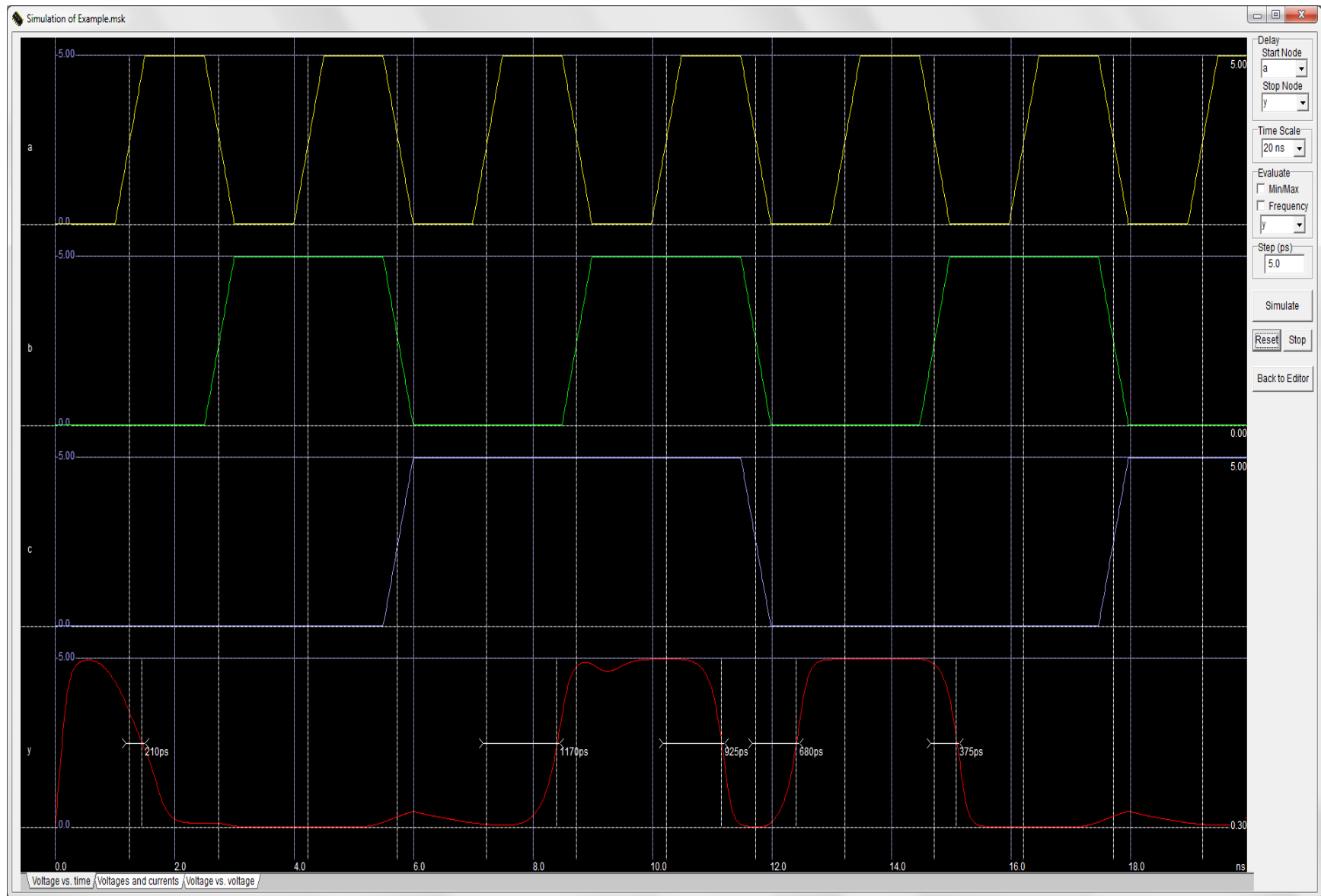
XNOR2 - simulare



XNOR2 - design



XNOR3 - simulare



XNOR3 - design

