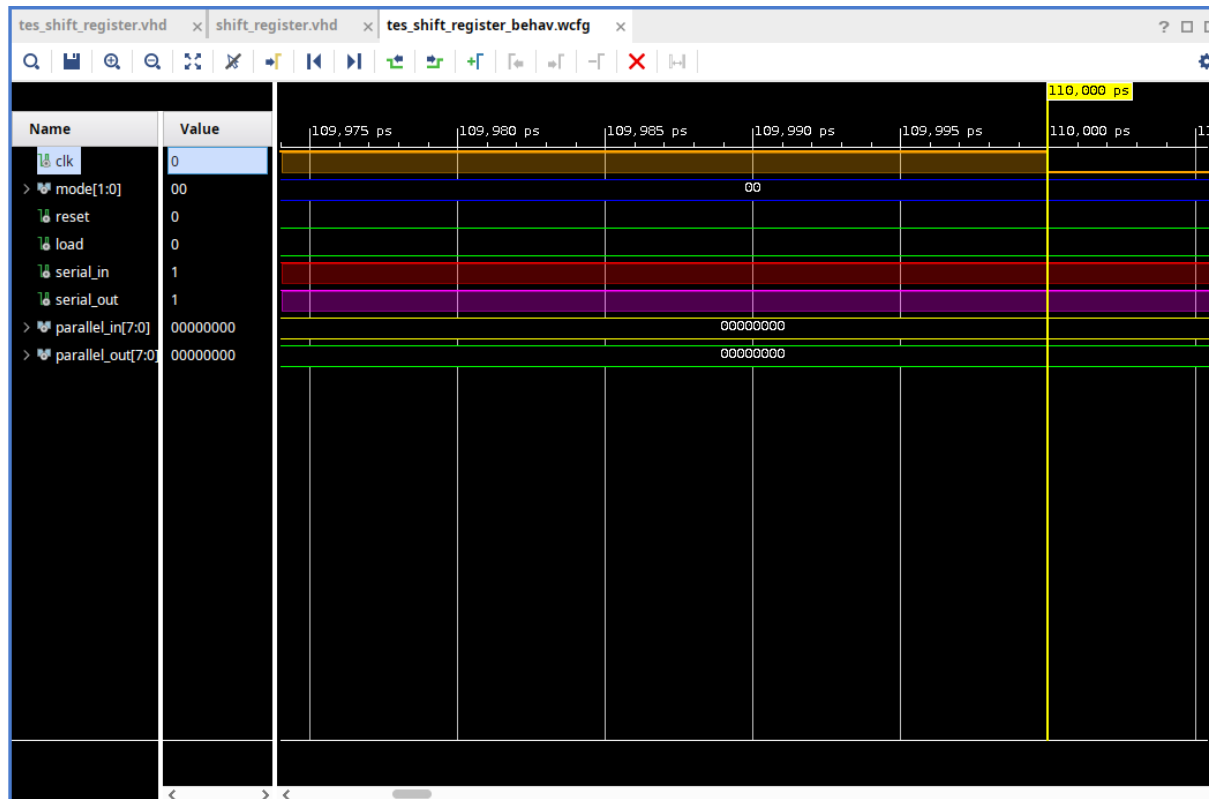


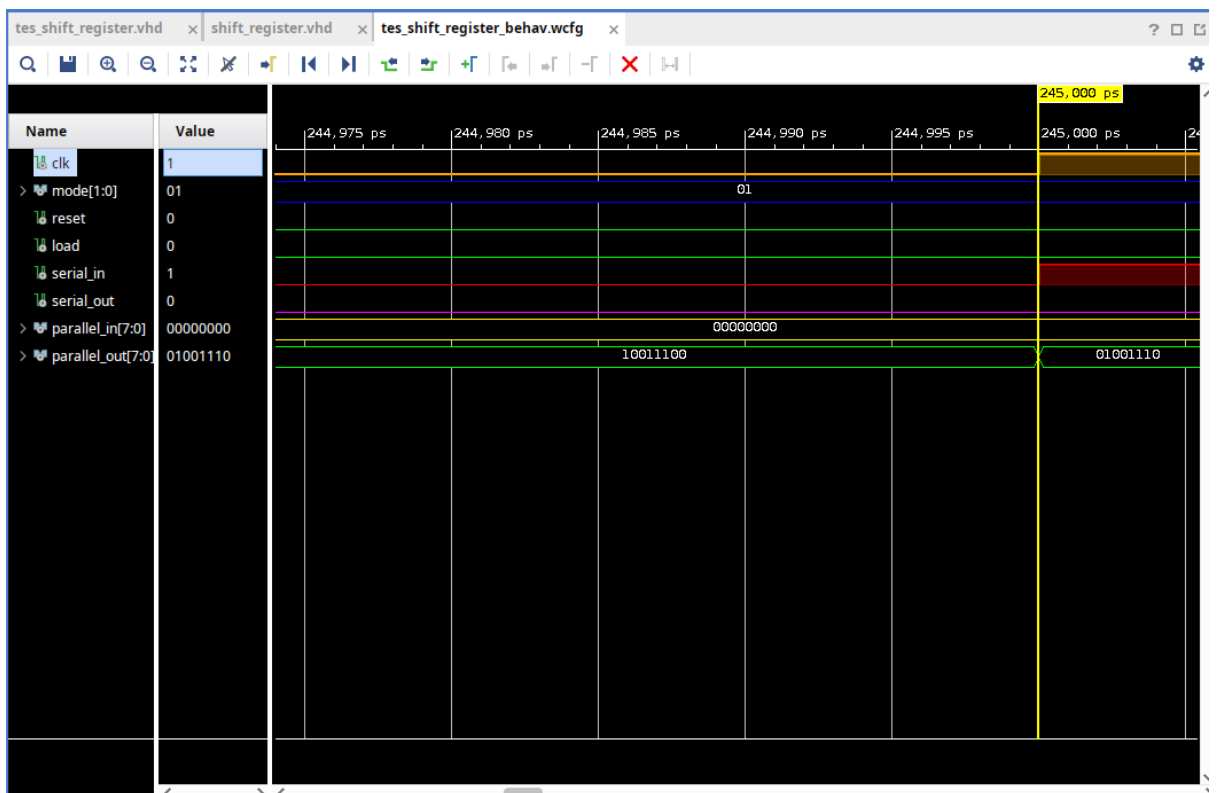
## CAPTURAS DE LAS PRUEBAS REALIZADAS

### UNIVERSAL SHIFT REGISTER

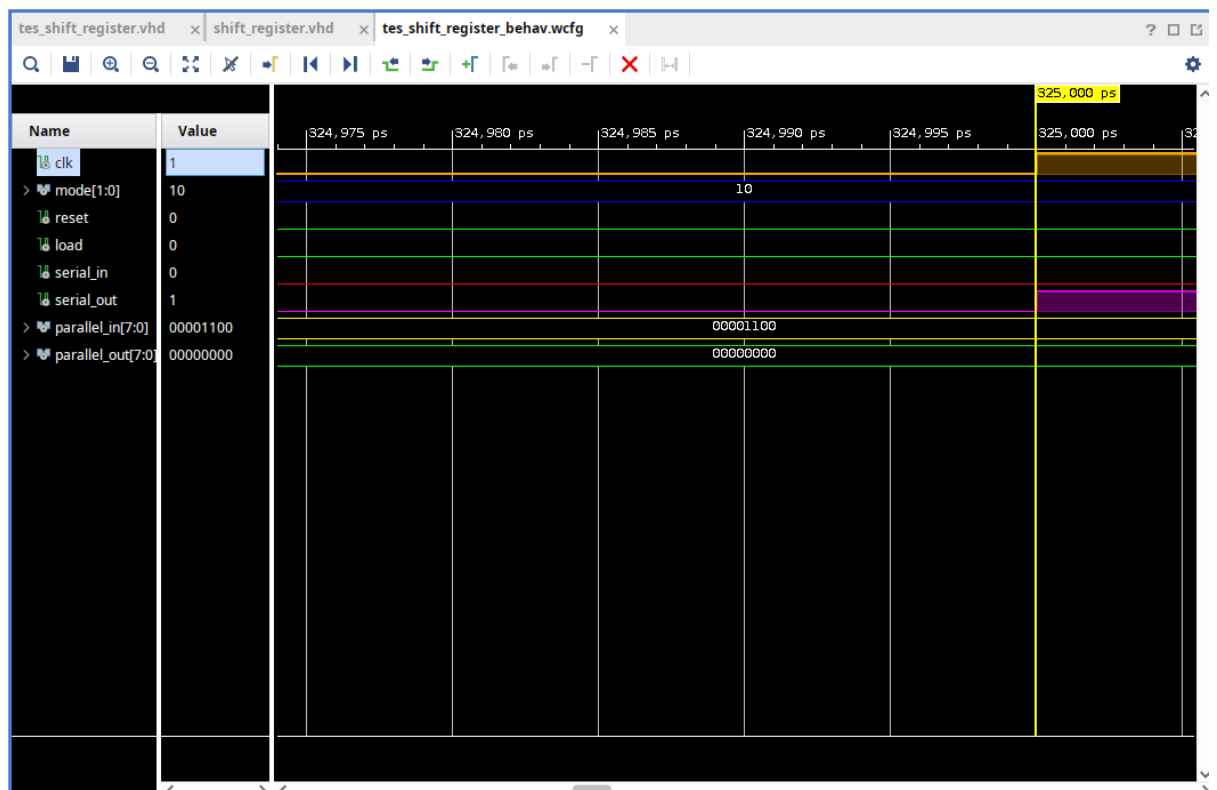
#### 1. TEST 1: SISO



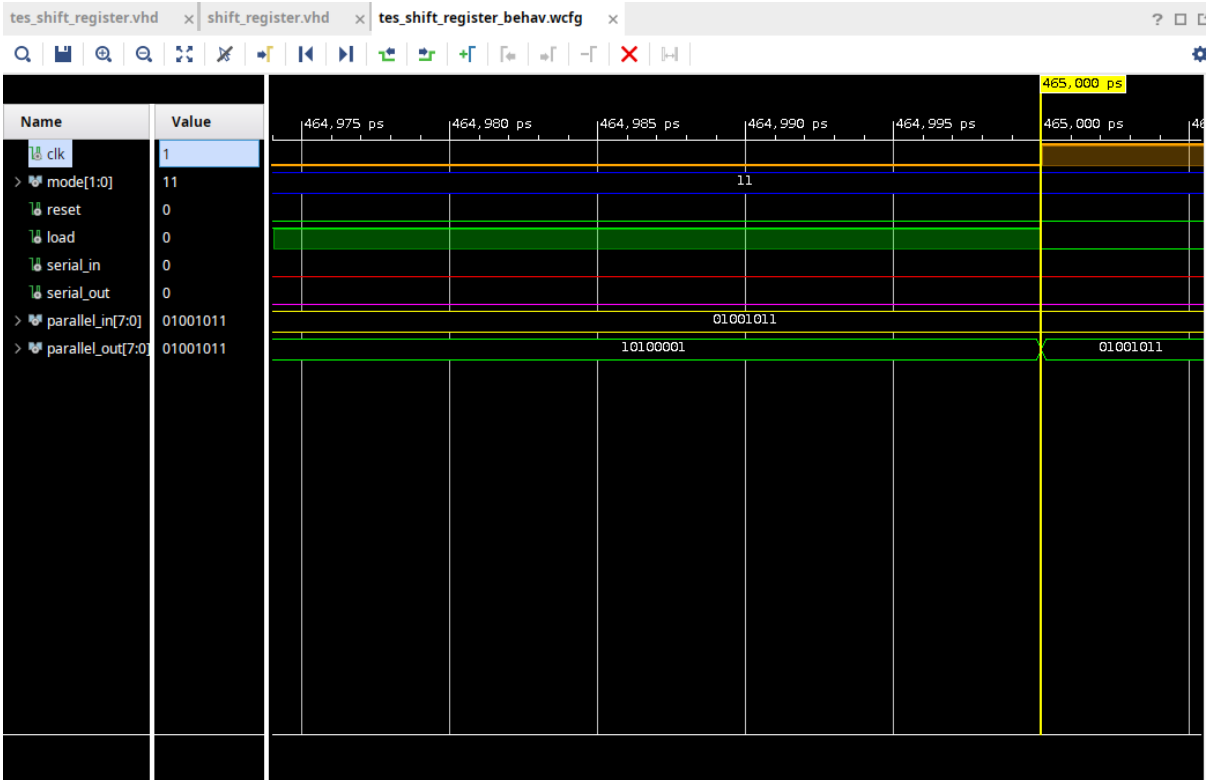
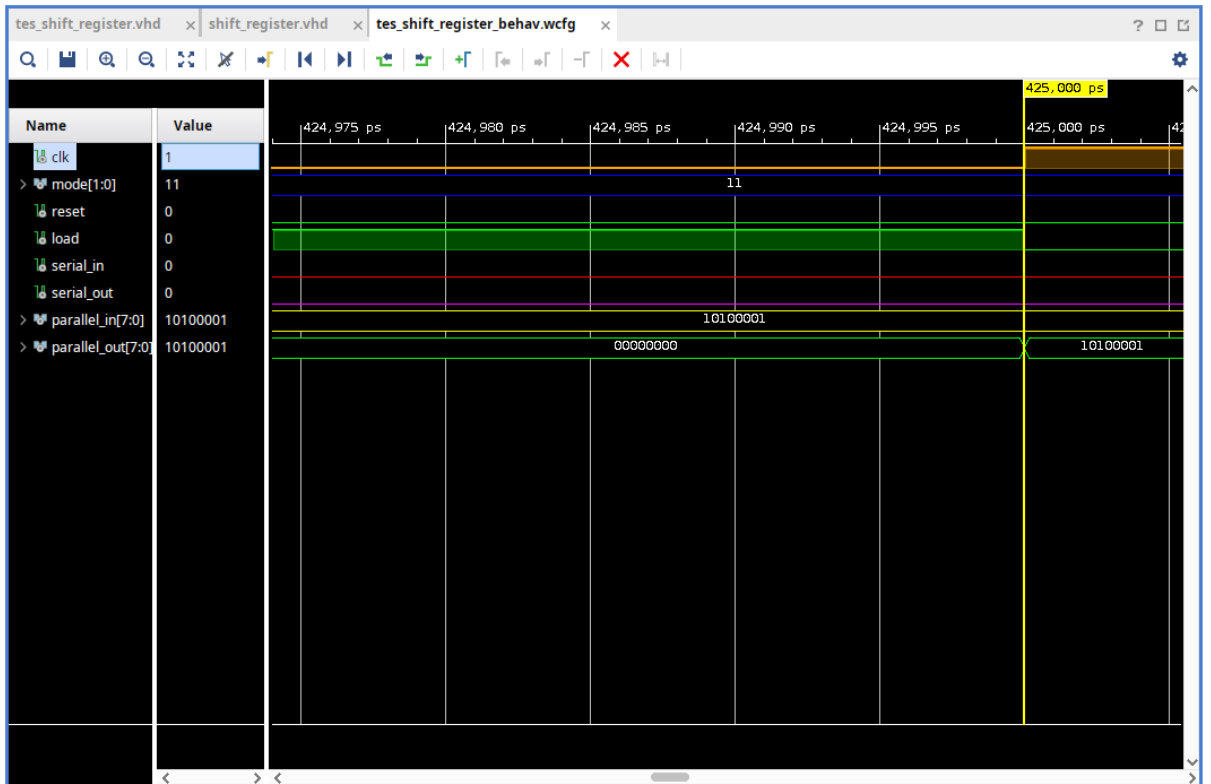
#### 2. TEST 2 : SIPO

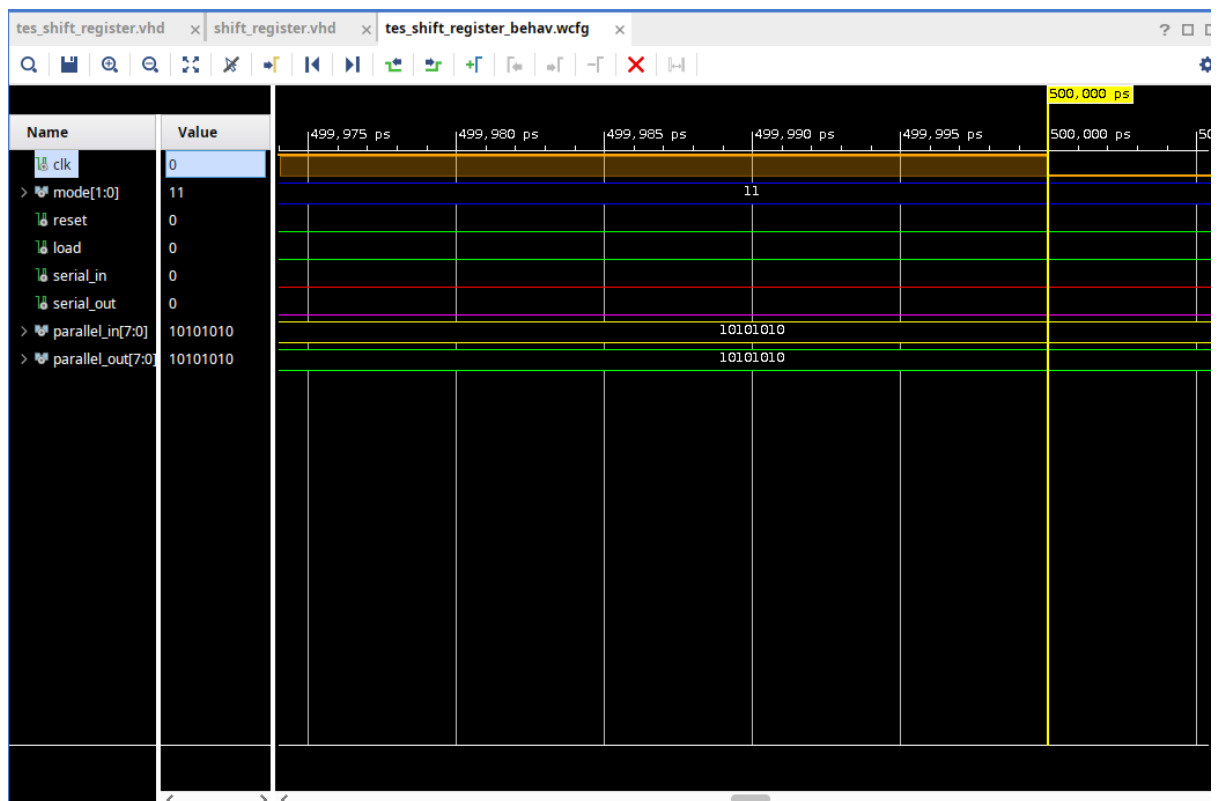


#### 3. TEST 3: PISO



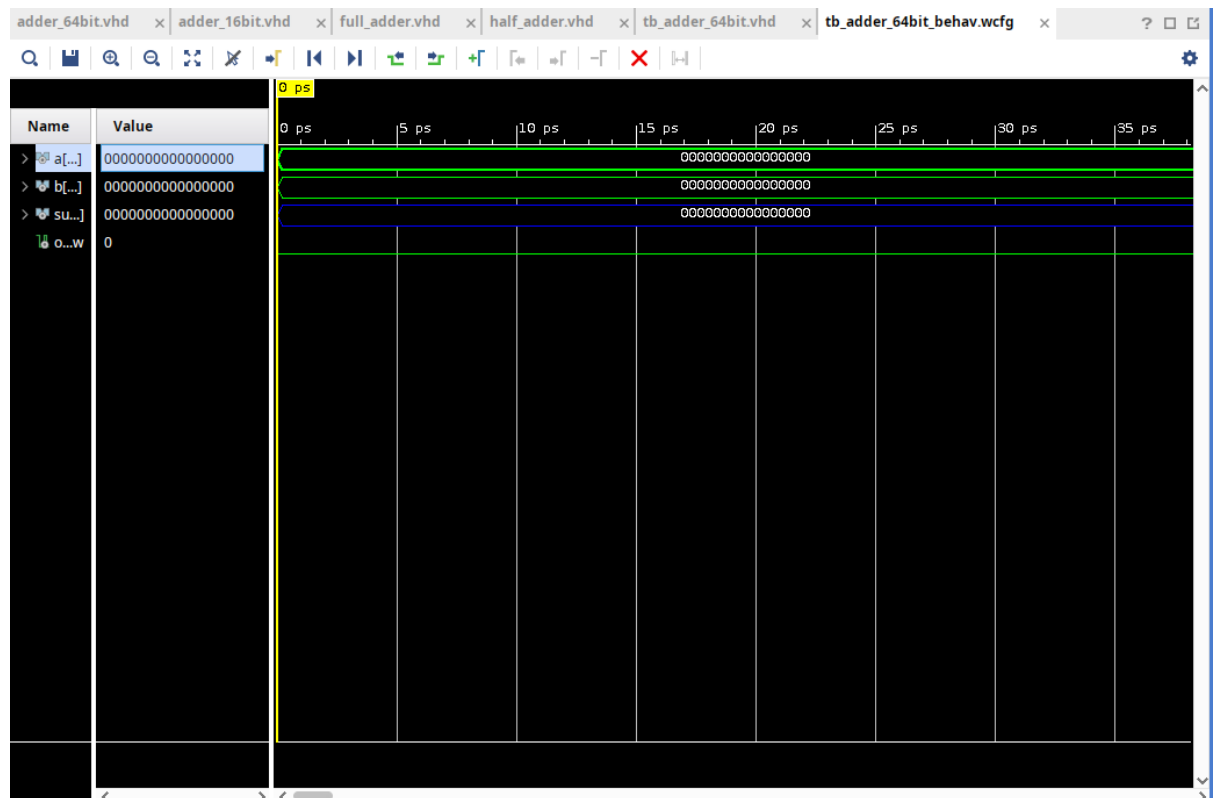
#### 4. TEST 4: PIPO





## HIERACHICAL 64 ADDER

### 1. TEST 1:

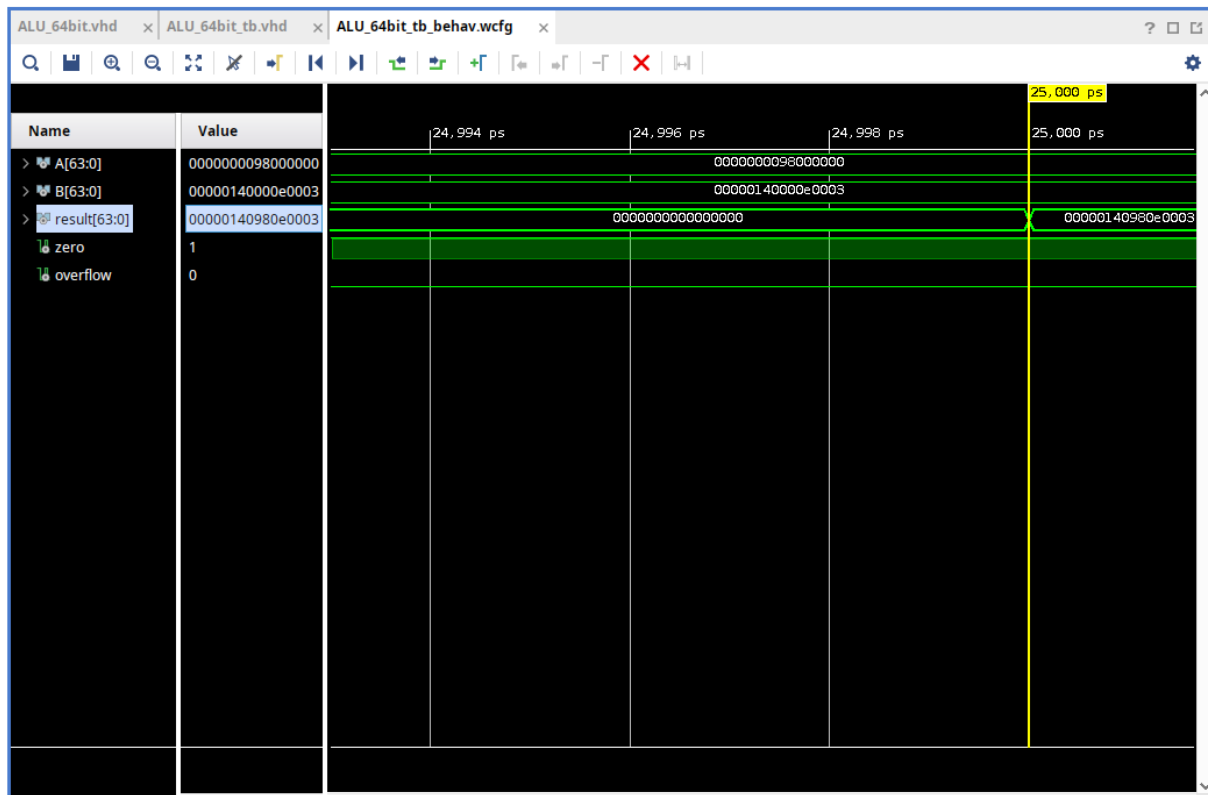


### 2. TEST 2:



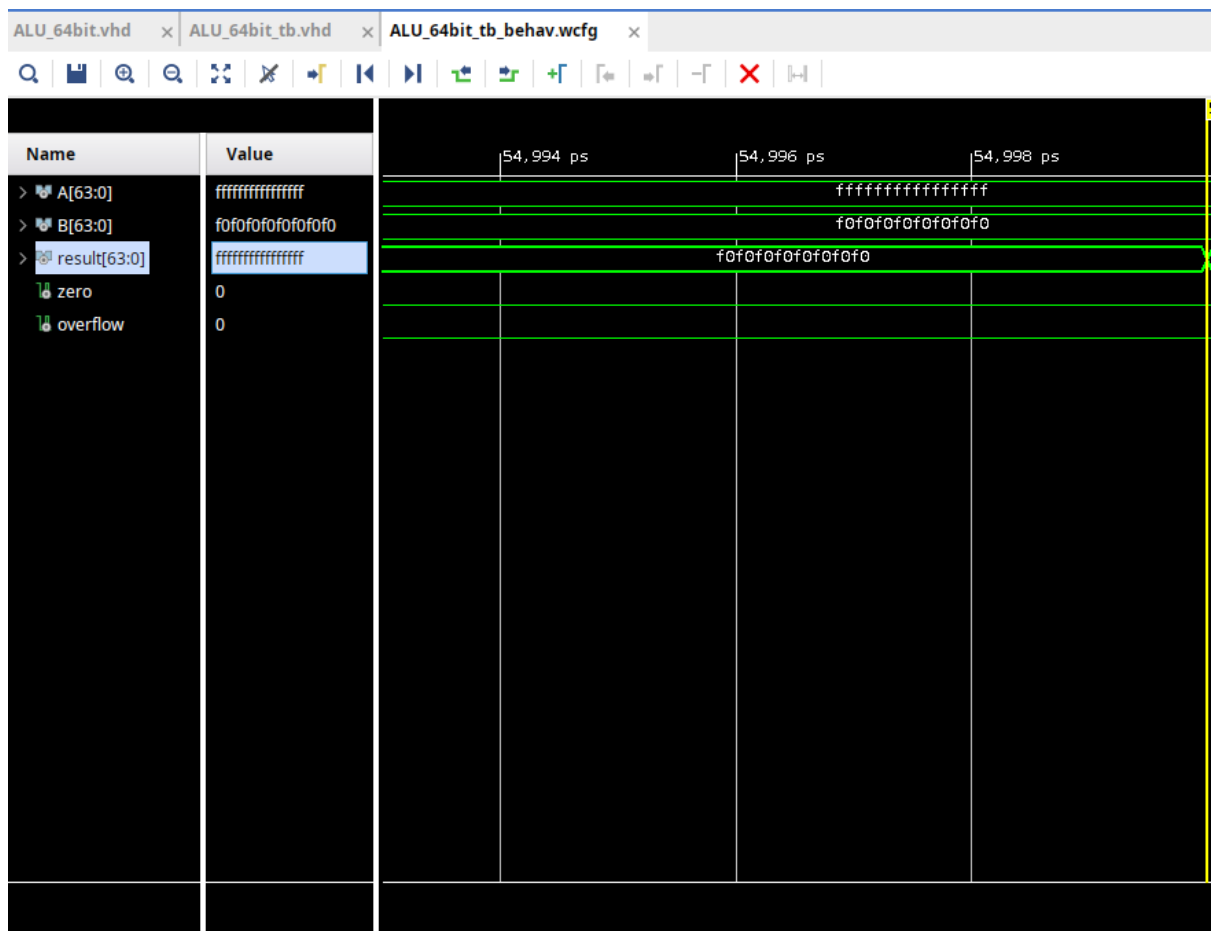
## ALU 64- BITS

### 1. TEST 1 :



### 2. TEST 2:





#### 4. TEST 4:

