

Laboratory 2: Resistor Combinations, KCL, KVL, Voltage and Current Dividers

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9/16/2020

Objectives

- Verify KCL and KVL
- Measure resistor combinations
- Measure branch currents and node voltages

Equipment and Components

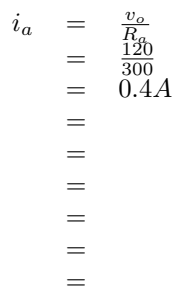
- A computer
- Matlab software

Preliminary

1. Refer to Chapters 2 and 3 of the textbook if necessary.
2. Complete the theoretical calculations related to this lab.

Procedure

- 1 Open Matlab
- 2 Create Simulink model of the circuit shown below by following the procedure in Lab 1
- 3 Fill up your simulation results in the following table.



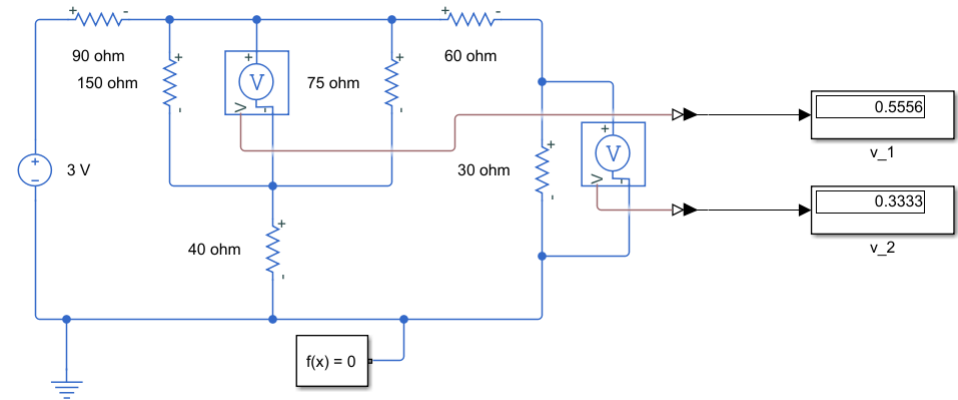
- What is the sum of i_a and i_b ? Sum = 2. What is i ? Explain.
Answer: i is the initial current flowing out of the voltage source. Also Current does not change when passing through resistors it only splits at forks in the circuit.
- What is the sum of v and v_o ? Sum = 200. Explain.
- Are your simulation results consistent with your theoretical results of Problem 2.18 in Assignment 2?
- Set the voltage source to be 100V and repeat the above steps. Fill up the table below. Comparing the results in Table 2 with those in Table 1, what do you find?
Answer: We found that when comparing the results between a 200V and a 100V source the 100V is exactly half the readings of the 200V



	Simulation Results	Theoretical Results
i_g	12.5A	
i_o	2A	

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5 Create the Simulink model of the following circuit and find v_1 and v_2 . Are the simulation solutions consistent with your theoretical solutions of Problem 3.30 in Assignment 3? Fill up the table shown below.



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	Simulation Results	Theoretical Results
v_1	0.5556V	
v_2	0.3333V	

Questions and Conclusions

- Use tables and graphs to explain your results.
- Summarize your findings and explanations in response to the questions posed in this lab.