

## Adrian B. Posadas

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### PROFESSIONAL SUMMARY

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- Senior Product Engineer (Design Verification)
- 9 years of IC design verification expertise on testing via test bench instruments (DC source, Power Supply, Digital Multimeter, Oscilloscope, Signal Generator) and Simulation (Cadence)

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### AREAS OF EXPERTISE

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|---------------------------------|----------------------|
| • Bench Test Verification       | • Cadence simulation |
| • Test Bench Instrument Mastery | • Circuit Debug      |
| • Test Bench Automation         | • Board Assembly     |
| • Test Indication Making        | • Circuit Analysis   |

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### CAREER EXPERIENCE

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#### Senior Product Engineer

April 2021 – Nov 2021

ROHM LSI Design Phils, Inc., Pasig City, Philippines

- Same tasks as Product Engineer
- Check and Verify results from Product Engineers and Verification Engineers before sending to the customer

#### Product Engineer

2018 - 2021

ROHM LSI Design Phils, Inc., Pasig City, Philippines

- Work hand-in-hand with Japanese IC Design Engineers to make Verification setups & indications for Design Verification Engineers.
- Improved quality of measurements from PH Design Verification Engineers by supporting and debugging before sending to customers in Japan.
- Reduced communication time of PH Design Verification Engineers to Japan Circuit Design Engineers resulting to faster job completion.
- Attended a 2-month Gate Driver Product Engineer training at ROHM Semiconductor HQ Kyoto, Japan

#### Senior Design Verification Engineer

2016-2018

ROHM LSI Design Phils, Inc., Pasig City, Philippines

- Accept more challenging design verification work with minimal supervision from IC designers
- Reduced test bench measurement time by 30% by using automation hardware and software
- Teach new Verification Engineers techniques for evaluating devices efficiently

**Design Verification Engineer****2012-2016**

ROHM LSI Design Phils, Inc., Pasig City, Philippines

- Worked together with LSI Designers in testing the different characteristics of analog and mixed signal ICs via test bench or simulation.
- Learned how to use test bench instruments like DC source, Oscilloscope, DC Power Supply, Digital Multimeter, Signal Generator and many more bench test equipment
- Learned how to use cadence for IC simulation
- Assembled circuit boards, both surface mount and thru-hole.

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**EDUCATION**

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Bachelor of Science, Major in Electronics and Communications Engineering  
De La Salle University Manila 2007-2011