

## Module#4 (LogBook–Contents)

\* Write your answer with suitable diagram.

submission deadline: [June 15, 2024](#) (Before 3:00 PM)

1. Explain the principle involved while interfacing an 8-bit ADC using interrupt; including suitable block diagram, process flow diagram and necessary ALP subroutine. **[2078 Bhadra]**
2. What are the parameters to characterize ADCs? Design a circuit to interface ADC0808 with 8085 microprocessor using 8255A PPI. **[2078 Kartik]**
3. Describe INL and DNL error of data converter with necessary illustrations. With necessary diagram, explain interfacing of 8 channel 8 bit ADC with 8085 microprocessor along with timing diagram. **[2076 Ashwin]**
4. Explain the interfacing technique of 12-bit DAC to 8-bit Data Bus. Explain different types of errors in ADC and DAC. **[2075 Chaitra, 2074 Chaitra, 2071 Chaitra, 2070 Chaitra]**
5. What are characteristics of A/D and D/A converters? With necessary diagram explain the interfacing of 10 bit DAC with 8085 along with timing diagram. **[2071 Shrawan, 2073 Chaitra]**
6. Why analog signals need to be transferred to digital? What are the selection criteria for selecting ADC? **[2073 Chaitra, 2073 Shrawan, 2080 Baishakh]**
7. Design an interfacing circuit diagram for 8 bit ADC using status check. Explain with the suitable flowchart and program. **[2080 Baishakh]**