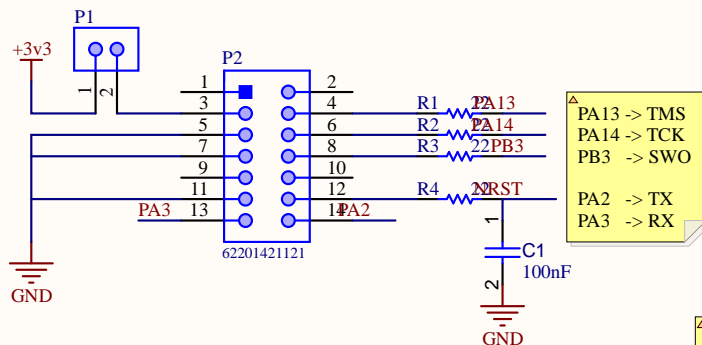


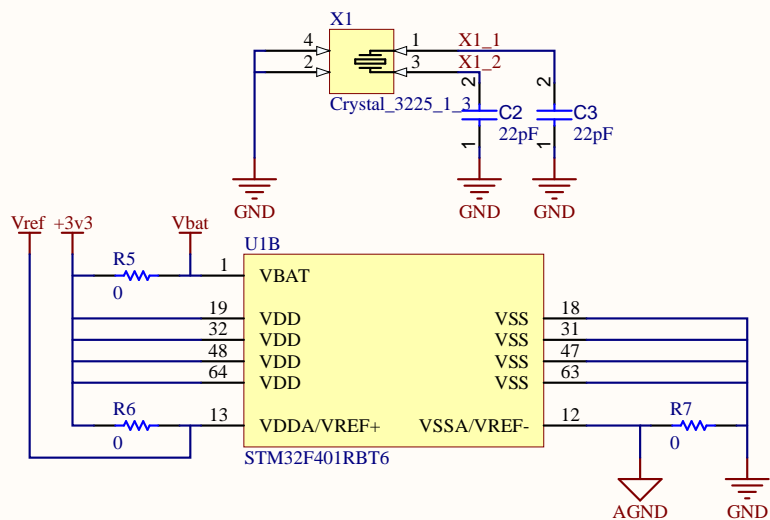
A

A



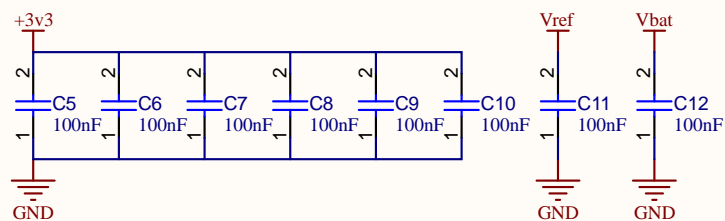
B

B



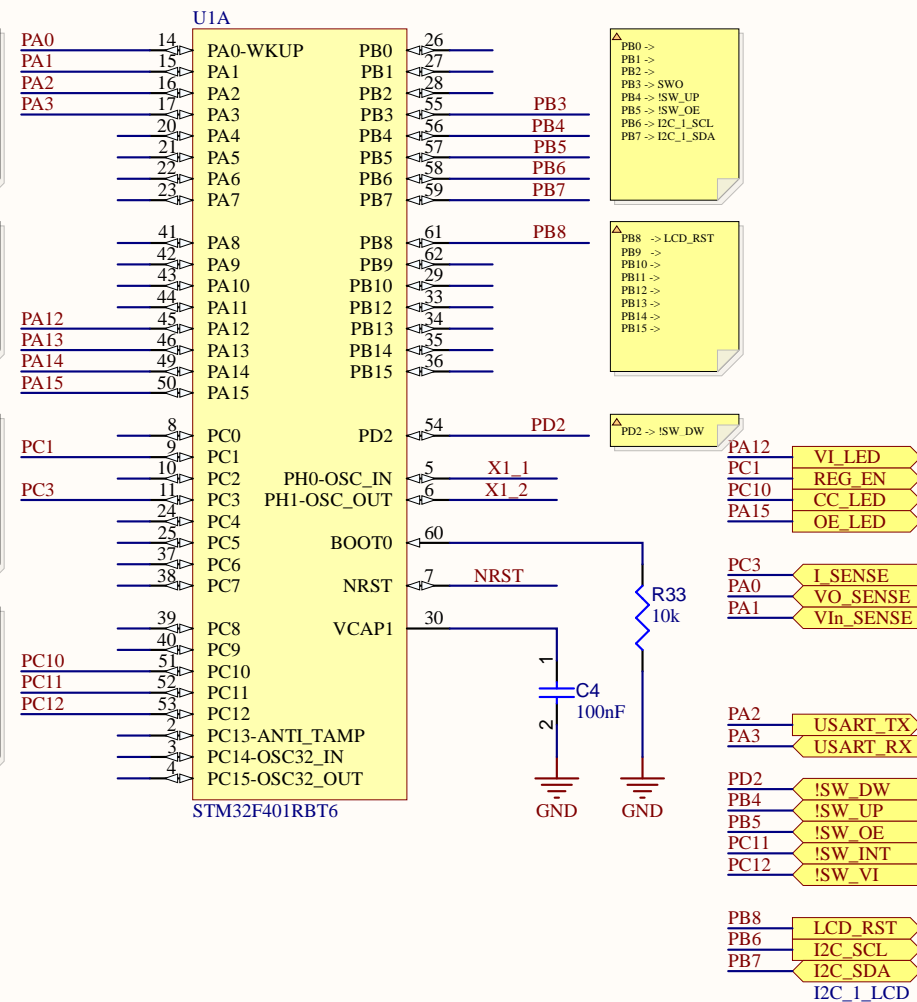
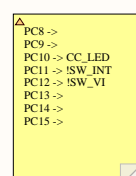
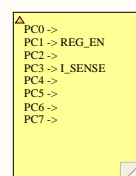
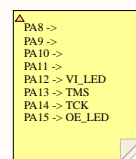
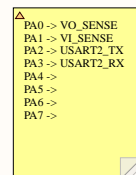
C

C



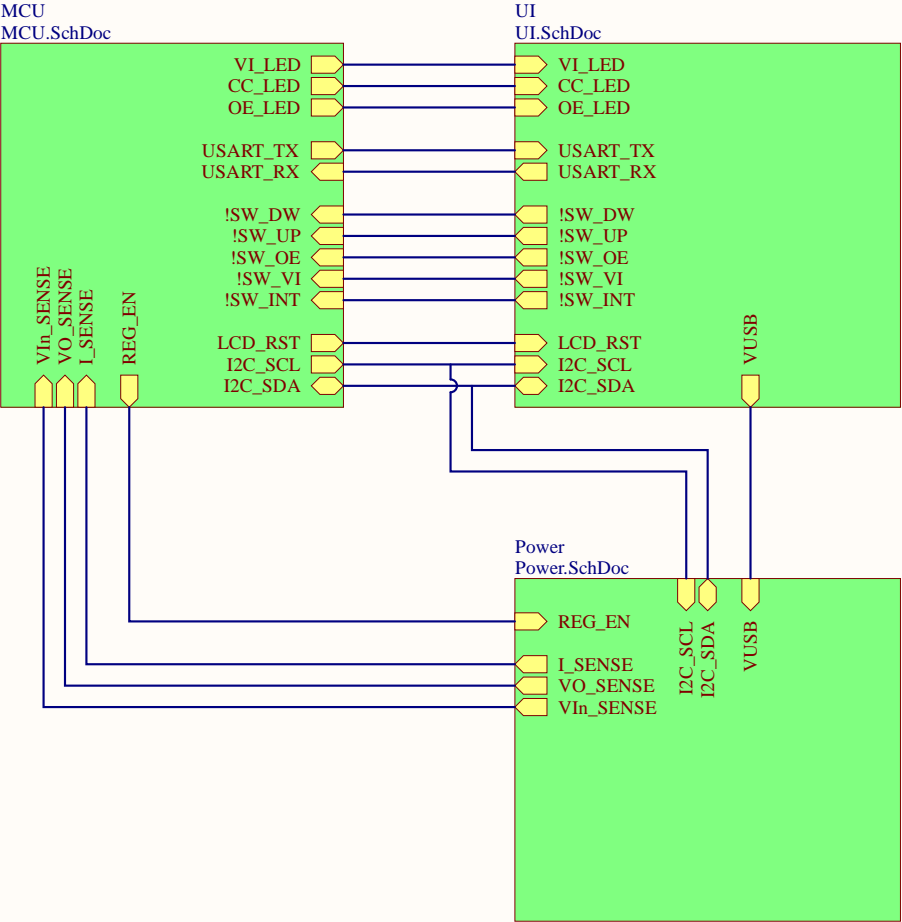
D

D

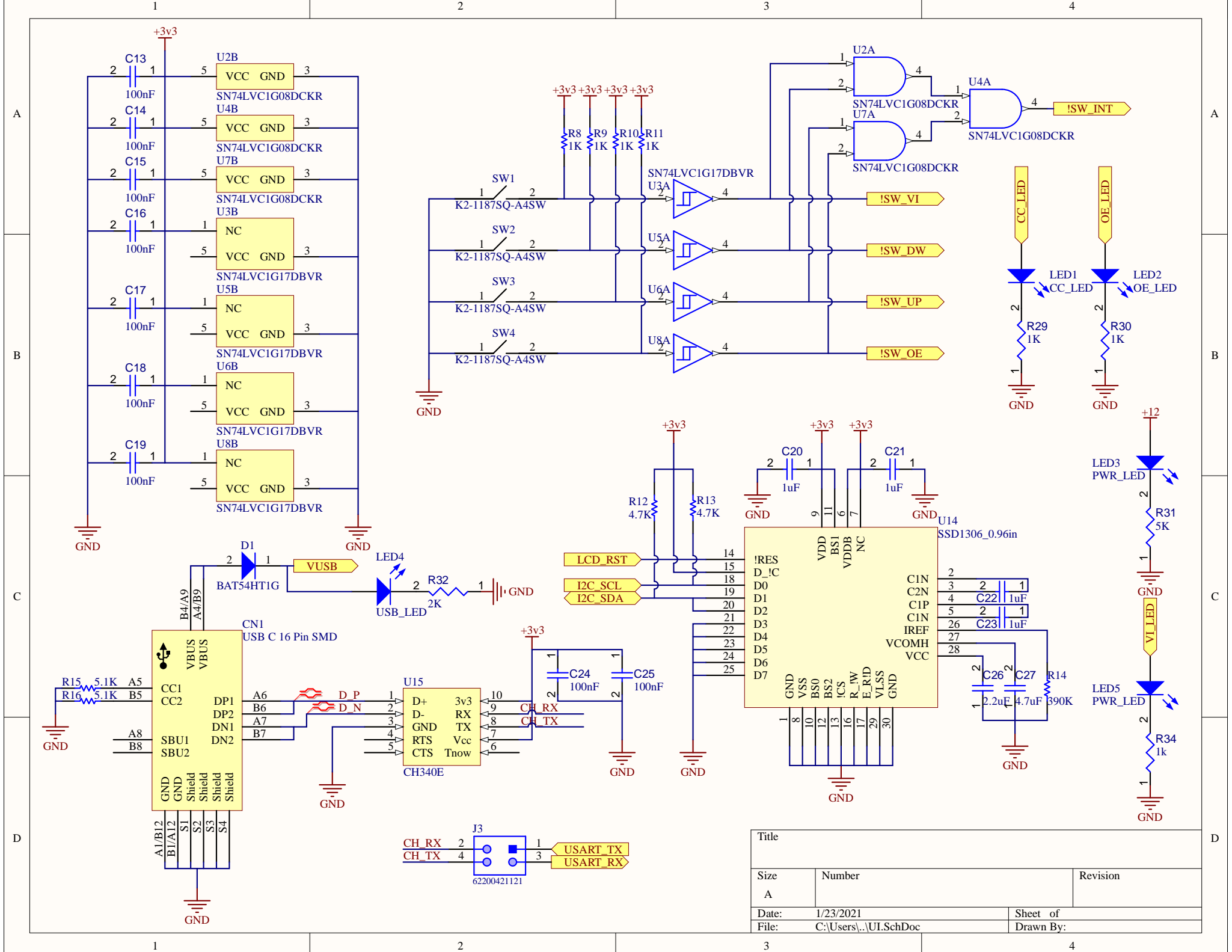


Title		
Size	Number	Revision
A		
Date:	1/23/2021	Sheet of
File:	C:\Users\...\MCU.SchDoc	Drawn By:





Title			
Size	Number		Revision
A			
Date:	1/23/2021		Sheet of
File:	C:\Users\...\TopLevel.SchDoc		Drawn By:



Title		
Size	Number	Revision
A		
Date:	1/23/2021	Sheet of
File:	C:\Users\...\UI.SchDoc	Drawn By:

**ADSEHGAL**  
Power Supply Rev 1.1  
[github.com/adsehg1](https://github.com/adsehg1)

OE

UP

DW

V/I

P1

USB Vin CC OE V/I  
R32 R31 R29 R30 R34

Vout GND

Vin GND

D2

J1

J2

S9

13

4

CN1

J3

R27

C24

C25

C25

C25

C25

C25

C25

C25

C25

C25

U17

U10

R28

C39

C4

C5

C5

C641

U12

D3

R19

R20

C3

C3

C11

R7

R6

C1

X1

C2

C2

C17

U6

U7

C12

C17

C17

C17

C15

R10

C14

R9

C13

R8

C13

C16

U8

R11

C15

R10

C14

C14

C40

R18

U11

C37

C38

C38

C38

C42

U13

R17

U16

C28

C28

C28

J1

J1

J1

J1

J1

J1

J1

C33

C34

C33

C34

C33

C34

C33

C32

U9

C32

C32

C32

C32

C32

C35

R21

R22

R25

R26

R23

R37

C39

C39

C39

C39

C39

C39

C39

C29

C30

C29

C30

C29

C30

C29

C3

C3

C3

C3

C3

C3

C3

C31

C31

C31

C31

C31

C31

C31

C36

C36

C36

C36

C36

C36

C36

C32

C32

C32

C32

C32

C32

C32

C37

C37

C37

C37

C37

C37

C37

C38

C38

C38

C38

C38

C38

C38

C39

C39

C39

C39

C39

C39

C39

C40

C40

C40

C40

C40

C40

C40

C41

C41

C41

C41

C41

C41

C41

C42

C42

C42

C42

C42

C42

C42

C43

C43

C43

C43

C43

C43

C43

C44

C44

C44

C44

C44

C44

C44

C45

C45

C45

C45

C45

C45

C45

C46

C46

C46

C46

C46

C46

C46

C47

C47

C47

C47

C47

C47

C47

C48

C48

C48

C48

C48

C48

C48

C49

C49

C49

C49

C49

C49

C49

C50

C50

C50

C50

C50

C50

C50

C51

C51

C51

C51

C51

C51

C51

C52

C52

C52

C52

C52

C52

C52

C53

C53

C53

C53

C53

C53

C53

C54

C54

C54

C54

C54

C54

C54

C55

C55

C55

C55

C55

C55

C55

C56

C56

C56

C56

C56

C56

C56

C57

C57

C57

C57

C57

C57

C57

C58

C58

C58

C58

C58

C58

C58

C59

C59

C59

C59

C59

C59

C59

C60

C60

C60

C60

C60

C60

C60

C61

C61

C61

C61

C61

C61

C61

C62

C62

C62

C62

C62

C62

C62

C63

C63

C63

C63

C63

C63

C63

C64

C64

C64

C64

C64

C64

C64

C65

C65

C65

# Board Stack Report