

NUCLEO64 STM32G4

MB1367

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[U_mb1367.Top](#)
[mb1367.Top.SchDoc](#)

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

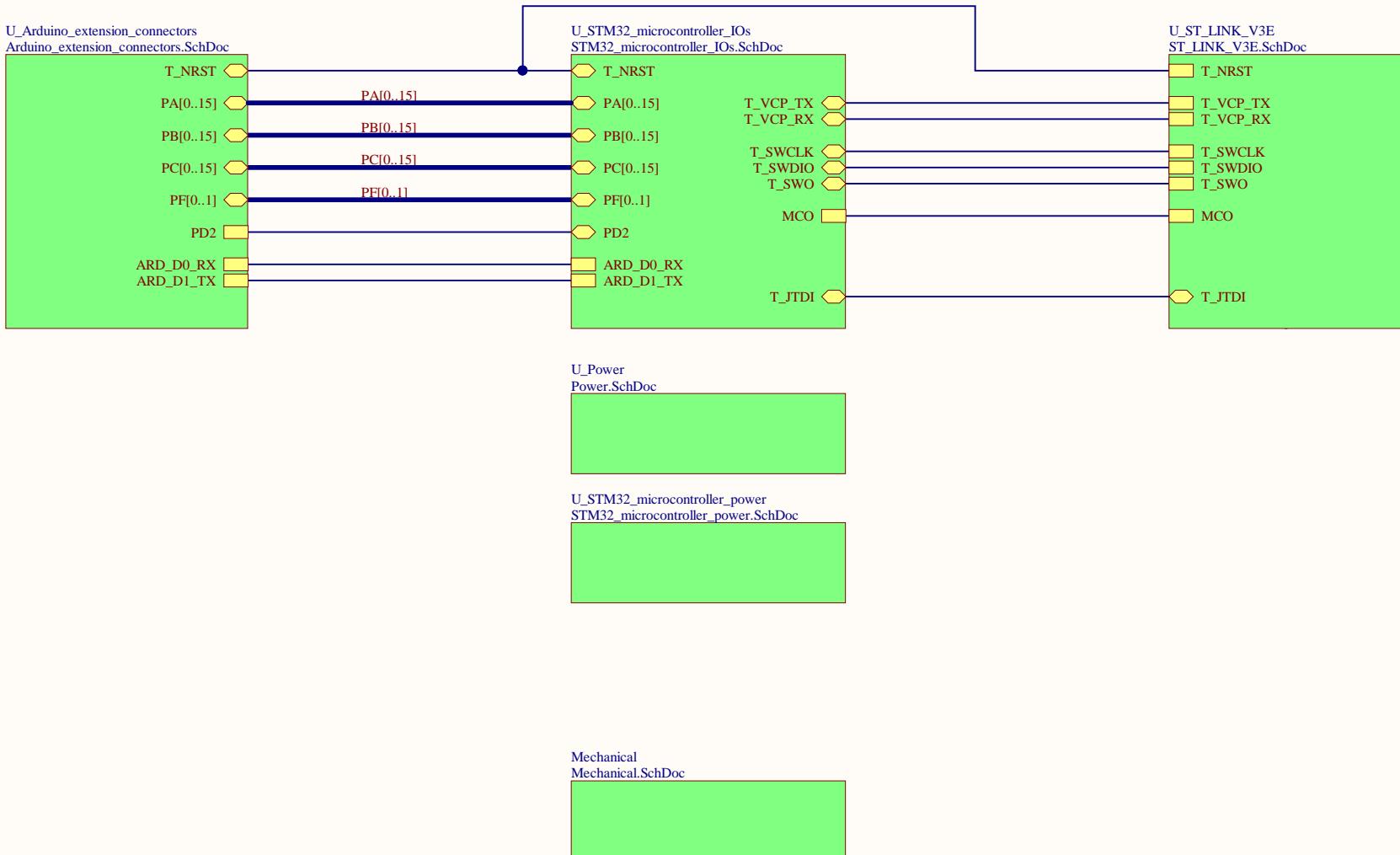
▲ Notes to generate the board layout.

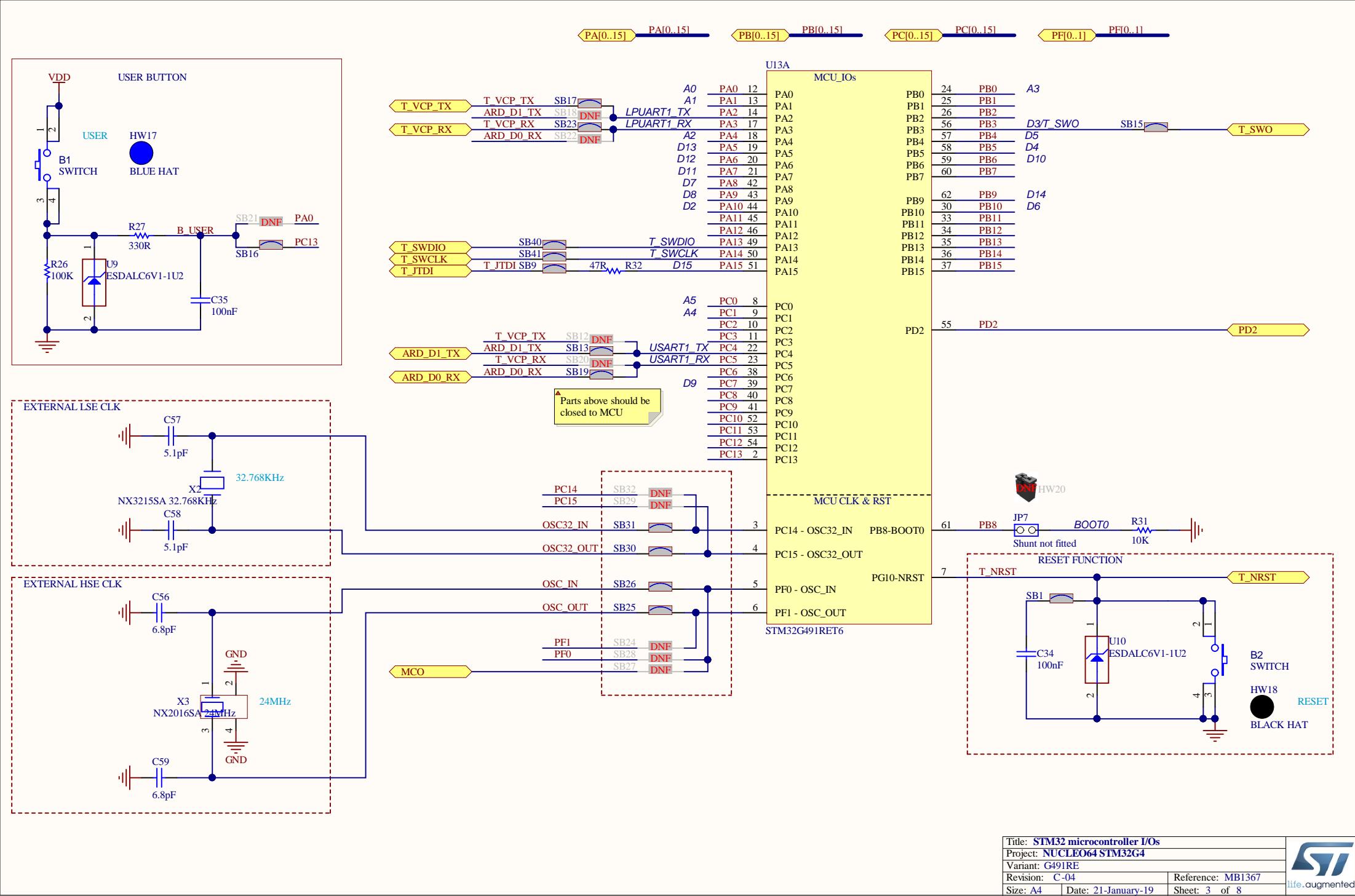
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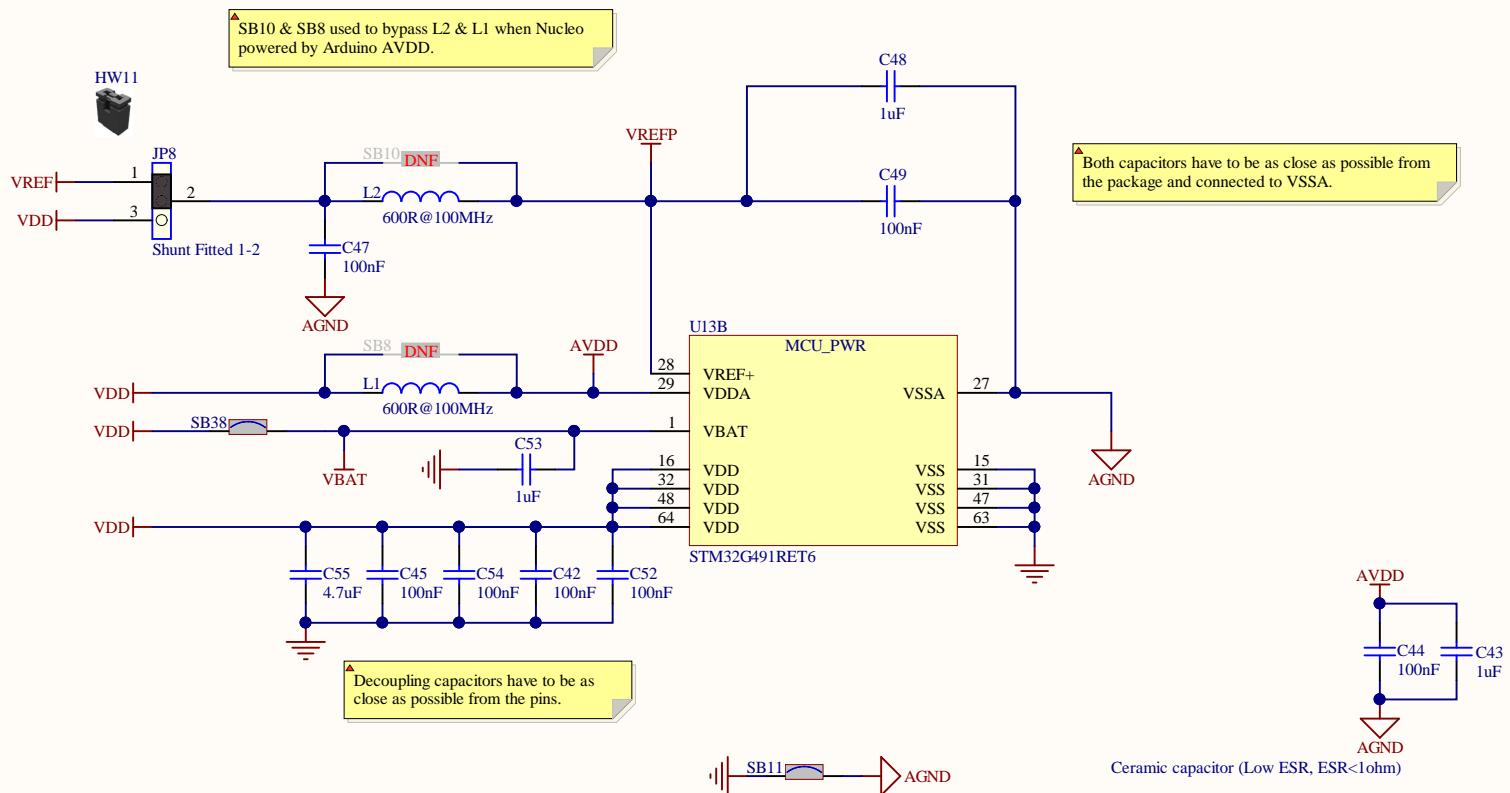
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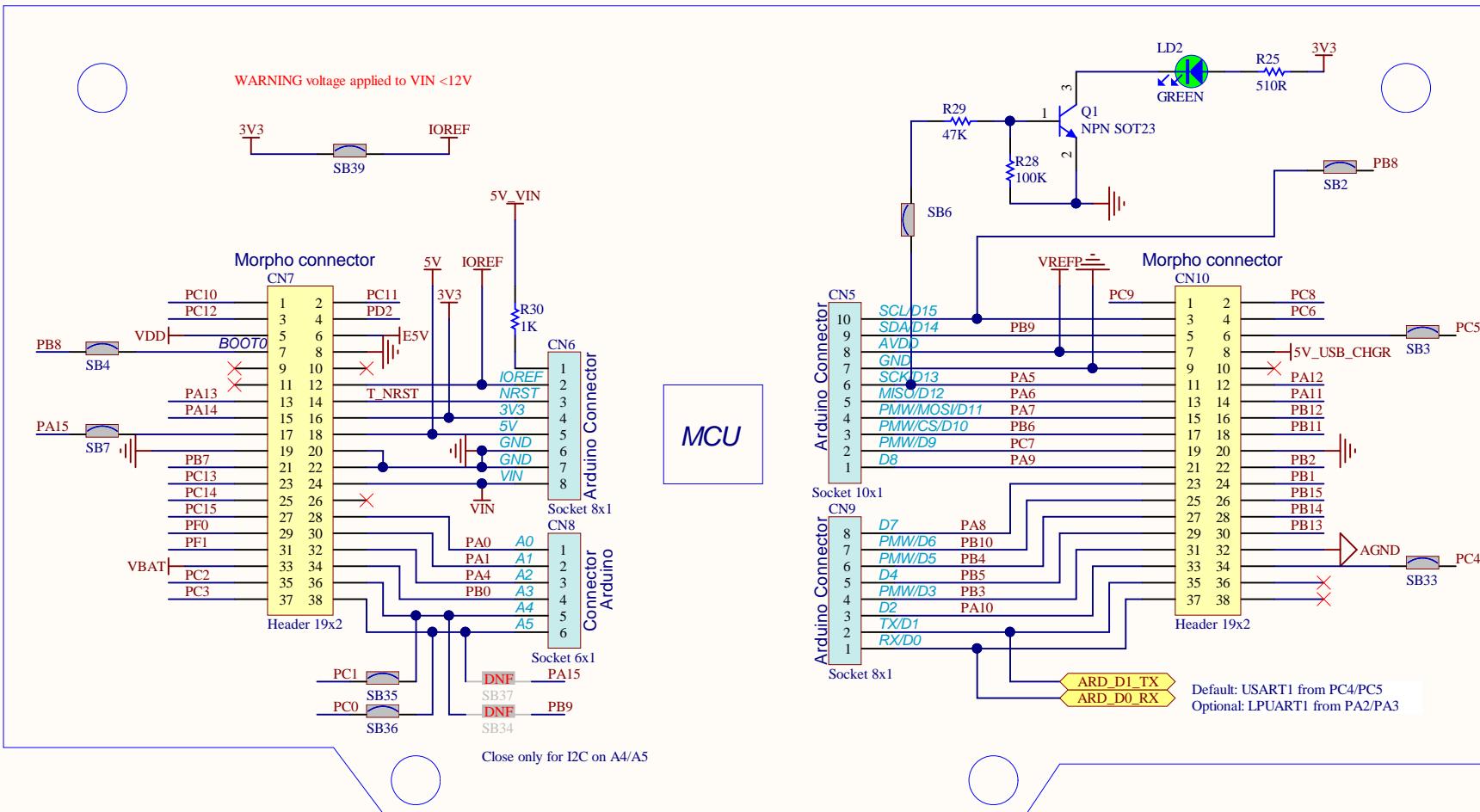
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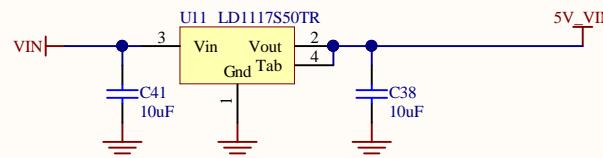




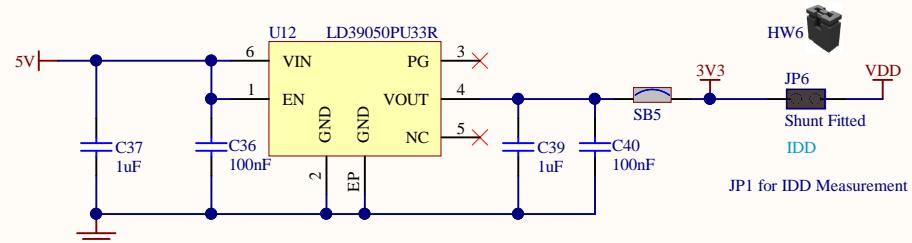
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 PB[0..15] PB[0..15]
 PC[0..15] PC[0..15]
 PD2 PD2
 T_NRST T_NRST
 PF[0..1] PF[0..1]



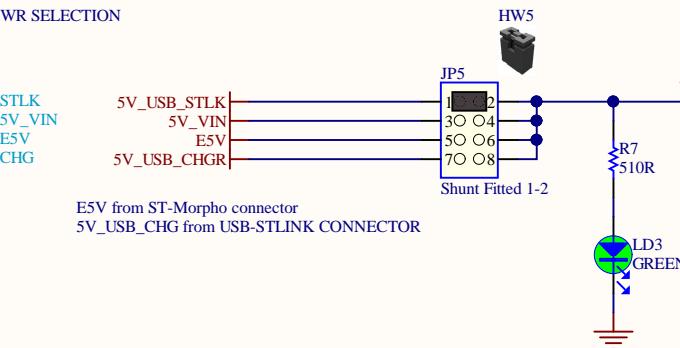
VIN / 5V PWR



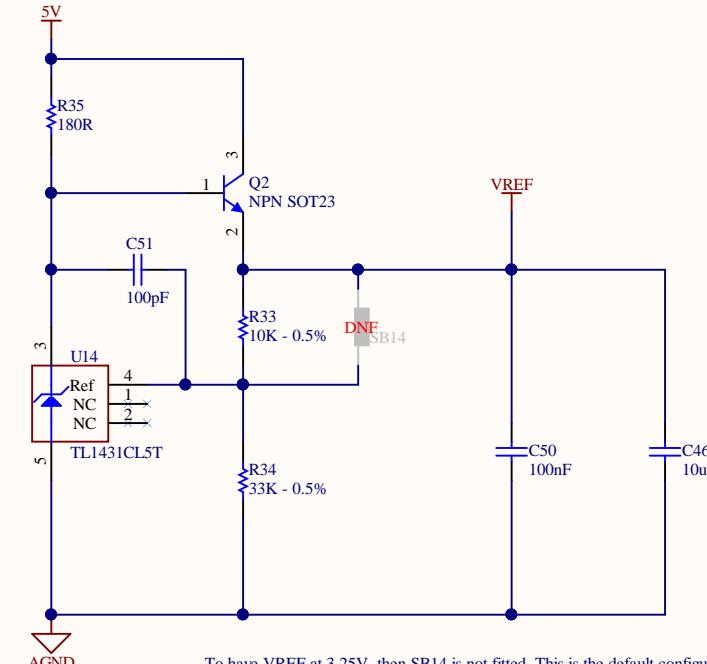
VDD PWR



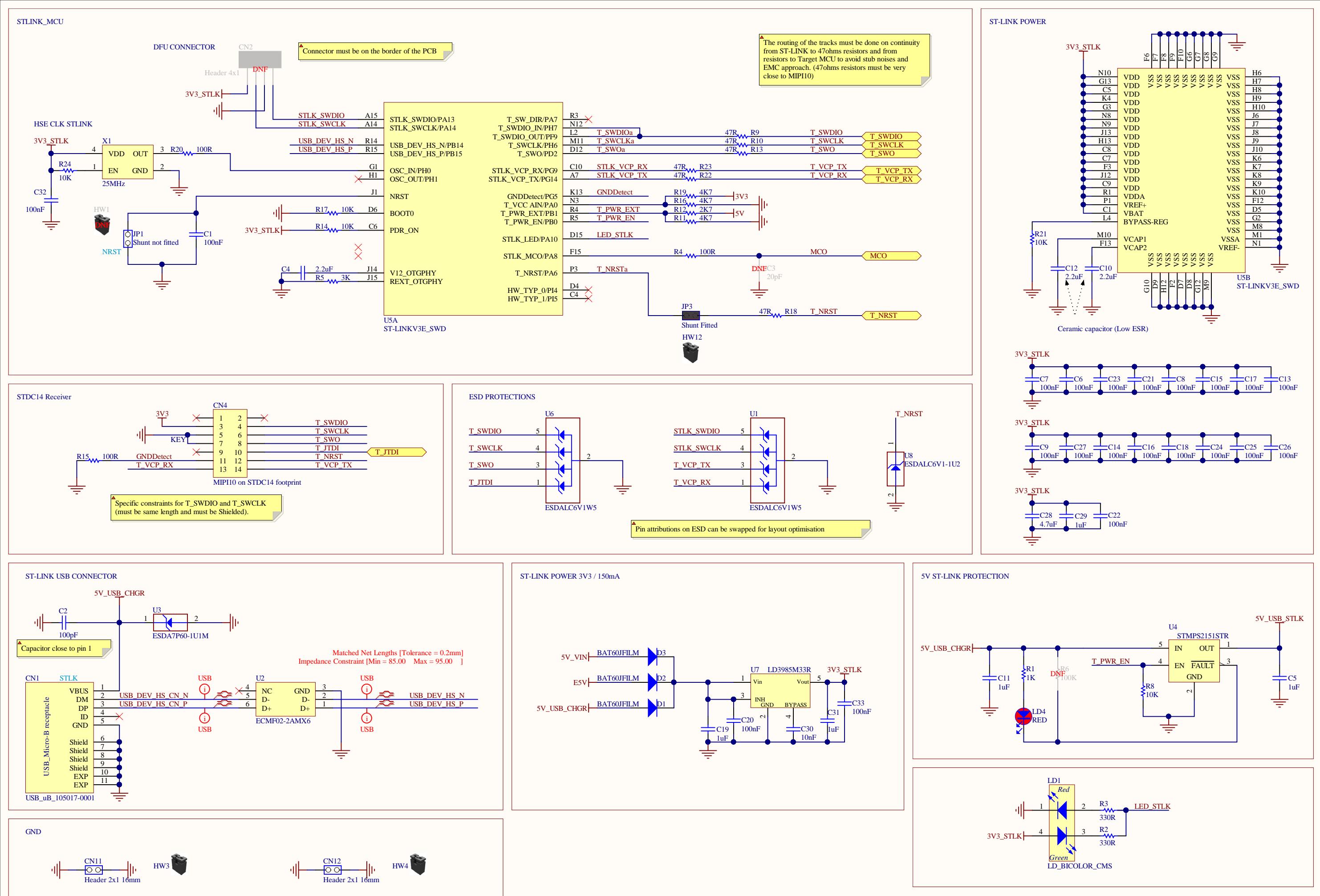
5V PWR SELECTION



VREF PWR



To have VREF at 3.25V, then SB14 is not fitted. This is the default configuration.
To have VREF at 2.5V, then SB14 is fitted and R34 is not fitted



HW101
BOARD REF
MBxxxxy-zz sywwwxxxxx

HW102
BOARD CPN
NUCLEO-G4xxRx

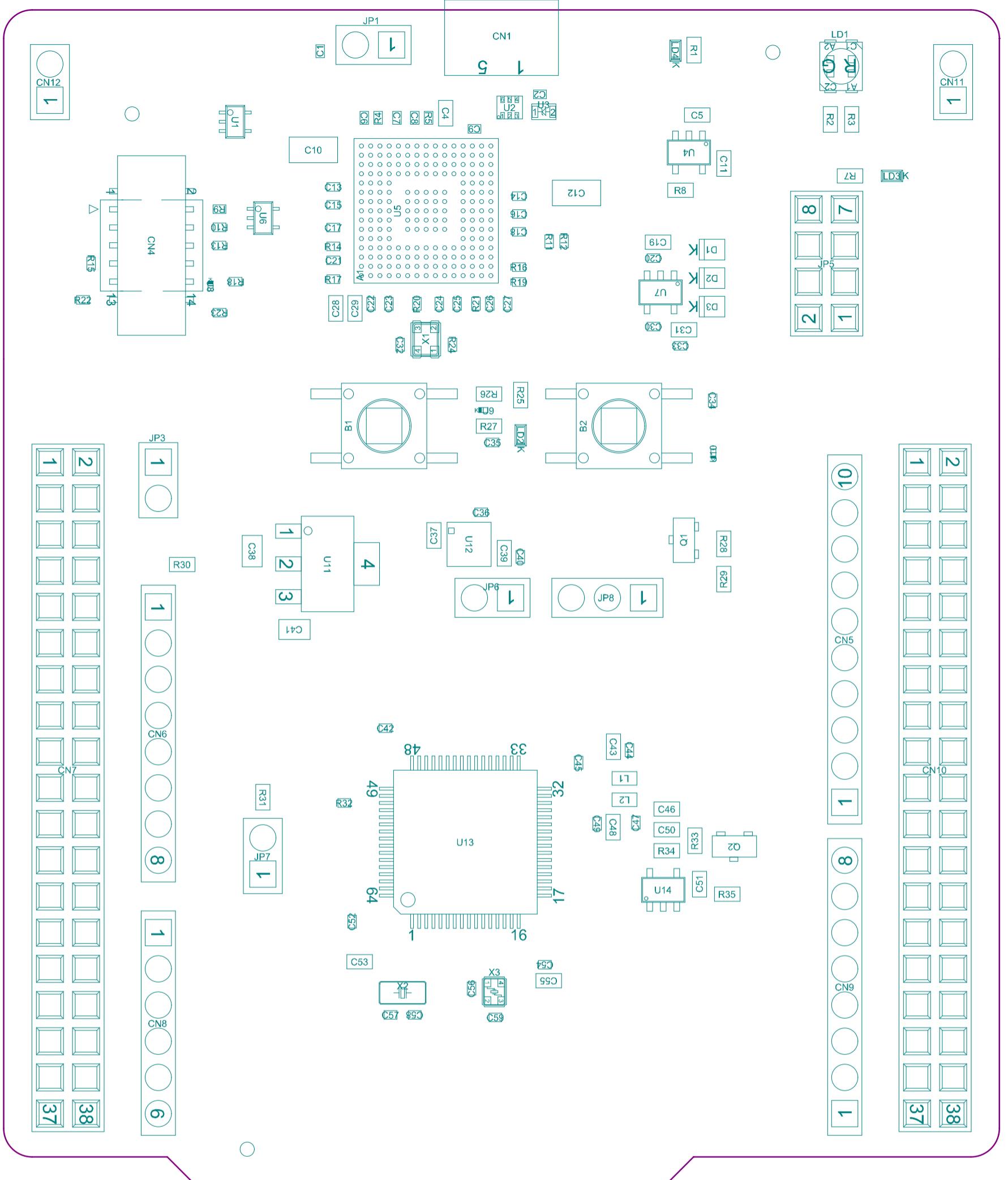
HW108
ES
ES

HW100
PCB
MBxxxxy

HW103 HW106 HW104 HW107 HW105
LOGO NUCLEO LOGO ST LOGO CE LOGO ESD LOGO ROHS

Title: Mechanical	
Project: NUCLEO64 STM32G4	
Variant: G49IRE	
Revision: C-04	
Size: A4	Date: 21-January-19
Reference: MB1367	Sheet: 8 of 8





Project: NUCLEO4 STM32G4

Layer:

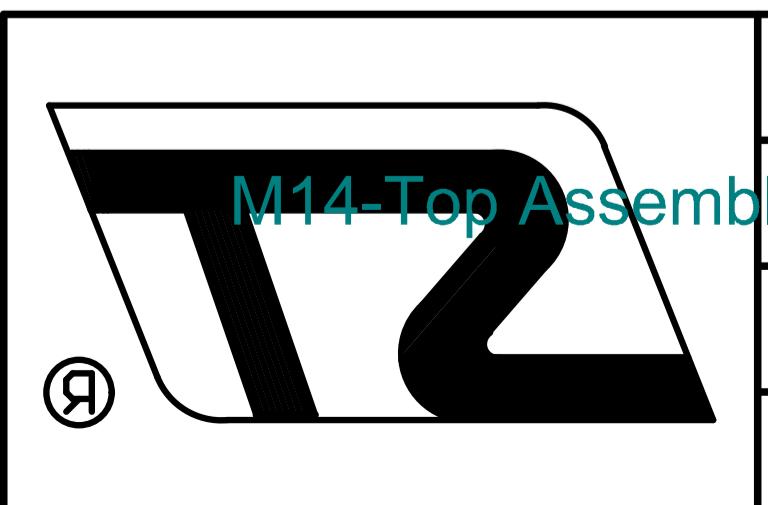
GM15.Gerber:

Ref: MB1362

Assembly: G401RE

Rev: C

M14-Top Assembly





M15-Bottom Assembly

Project: NUCLEO4 STM32G4

Gerber: GM14

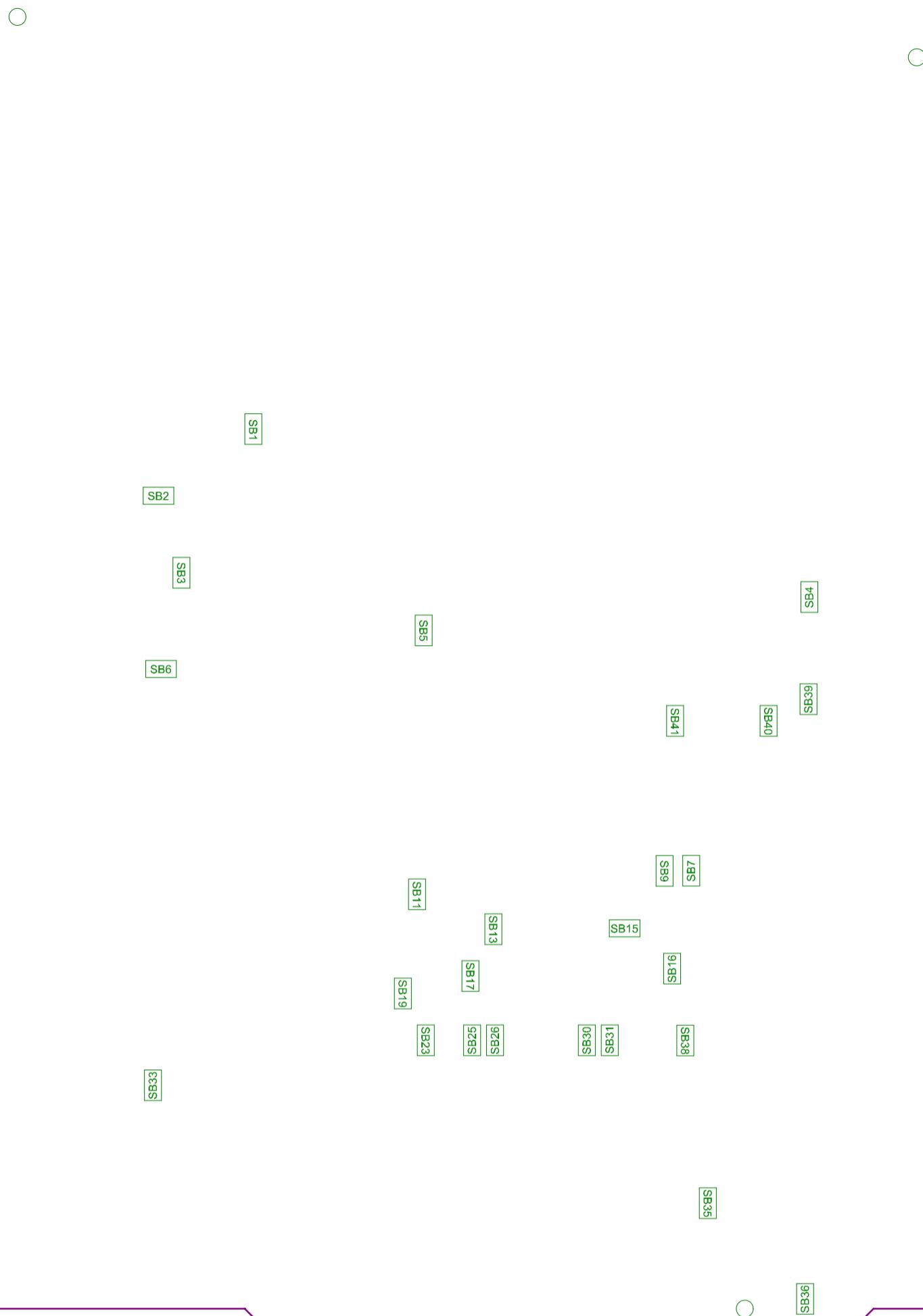
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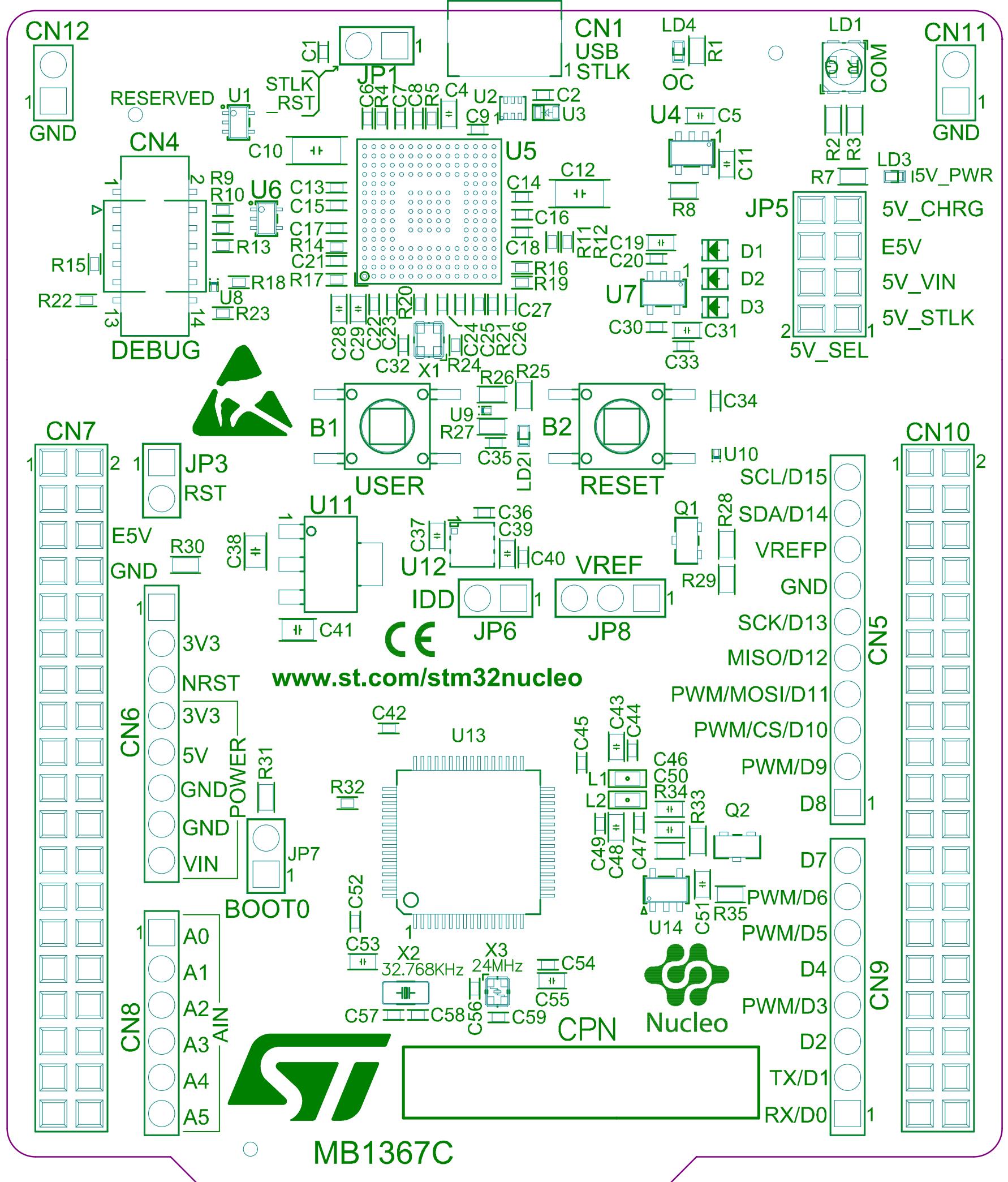
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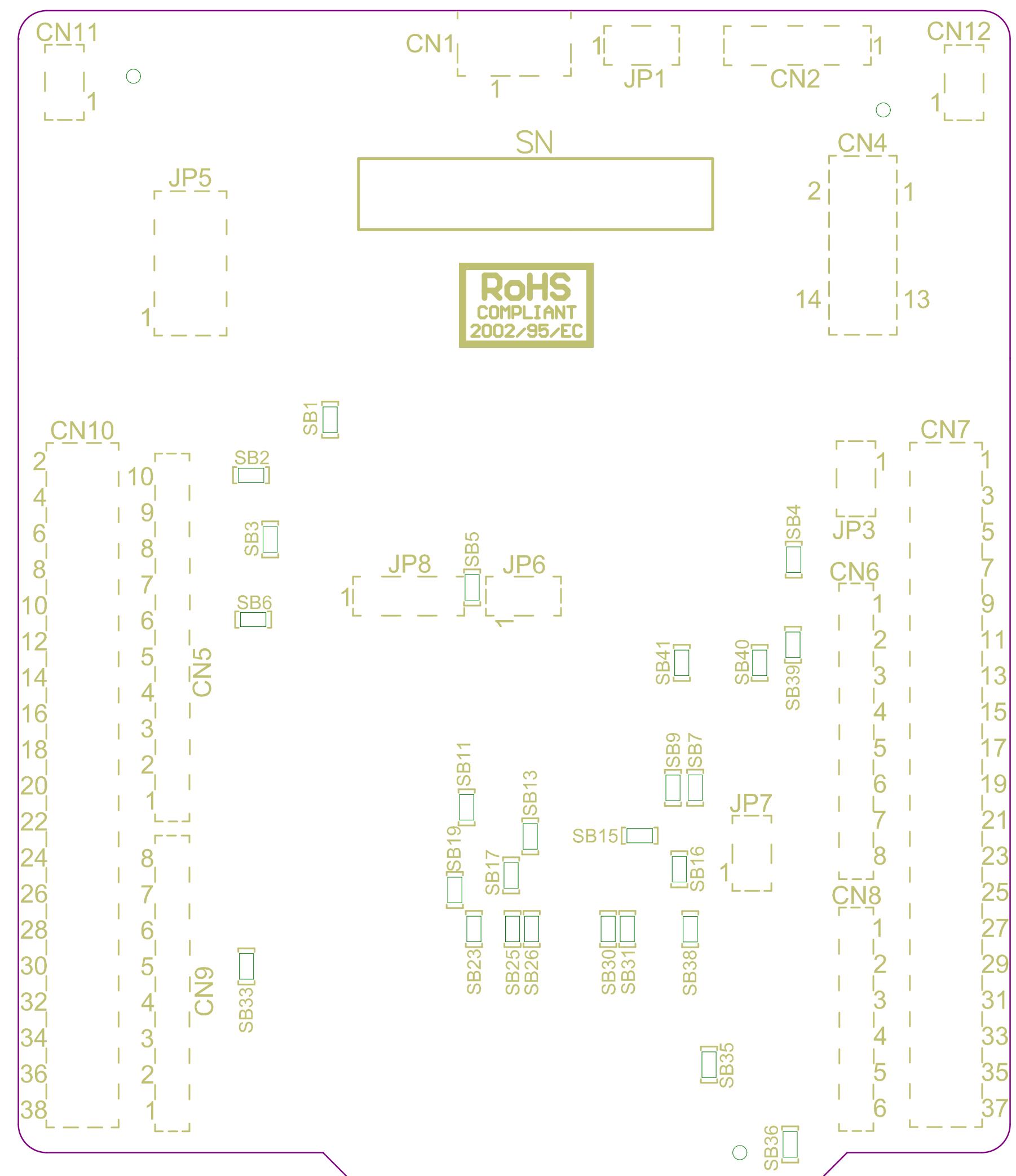
Rev: C

Variant: G41RE

Date: 21-January-18







Bottom Overlay

CART-1

.GTO